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A Type System for the Automatic Distribution of Higher-order Synchronous Dataflow Programs

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Abstract: This paper addresses the design of distributed systems with synchronous dataflow languages. As modular design entails handling both architecture and functional modularity, we propose a language-oriented solution, involving the extension of a synchronous dataflow language with primitives for program distribution. These primitives allow the programmer to describe the architecture of the system and to express where streams and expressions are located in this architecture. A distributed semantics is first proposed as formalization of the distributed execution of programs. A type and effect system is then provided in order to infer the localization of non-annotated values by means of type inference and to ensure, at compilation time, the consistency of the distribution. A type-directed projection operation allows us to obtain automatically, from a centralized typed program, the local program to be executed by each computing resource. The type system as well as the automatic distribution mechanism has been implemented in the Lucid Synchrone compiler.

Key-words: Synchronous programming, automatic distribution, type systems

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Un système de types pour la répartition automatique de programmes flot de données synchrones d'ordre supérieur

Résumé : Ce rapport traite de la conception de systèmes répartis au moyen de langages flot de données synchrones. La conception modulaire de tels systèmes comprend deux préoccupations différentes, la modularité fonctionnelle et la modularité d'architecture. Ces deux préoccupations pouvant s'avérer orthogonales, nous proposons une solution intégrée dans un langage de programmation, par son extension avec des primitives de répartition. Ces primitives permettent de programmer un système réparti entier avec un seul et même langage, ainsi que la description de l'architecture et l'expression de la localisation de certains flots ou calculs sur cette architecture. Une sémantique répartie est dans un premier temps proposée comme formalisation de l'exécution répartie de programmes synchrones. Ensuite, nous proposons un système de types à effets permettant d'inférer la localisation des calculs ou des flots non annotées du programme, ainsi que d'assurer à la compilation la consistance des annotations de répartition fournies par le programmeur. Finalement, une opération de projection dirigée par les types est définie. Cette opération permet d'obtenir automatiquement, à partir d'un seul programme synchrone annoté, un programme local destiné à être exécuté par chaque ressource de calcul déclarée dans l'architecture. Ce système de type, ainsi que l'opération de projection, ont été implémentées dans le compilateur du langage Lucid Synchrone.

Mots-clés : Programmation synchrones, répartition automatique, systèmes de type

1 Introduction

Synchronous programming languages [6] are frequently used in the industry for the design of real-time embedded systems. Such languages define deterministic behaviors and lie on formal semantics, making them suitable for the design and implementation of safety critical systems. They are used, for example, in critical domains such as automotive, avionics, or nuclear industry.

Most of the systems designed with synchronous languages are centralized systems. The parallelism expressed in these languages is a *functional* one whose purpose is to ease the design process by providing ideal timing and concurrency constructs to the designers. A synchronous program is then compiled into a sequential program emulating the parallel execution of the functional parallel branches. This sequential program is executed on a single computing resource. However, most embedded systems are composed of several computing resources (named “locations”). There are many reasons such as performance, dedicated actuators or sensors drivers, or adaptivity of the locations to the tasks they are assigned to (e.g., pure computing tasks vs control tasks). We call this the *execution*, or *physical* parallelism. This paper addresses the problem of mapping the functional parallelism onto the physical one, in a modular way.

Programs of a distributed system can be designed separately; but it occurs that in complex and multifunctional embedded systems, functionalities are frequently independent of the hardware architecture, implying conflicts between architecture and functional modularity. Thus, one functionality can use several locations and one location can be involved in several functionalities. As a result, programming separately each location compromises the modularity and is error-prone. This situation occurs within several industrial areas, among which automotive embedded systems, and software-defined radio [16].

Within this context, our motivation is to introduce distribution primitives in a synchronous dataflow language allowing the annotation of a synchronous program without altering its semantics. These annotations allow the programmer to state on which location some values are located or computed. We then use a type and effects system [19], named *spacial* type system hereafter, to infer modularly the localization of non-annotated values. Finally, a projection operation allows us to obtain automatically, from a complete typed program, the local program to be executed on each location.

Our paper is organized as follows: in Section 2, we expose the context and motivations of this work. The primitives added to our language will be presented in Section 2.2, the spacial type system in Section 2.3, some examples in Section 2.4,

and an application in Section 2.5. Section 3 will then present the semantics, and the formalization of the spacial type system. Section 4 shows the type-based distribution method. Finally, related work and discussion about the solution will be exposed in Section 5.

2 Motivations and Overview

2.1 Distribution of Synchronous Programs

Dataflow languages, commonly used to describe block-diagram systems, express the manipulation of infinite streams of values as primitive values. Consequently, the notation 1 represents the infinite sequence $1, 1, \dots$ (in the following, we will denote by x_i the i th value of the stream x). In the same way, `int` stands for the type of infinite sequences of integers. In this context, functions (called nodes hereafter) are stream functions (e.g., `int` \rightarrow `int` stands for the type of functions from integer streams to integer streams). Combinatorial functions are implicitly lifted to apply pointwise to their arguments (e.g., if $\mathbf{x} = (x_i)_{i \in \mathbb{N}}$ and $\mathbf{y} = (y_i)_{i \in \mathbb{N}}$ are two integer streams, $(\mathbf{x} + \mathbf{y}) = (x_i + y_i)_{i \in \mathbb{N}}$).

The classical compilation method involves compiling a synchronous program into one function f , which computes the values of outputs, and updates the system's state, from the values of inputs and the current state. This function f is then embedded inside a periodic execution loop [3]. We will adapt this classical compilation scheme to a distributed framework: the result of the compilation of a distributed system will consist of n functions f_i , one for each location i , which will compute the values of outputs, communication channels, and local state, from the values of inputs, other incoming communication channels, and the current local state.

2.2 Language-based Distribution

We consider *functional* distribution: distribution is not achieved for the sake of performance but because the system described is intrinsically distributed. The distribution is driven by the fact that some functions have a meaning only at some specific locations and not at others. We can think, e.g., of a function returning the value of a physical sensor and which has to be executed where the sensor is. Therefore, locations will be defined by the functionalities they provide.

Designing such distributed systems is non-trivial, since problems such as the scheduling of communications or the type consistency of the communicated data arise. The usual method, using architecture languages such as AADL [2], involves

describing the system's architecture by partitioning it in subsystems. Each subsystem can then be defined separately, possibly with different languages.

However, in the case of tightly dependent subsystems, where some conflicts between architectural and functional modularity can occur, it is more efficient and less error-prone to define the system as a whole, together with architectural annotations [12]. Our contribution consequently involves providing language primitives, to allow the programmer to describe the hardware architecture, and to express where some values are located, i.e., on which location some computations are performed.

The architecture is described by the explicit declaration of the set of existing locations and the links between them. At this point, locations are symbolic: the declaration of a location introduces a symbolic name, which will then be used to express the fact that a stream is computed or available at this symbolic location. We define in Section 4.4 a *projection* operation, which operates w.r.t. a symbolic name, and which produces a single non-distributed synchronous program to be executed at the physical location represented by this symbolic name.

The declaration of a physical location A follows the syntax: `loc A`. The existence of a link from A to B is stated by: `link A to B`. Note that we distinguish *communication links* from *communication channels*, introduced in Section 3.4: communication links, specified by the primitive `link`, state the *possibility* of communications from one location to another. In contrast, actual *channels* used by the distributed system are inferred by the type system.

The statement `e at A` means that every value used in the expression e (streams and nodes) will be located at A . The programmer does not need to express the localization of every value. A type system is provided, whose double function is to check the validity of the localization expressed in regard to the architecture (w.r.t. existing communication links), and to infer the localization of non-explicitly located values.

For instance, the node `f` given below consists of two computations `g` and `h`, respectively located by the programmer on locations A and B , thanks to the `at A` and `at B` annotations.

```
node f(x) = z with
  y = g(x) at A
  and z = h(y) at B;
```

Communications are abstracted, and thus not expressed by the programmer, neither technically, nor concerning their localization inside the code. The technical expression of communications are let to the further phase of integration on actual

architecture: our method only deal with inferring localization of these communications, and their coherence throughout the distributed code. We assume for now that communications can occur at any localization, and can concern any value entirely concealed within a location (i.e., not the distributed data structures, like distributed pairs). Such choice concern a compromise, from a programmer point of view, between no control at all (communications are possible everywhere) and absolute control (the programmer expresses every communication). We will currently assume that communications can occur everywhere in the code, and show later how this can be refined.

2.3 A Type System for the Automatic Distribution

The main difficulty of synchronous dataflow programs distribution lies in the preservation of the control flow. Usually, such programs are inlined before distribution [9]: as a result, the control flow is materialized by classic control structures such as if/then/elses. We place ourselves in a functional framework, where for the sake of modularity, functions can neither be inlined nor analyzed dependently of their calling context. Hence, to preserve the coherence of the control flow on all the locations, we must place on each location, along with functions defined on it, the functions containing the calls to these functions, and so on. This also allows us to preserve higher-order features, hence allowing the expression of dynamic reconfiguration of nodes by application of other nodes as inputs.

Consequently, the complete control flow of the program cannot be built for analysing its consistency. Hence, type systems are provided so as to perform such an analysis. Besides the classical type system used for data consistency analysis, we provide a special type system dedicated to the distributed execution of the program. We call it a *spacial type system* and, when clear from context, we shall simply refer to it as a type system (since this paper does not address classical typing). This spacial type system describes the localization of streams, and a type-directed approach is followed to achieve code distribution.

The motivation for using a type system is to achieve type inference: in order not to force the programmer to specify everything (i.e., the localization of each stream), spacial types will be inferred from the available spacial annotations in the source. The spacial type system also checks the consistency of these annotations with the given architecture. Spacial consistency means, e.g., that applying a node located on a location to a stream located elsewhere is not correct. As we are in a functional context, spacial types will be inferred for each defined node modularly. The type system presented is a type and effects system [19].

A typed program is then automatically distributed by the compiler, by extracting, for each declared location, one program strictly composed of computations to be performed on this location, as well as added communications from and to other locations in the form of added inputs and outputs.

The spacial type of a stream is the location where this stream is located. In the case of a stream whose values are communicated via a channel from one location to another, its spacial type is a set: it is the set of locations where the stream will be available. The spacial type of a node f is written $t_i \dashv\langle S \rangle \rightarrow t_o$, where t_i and t_o are respectively the spacial types of f 's inputs and outputs, and S is the set of locations involved in the computation of f . This set of locations can be larger than the union of t_i and t_o 's sets of locations, since the computation of f can involve intermediary locations.

2.4 Examples

All the examples below assume the architecture declaration:

```
loc A;
loc B;
link A to B;
```

The first example comprises a conditional control structure, allowing a node $f(x)$ to be computed, either at A, or at B, according to the value of its input m . We assume that f is of spacial type $\forall \delta. c \text{ at } \delta \dashv\langle \{\delta\} \rangle \rightarrow c \text{ at } \delta$.

```
node g(m,x) = z with
  if m then
    let y = f(x) at A in z = y
  else
    let y = f(x) at B in z = y
```

The values defined in the two branches of the `if` have to be of the same type, i.e., at the same location. Furthermore, the `if/then/else` structure has to be duplicated on A and B, so as to be able to decide, at both locations, whether to apply f or not. Both values m and x must also be available on both locations. As the only existing link in the architecture is from A to B, the only way to resolve such constraints is to locate m and x at A, and the node output at B. The spacial type of the node g is therefore:

$$c \text{ at } A \times c \text{ at } A \dashv\langle \{A, B\} \rangle \rightarrow c \text{ at } B$$

It can be noted here that this node cannot be used within a located declaration. The program below will be rejected by our type system.

```
node g'(m,x) = y with
  y = g(m,x) at A
```

The second example is a sequence of three nodes **f1**, **f2** and **f3**, each assumed to be of spacial type $\forall \delta. c \text{ at } \delta \multimap \{\delta\} \rightarrow c \text{ at } \delta$. **f1** and **f3** are localized by the programmer, respectively on A and B. **f2** is not explicitly localized.

```
node g(x) = y3 with
  y1 = f1(x) at A
  and y2 = f2(y1)
  and y3 = f3(y2) at B
```

This node will be given the spacial type $c \text{ at } A \multimap \{A, B\} \rightarrow c \text{ at } B$. As the localization of computations has to be done modularly, a spacial type for **f2** will be given once, among the two possibilities $c \text{ at } A \multimap \{A\} \rightarrow c \text{ at } A$ and $c \text{ at } B \multimap \{B\} \rightarrow c \text{ at } B$.

In contrast, since there is no communication link from B to A, the following node will be rejected by the type system:

```
node g(x) = y3 with
  y1 = f1(x) at B
  and y2 = f2(y1)
  and y3 = f3(y2) at A
```

The fourth example involves a higher-order node: the node **h** takes as input two nodes **f** and **g**, and an input **x**, and applies **f** to **x** at the location A, and then **g** to the result of the first application at the location B.

```
node h (f,g,x) = z with
  y = f(x) at A
  and z = g(y) at B
```

The spacial type of **h** is then:

$$\forall \alpha, \beta, \gamma. \left(\begin{array}{l} (\alpha \text{ at } A \multimap \{A\} \rightarrow \beta \text{ at } A) \\ \times (\beta \text{ at } B \multimap \{B\} \rightarrow \gamma \text{ at } B) \\ \times (\alpha \text{ at } A) \end{array} \right) \multimap \{A, B\} \rightarrow \gamma \text{ at } B$$

The fifth example is the same higher-order node, but without any annotations:

node h (f, g, x) = z with
 $y = f(x)$
 and $z = g(y)$

The spacial type of h is then:

$$\forall \alpha, \beta, \gamma. \forall \delta. \left(\begin{array}{l} (\alpha \text{ at } \delta \rightarrow \{\delta\} \rightarrow \beta \text{ at } \delta) \\ \times (\beta \text{ at } \delta \rightarrow \{\delta\} \rightarrow \gamma \text{ at } \delta) \\ \times (\alpha \text{ at } \delta) \end{array} \right) \rightarrow \{\delta\} \rightarrow \gamma \text{ at } \delta$$

As each node is projected to specialized code for each symbolic location, when no annotation is provided, we must enforce each instance of the node to be executed at one single location; this location can be any symbolic location, and is also generalized.

Finally, a node, for more modularity, can be defined with local locations. These new locations are introduced as a list between $[..]$, can then be used within the node. This higher-order node uses two location variables δ_1 and δ_2 :

node h [δ_1, δ_2] (f, g, x) = z with
 $y = f(x)$ at δ_1
 and $z = g(y)$ at δ_2

h receives then the spacial type:

$$\forall \alpha, \beta, \gamma. \forall \delta_1, \delta_2 : \{\delta_1 \triangleright \delta_2\}. \left(\begin{array}{l} (\alpha \text{ at } \delta_1 \rightarrow \{\delta_1\} \rightarrow \beta \text{ at } \delta_1) \\ \times (\beta \text{ at } \delta_2 \rightarrow \{\delta_2\} \rightarrow \gamma \text{ at } \delta_2) \\ \times (\alpha \text{ at } \delta_1) \end{array} \right) \rightarrow \{\delta_1, \delta_2\} \rightarrow \gamma \text{ at } \delta_2$$

The set of constraints $(\{\delta_1 \triangleright \delta_2\})$ is inferred from the links required by the node. These constraints are resolved, with the actual architecture, when this node is instantiated. A constraint $\delta \triangleright \delta'$ is resolved, either by stating $\delta = \delta' = s$, either with two locations s and s' such that there exists a communication link from s to s' in the local architecture.

Thus, the node h can be instantiated these two ways (assuming the existence of two nodes f and g , both of spacial type $\forall \delta. c \text{ at } \delta \rightarrow \{\delta\} \rightarrow c \text{ at } \delta$):

$y1 = h$ (fat A, gat A, x1)
 and $y2 = h$ (fat A, gat B, x2)

We can observe than an arrow type appearing on the left of another arrow type cannot comprise more than one location. This is caused by the form taken by the distribution: since the projection operation on one node is performed on locations, and not sets of locations, we cannot handle effect variables, as it is the case in other type and effect systems.

2.5 Application

As a concrete example, we consider the definition of a reception channel of a software radio. A *software radio*, or *software-defined radio*, is a radio in which components usually defined as hardware, e.g., demodulation or filter components, are defined as software [16]. This allows in particular the reconfiguration, possibly dynamic, of these components.

Consider a reception channel composed of three main components: a pass-band filter allowing the selection of the carrier wave, a demodulator component, and a component allowing the analysis of the received signal, e.g., an error-correction function. For the sake of performances, these components are usually implemented on different architectural elements: the pass-band filter on a FPGA, the demodulator on a digital signal processor (DSP), and the error correction on a general-purpose processor (GPP).

Each component of this reception channel could easily be defined separately. But in the case of software-defined radio, the system must support either several standards, or several functionalities [13]. In other word, there is a strong motivation for dynamicity.

Let us study the case of a multichannel reception system that supports the two mobile standards GSM and UMTS:

- The GSM standard involves a filter for 1800 MHz frequencies, a GMSK demodulator, and a CRC / convolutional error correction module.
- The UMTS standard involves a 2 GHz filter, a QPSK demodulator, and a CRC / convolutional / turbo codes error correction module.

Figure 1 shows an implementation of this reception channel on a system composed of three hardware components: a FPGA dedicated to the execution of the two pass-band filters, a DSP for the demodulation functions, and a GPP for error-correction modules and for the control of the whole system, i.e., in this case, the switch between the two channels. This system has one input \mathbf{x} , the radio signal from the antenna. \mathbf{y} denotes the output signal of the system, i.e., the decoded and corrected information received

by the channel. From this value, a function `gsm_or_ums` (noted `g` on Figure 1 for the sake of brevity), local at GPP, computes what channel will be used at next instant. In this figure, each location is graphically represented by a gray box.

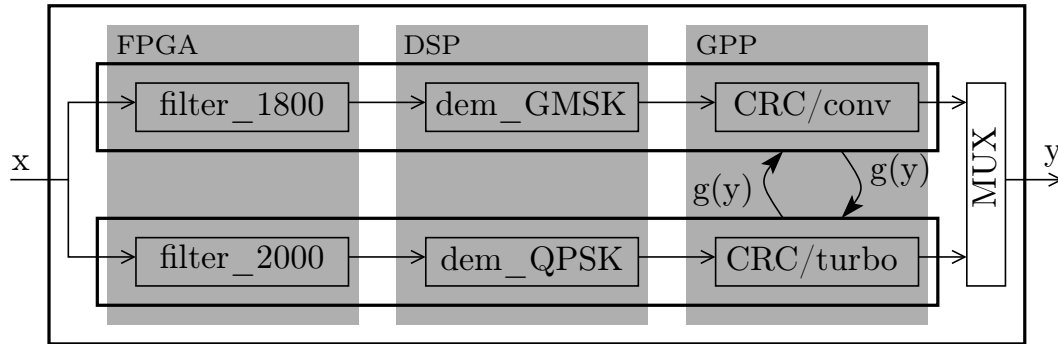


Figure 1: Functional model of a multichannel software radio.

In a classical context, designing this multichannel software radio would be performed by separately programming each of the three hardware components, which raises two problems:

1. There is no guarantee that the components interact as specified: i.e., the 1800 filter with the GMSK demodulator, and so on. This requires the MUX function to be duplicated on the three computing resources, so as to guarantee the correction of the system. This situation compromises the modularity of the system.
2. Each of the two channels corresponds to an independent software entity. Programming independently each hardware component leads to the separate design, at least from some point of the design flow, of closely related software components (e.g., filter and demodulator of the same channel).

For the sake of modularity, this system would be better designed by considering the channels independently, and not the hardware components. This situation claims for adding primitives allowing to express the localization of streams directly in the language. Such primitives should allow the programming of software components independently of the architecture, handled as a separate concern. Thus, consistency analysis such as data typing could be performed on the global program: communication channels could be typed and the data consistency of the whole system could

be checked. This way, inconsistencies due to serialisation could be detected at compilation time.

Figure 2 shows the implementation of this multichannel software radio, with the extended language introduced in preceding sections. This implementation strictly

```

node channel(filter,demod,crc,x) = y with
  f = filter(x) at FPGA
  and d = demod(f) at DSP
  and y = crc(d) at GPP

node multichannel_sdr(x) = y with
  c = gsm_or_umts(y) at GPP in
  and
  if (true fby c) then
    y = channel(filter_1800,gmsk,conv,x)
  else
    y = channel(filter_2000,qpsk,turbo,x)

```

Figure 2: Multichannel software radio implementation

follows the architecture of the system described in Figure 1. It shows the declaration of three symbolic locations (FPGA, DSP, and GPP). We assume that all filter, demodulation, and correction functions are local ones, i.e., they are of spacial type $\forall \delta. c \text{ at } \delta \dashv\!\!\dashv\!\!\rightarrow c \text{ at } \delta$. Since the conditional construct comprises declarations that have to be executed on the set of locations $\{\text{FPGA}, \text{DSP}, \text{GPP}\}$, c is thus inferred to be communicated to these locations. The conditional `if/then/else` is evaluated with the value of c at the previous instant (the definition of `fby` is: $(\text{xfby})_i = x_0$ if $i = 0$, y_{i-1} else). The distribution of this example will put a copy of this `if/then/else` on these three locations. Finally, the expression `true fby c` will be computed at GPP, since the result of this expression has to be communicated to the three locations where the conditional construct will be duplicated.

By the same reasoning, we can infer that the spacial type of x is FPGA, and the one of y is GPP. As a result, the spacial type of the node `multichannel_sdr` is:

$$(c \text{ at FPGA}) \dashv\!\!\dashv\!\!\rightarrow (\{ \text{FPGA}, \text{DSP}, \text{GPP} \}) \rightarrow (c \text{ at GPP})$$

3 Formalization

This section is organized as follows: we first define a synchronous dataflow core language in Section 3.1 and give it its centralized semantics (Section 3.2) and its distributed semantics (Section 3.3). The centralized semantics is considered to be the reference semantics and we only consider programs which reacts with respect to this semantics. Programs that do not react (e.g., for typing or causality reasons) are assumed to be rejected by other means [10, 11]. The distributed semantics allows us to give a meaning to location annotations. A spacial type system is then presented in Section 3.4. It is used to both reject programs which cannot be distributed and to annotate every expression from the source code with explicit locations. These annotations are then used by a type-directed projection operation which produces a program for each location (Section 4.4).

3.1 The Core Language Syntax

A program is made of an architecture description (\mathcal{A}), a sequence of node definitions (d) and a main set of equations (D).

An architecture description is a set of declarations of locations (`loc A`) or links (`link A to A`) which state the existence of a communication link from one location to another. A location s is either a location variable δ , or a location constant A .

A node definition is composed of an expression and a set of equations. A set of local locations $\{\delta_1, \dots, \delta_n\}$ can be associated as location parameters of a node definition (`node $f[\delta_1, \dots, \delta_n](x) = e$ with D`).

Definitions D are either single equations ($x = e$), definitions naming the result of an application ($x = x(e)$), parallel declarations (`D and D`), or alternative declarations (`if e then D else D`).

An expression e may be an immediate value (i), a variable (x), a pair construction ((e, e)), a binary combinatory operation (`op(e, e)`, where `op` can be `(+)`, `(-)`, `(...)`), an initialized delay (`e fby e`), an access function (`fst e and snd e`), or an expression annotated with an explicit location s (`e at s`).

$$\begin{aligned}
P &::= \mathcal{A};d;D \\
\mathcal{A} &::= \mathcal{A};\mathcal{A} \mid \text{loc } A \mid \text{link } A \text{ to } A \\
s &::= \delta \mid A \\
d &::= \text{node } f[\delta_1, \dots, \delta_n](x) = e \text{ with } D \mid d;d \\
D &::= x = e \mid x = x(e) \mid D \text{ and } D \\
&\quad \mid \text{if } e \text{ then } D \text{ else } D \\
e &::= i \mid x \mid (e, e) \mid \text{op}(e, e) \mid e \text{ fby } e \mid \text{fst } e \mid \text{snd } e \mid e \text{ at } s
\end{aligned}$$

3.2 The Centralized Synchronous Semantics

The purpose of the centralized semantics is to serve as a reference semantics. This semantics does not take into account distribution primitives. We first introduce auxiliary definitions. A value is either an immediate constant (i), a pair or a function. A sequence of location parameters $\vec{\delta}$ is associated to functions.

$$\begin{aligned} v &::= i \mid (v, v) \mid \delta \lambda x. e \text{ with } D \\ R &::= [v_1/x_1, \dots, v_n/x_n] \end{aligned}$$

An environment R associates values to names and assumes that names are pairwise distinct (for all $i \neq j$, $x_i \neq x_j$).

Given a sequence d of node definitions **node** $f_i[\vec{\delta}_i](x_i) = e_i$ **with** D_i , an initial global environment R_d is defined, holding λ -values of each f_i . This initial environment will be given as input of the main program.

$$R_d = [\lambda x_1. e_1 \text{ with } D_1/f_1, \dots, \lambda x_n. e_n \text{ with } D_n/f_n]$$

The synchronous centralized semantics is defined by mean of two reaction predicates. $R \vdash e_1 \xrightarrow{v} e_2$ states that in the reaction environment R , the expression e_1 emits the value v and rewrites into the new expression e_2 . The predicate $R \vdash D_1 \xrightarrow{R'} D_2$ states that in the reaction environment R , the declaration D_1 defines the reaction environment R' and rewrites into D_2 . The centralized execution of a program P is denoted $S_{in} \vdash P : S_{out}$, meaning that under a sequence of input environments $S_{in} = R_1.R_2 \dots$, the program $P = \mathcal{A};d;D$ produces a sequence of output environments $S_{out} = R'_1.R'_2 \dots$ such that (denoting $\text{hd}(R.S) = R$ and $\text{tl}(R.S) = S$):

$$\frac{R_d, \text{hd}(S_{in}), \text{hd}(S_{out}) \vdash D \xrightarrow{\text{hd}(S_{out})} D' \quad \text{tl}(S_{in}) \vdash \mathcal{A};d;D' : \text{tl}(S_{out})}{S_{in} \vdash \mathcal{A};d;D : S_{out}}$$

The rules for the reaction predicates are given in Figure 3. An immediate value emits itself and rewrites to itself (rule IMM). A variable emits its current value as it is present in the reaction environment (rule INST). An initialized delay e_1 **fb** e_2 emits the first value of e_1 , then the previous value of e_2 (rule FBY). An operation is performed pointwisely on immediate values (rule OP). Pair construction and destruction follow classical rules (rules PAIR, FST and SND). Note that locations are not taken into account here (rule AT): annotations added by the programmer does not alter the centralized semantics of the program (i.e., its functionality). An

equation $x = e$ emits the reaction environment defining x (rule DEF). A sequential function application is replaced by its body and argument definition (rule APP). The rule AND states that parallel equations are mutually recursive. The rule LET stands for sequential definitions. A conditional statement executes its first branch if its condition is true (rule IF-1) and its second branch otherwise (rule IF-2)

3.3 The Distributed Synchronous Semantics

The distributed semantics also operates on a program $P = \mathcal{A};d;D$, but takes into account the architecture description and the explicit locations. However, it remains a synchronous semantics in the sense that the desynchronization due to the insertion of communications is not accounted for. It defines a *spacialized execution*: the values \hat{v} emitted by expressions are now *distributed values*, i.e., they are annotated with location informations stating how these values are distributed on the architecture.

$$\begin{aligned}\hat{v} &::= dv \text{ at } s \\ dv &::= i \mid (\hat{v}_1, \hat{v}_2) \mid \Lambda \vec{\delta}. \lambda x. e \text{ with } D \\ \hat{R} &::= [\hat{v}_1/x_1, \dots, \hat{v}_n/x_n] \\ s &::= A \mid \top \\ G &::= \langle \mathcal{S}, \mathcal{L} \rangle\end{aligned}$$

A location s stands either for a single location A or a special value \top abstracting a set of locations. The following definition of \sqcup holds:

$$s \sqcup s = s \qquad \top \sqcup s = s \sqcup \top = \top$$

From this definition of distributed values, we can observe that only a subset of such values are meaningful: for distributed pairs, the values $(dv \text{ at } A, dv' \text{ at } A) \text{ at } A$ (i.e., a centralized pair) and $(dv \text{ at } A, dv' \text{ at } B) \text{ at } \top$ (a distributed pair) are meaningful, whereas no meaning can be associated to the distributed value $(dv \text{ at } A, dv' \text{ at } B) \text{ at } A$. The meaningful values are defined as *well-formed values*: a distributed value \hat{v} is *well-formed* iff:

- either $\hat{v} = i \text{ at } A$ or $\hat{v} = \lambda x. e \text{ with } D \text{ at } A$;
- or, $\hat{v} = (dv_1 \text{ at } s_1, dv_2 \text{ at } s_2) \text{ at } s$, where $(dv_i \text{ at } s_i)_{i=1,2}$ are well-formed, and $s = s_1 \sqcup s_2$.

The operator $\text{loc}(\cdot)$ gathers the set of locations from a distributed value:

$$\begin{aligned}\text{loc}(i \text{ at } s) &= \{s\} \\ \text{loc}(\lambda x. e \text{ with } D \text{ at } s) &= \{s\} \\ \text{loc}((\hat{v}_1, \hat{v}_2) \text{ at } s) &= \text{loc}(\hat{v}_1) \cup \text{loc}(\hat{v}_2)\end{aligned}$$

$$\begin{array}{c}
\text{(IMM)} \\
R \vdash i \xrightarrow{i} i \\
\\
\text{(INST)} \\
R, [v/x] \vdash x \xrightarrow{v} x \\
\\
\text{(FBY)} \\
\frac{R \vdash e_1 \xrightarrow{v_1} e'_1 \quad R \vdash e_2 \xrightarrow{v_2} e'_2}{R \vdash e_1 \text{ fby } e_2 \xrightarrow{v_1, v_2} e'_1 \text{ fby } e'_2} \\
\\
\text{(OP)} \\
\frac{R \vdash e_1 \xrightarrow{i_1} e'_1 \quad R \vdash e_2 \xrightarrow{i_2} e'_2 \quad i = \text{op}(i_1, i_2)}{R \vdash \text{op}(e_1, e_2) \xrightarrow{i} \text{op}(e'_1, e'_2)} \\
\\
\text{(PAIR)} \\
\frac{R \vdash e_1 \xrightarrow{v_1} e'_1 \quad R \vdash e_2 \xrightarrow{v_2} e'_2}{R \vdash (e_1, e_2) \xrightarrow{(v_1, v_2)} (e'_1, e'_2)} \\
\\
\text{(FST)} \\
\frac{R \vdash e \xrightarrow{(v_1, v_2)} e'}{R \vdash \text{fst } e \xrightarrow{v_1} \text{fst } e'} \\
\\
\text{(SND)} \\
\frac{R \vdash e \xrightarrow{(v_1, v_2)} e'}{R \vdash \text{snd } e \xrightarrow{v_2} \text{snd } e'} \\
\\
\text{(AT)} \\
\frac{R \vdash e \xrightarrow{v'} e'}{R \vdash e \text{ at } s \xrightarrow{v} e' \text{ at } s} \\
\\
\text{(DEF)} \\
\frac{R \vdash e \xrightarrow{v} e'}{R \vdash x = e \xrightarrow{[v/x]} x = e'} \\
\\
\text{(APP)} \\
\frac{R(f) = \lambda y. e \text{ with } D \quad R \vdash x = e \text{ and } y = e' \text{ and } D \xrightarrow{R'} D'}{R \vdash x = f(e') \xrightarrow{R'} D'} \\
\\
\text{(AND)} \\
\frac{R, R_2 \vdash D_1 \xrightarrow{R_1} D'_1 \quad R, R_1 \vdash D_2 \xrightarrow{R_2} D'_2}{R \vdash D_1 \text{ and } D_2 \xrightarrow{R_1, R_2} D'_1 \text{ and } D'_2} \\
\\
\text{(IF-1)} \\
\frac{R \vdash e \xrightarrow{\text{true}} e' \quad R \vdash D_1 \xrightarrow{R'} D'_1}{R \vdash \text{if } e \text{ then } D_1 \text{ else } D_2 \xrightarrow{R'} \text{if } e' \text{ then } D'_1 \text{ else } D_2} \\
\\
\text{(IF-2)} \\
\frac{R \vdash e \xrightarrow{\text{false}} e' \quad R \vdash D_2 \xrightarrow{R'} D'_2}{R \vdash \text{if } e \text{ then } D_1 \text{ else } D_2 \xrightarrow{R'} \text{if } e' \text{ then } D_1 \text{ else } D'_2}
\end{array}$$

Figure 3: Centralized synchronous semantics

The $|\cdot|$ operator erases annotations from a distributed value to get a centralised value:

$$\begin{aligned} |i \text{ at } s| &= i \\ |\lambda x.e \text{ with } D \text{ at } s| &= \lambda x.e \text{ with } D \\ |(\hat{v}_1, \hat{v}_2) \text{ at } s| &= (|\hat{v}_1|, |\hat{v}_2|) \end{aligned}$$

This operator extends straightforwardly to reaction environments.

The distributed semantics is defined by means of two predicates refined from their centralized versions. $\hat{R} \stackrel{\ell}{\vdash} e_1 \xrightarrow{\hat{v}} e_2$ states that in the distributed reaction environment \hat{R} , the expression e_1 emits the distributed value \hat{v} and rewrites into e_2 . ℓ represents the set of locations involved in the computation of \hat{v} . The predicate for declarations is defined as well.

We note \mathcal{S} the set of declared constant locations, and $\mathcal{L} \subseteq \mathcal{S} \times \mathcal{S}$ the set of declared communication links. The relation \mathcal{L} defines the *possibility* of communications, and not the actual existence of communication channels, which will be inferred by the refined version of the type system. G denotes an architecture graph, composed of a set of locations \mathcal{S} , and a set of links \mathcal{L} between these locations.

An architecture description \mathcal{A} defines an architecture graph G : the notation $G \vdash \mathcal{A} : G'$ means that given the architecture graph G , \mathcal{A} defines the new architecture graph G' . The rules ARCH, DEF-LOC and DEF-LINK define this predicate:

$$\begin{array}{c} \text{(ARCH)} \\ \frac{\langle \mathcal{S}, \mathcal{L} \rangle \vdash \mathcal{A}_1 : \langle \mathcal{S}_1, \mathcal{L}_1 \rangle \quad \langle \mathcal{S}_1, \mathcal{L}_1 \rangle \vdash \mathcal{A}_2 : \langle \mathcal{S}_2, \mathcal{L}_2 \rangle}{\langle \mathcal{S}, \mathcal{L} \rangle \vdash \mathcal{A}_1 ; \mathcal{A}_2 : \langle \mathcal{S}_2, \mathcal{L}_2 \rangle} \quad \text{(DEF-LOC)} \\ \langle \mathcal{S}, \mathcal{L} \rangle \vdash \text{loc } A : \langle \mathcal{S} \cup \{A\}, \mathcal{L} \rangle \end{array}$$

$$\begin{array}{c} \text{(DEF-LINK)} \\ \frac{A_1, A_2 \in \mathcal{S}}{\langle \mathcal{S}, \mathcal{L} \rangle \vdash \text{link } A_1 \text{ to } A_2 : \langle \mathcal{S}, \mathcal{L} \cup \{A_1 \mapsto A_2\} \rangle} \end{array}$$

For clarity reasons, we assume that the graph G defined by an architecture is global for subsequent semantic rules. The annotated execution of a program P is denoted $\hat{S}_{in} \vdash P : \hat{S}_{out}$, meaning that under a sequence of input environments $\hat{S}_{in} = \hat{R}_1.\hat{R}_2\dots$, the program $P = \mathcal{A};d;D$ produces a sequence of output environments $\hat{S}_{out} = \hat{R}'_1.\hat{R}'_2\dots$ such that:

$$\frac{\langle \emptyset, \emptyset \rangle \vdash \mathcal{A} : G \quad \hat{R}_d, \text{hd}(\hat{S}_{in}), \text{hd}(\hat{S}_{out}) \Vdash^{\ell} D \xrightarrow{\text{hd}(\hat{S}_{out})} D' \quad \text{tl}(\hat{S}_{in}) \Vdash \mathcal{A}; d; D' : \text{tl}(\hat{S}_{out})}{\hat{S}_{in} \Vdash \mathcal{A}; d; D : \hat{S}_{out}}$$

Where, as for the centralized semantics, \hat{R}_d defined from the sequence of node definitions $d = \text{node } f_i[\vec{\delta}_i](x_i) = e_i \text{ with } D_i$ as:

$$\hat{R}_d = [\Lambda \vec{\delta}_1. \lambda x_1. e_1 \text{ with } D_1/f_1, \dots, \Lambda \vec{\delta}_n. \lambda x_n. e_n \text{ with } D_n/f_n]$$

The rules for the predicates $\hat{R} \Vdash^{\ell} e_1 \xrightarrow{\hat{v}} e_2$ and $\hat{R} \Vdash^{\ell} D_1 \xrightarrow{\hat{R}'} D_2$ are given in Figures ?? and 5. An immediate value can be emitted anywhere (rule IMM). Rule INST defines the instantiation. A distributed value can be communicated from location s to location s' if there exists a communication link from s to s' (rule COMM). A binary operation can be performed only on immediate values located on the same location A ; the result is located on A as well (rule OP). An annotated expression must involve at most the location stated for its computation (rule AT). An application involves choosing a set of constant locations, and replacing location parameters by these locations in the expression and the declaration (rule AT). The other rules state that the computation of a statement involves the union of the locations involved for the computation of its compounds.

Lemma 1 states that in a well-formed environment, a program reacts only by emitting well-formed values.

Lemma 1. *For all D, D', \hat{R}, \hat{R}' such that $\hat{R} \Vdash^{\ell} D \xrightarrow{\hat{R}'} D'$, if \hat{R} is well-formed, then \hat{R}' is well-formed.*

Proof. By induction on the structure of the expression or declaration. The proof lies on the definitions of well-formed values, and the \sqcup operation:

- if a value (\hat{v}_1, \hat{v}_2) at s is well-formed, then \hat{v}_1 and \hat{v}_2 are well-formed (case $e = \text{fst } e'$ or $e = \text{snd } e'$);
- if two values dv_1 at s_1 and dv_2 at s_2 are well-formed, then the value:

$$(dv_1 \text{ at } s_1, dv_2 \text{ at } s_2) \text{ at } s_1 \sqcup s_2$$

is well-formed (case $e = (e_1, e_2)$);

$$\begin{array}{c}
\text{(IMM)} \\
\hat{R} \Vdash^\ell i \xrightarrow{i \text{ at } s} i
\end{array}
\qquad
\begin{array}{c}
\text{(INST)} \\
\hat{R}, [\hat{v}/x] \Vdash^{\text{loc}(\hat{v})} x \xrightarrow{\hat{v}} x
\end{array}
\qquad
\begin{array}{c}
\text{(COMM)} \\
\frac{\hat{R} \Vdash^\ell e \xrightarrow{dv \text{ at } s} e' \quad (s, s') \in \mathcal{L}}{\hat{R} \Vdash^{\ell \cup \{s'\}} e \xrightarrow{dv[s'/s] \text{ at } s'} e'}
\end{array}$$

$$\begin{array}{c}
\text{(FBY)} \\
\frac{\hat{R} \Vdash^{\ell_1} e_1 \xrightarrow{\hat{v}_1} e'_1 \quad \hat{R} \Vdash^{\ell_2} e_2 \xrightarrow{\hat{v}_2} e'_2}{\hat{R} \Vdash^{\ell_1 \cup \ell_2} e_1 \text{ fby } e_2 \xrightarrow{\hat{v}_1 \mid \hat{v}_2} \text{fby } e'_2}
\end{array}$$

$$\begin{array}{c}
\text{(OP)} \\
\frac{\hat{R} \Vdash^{\ell_1} e_1 \xrightarrow{i_1 \text{ at } A} e'_1 \quad \hat{R} \Vdash^{\ell_2} e_2 \xrightarrow{i_2 \text{ at } A} e'_2 \quad i = \text{op}(i_1, i_2)}{\hat{R} \Vdash^{\ell_1 \cup \ell_2} \text{op}(e_1, e_2) \xrightarrow{i \text{ at } A} \text{op}(e'_1, e'_2)}
\end{array}$$

$$\begin{array}{c}
\text{(PAIR)} \\
\frac{\hat{R} \Vdash^{\ell_1} e_1 \xrightarrow{dv_1 \text{ at } s_1} e'_1 \quad \hat{R} \Vdash^{\ell_2} e_2 \xrightarrow{dv_2 \text{ at } s_2} e'_2}{\hat{R} \Vdash^{\ell_1 \cup \ell_2} (e_1, e_2) \xrightarrow{(dv_1 \text{ at } s_1, dv_2 \text{ at } s_2) \text{ at } s_1 \sqcup s_2} (e'_1, e'_2)}
\end{array}$$

$$\begin{array}{c}
\text{(FST)} \\
\frac{\hat{R} \Vdash^\ell e \xrightarrow{(\hat{v}_1, \hat{v}_2) \text{ at } s} e'}{\hat{R} \Vdash^\ell \text{fst } e \xrightarrow{\hat{v}_1} \text{fst } e'}
\end{array}
\qquad
\begin{array}{c}
\text{(SND)} \\
\frac{\hat{R} \Vdash^\ell e \xrightarrow{(\hat{v}_1, \hat{v}_2) \text{ at } s} e'}{\hat{R} \Vdash^\ell \text{snd } e \xrightarrow{\hat{v}_2} \text{snd } e'}
\end{array}
\qquad
\begin{array}{c}
\text{(AT)} \\
\frac{\hat{R} \Vdash^{\{s\}} e \xrightarrow{\hat{v}} e'}{\hat{R} \Vdash^{\{s\}} e \text{ at } s \xrightarrow{\hat{v}} e' \text{ at } s}
\end{array}$$

Figure 4: Distributed synchronous semantics (expressions)

$$\begin{array}{c}
\text{(DEF)} \\
\frac{\hat{R} \Vdash^{\ell} e \xrightarrow{\hat{v}} e'}{\hat{R} \Vdash^{\ell} x = e \xrightarrow{[\hat{v}/x]} x = e'} \\
\\
\text{(APP)} \\
\frac{\hat{R}(f) = \Lambda \delta_1, \dots, \delta_n. \lambda y. e \text{ with } D \text{ at } s \quad \{s_1, \dots, s_n\} \subseteq \mathcal{S} \quad \hat{R} \Vdash^{\ell} x = e[\vec{s}/\vec{\delta}] \text{ and } y = e' \text{ and } D[\vec{s}/\vec{\delta}] \xrightarrow{\hat{R}'} D'}{\hat{R} \Vdash^{\ell} x = f(e') \xrightarrow{\hat{R}'} D'} \\
\\
\text{(AND)} \\
\frac{\hat{R}, \hat{R}_2 \Vdash^{\ell_1} D_1 \xrightarrow{\hat{R}_1} D'_1 \quad \hat{R}, \hat{R}_1 \Vdash^{\ell_2} D_2 \xrightarrow{\hat{R}_2} D'_2}{\hat{R} \Vdash^{\ell_1 \cup \ell_2} D_1 \text{ and } D_2 \xrightarrow{\hat{R}_1, \hat{R}_2} D'_1 \text{ and } D'_2} \\
\\
\text{(IF-1)} \\
\frac{\hat{R} \Vdash^{\ell} e \xrightarrow{\text{true at } s} e' \quad \hat{R} \Vdash^{\ell'} D_1 \xrightarrow{\hat{R}'} D'_1 \quad \forall s' \in \ell', s \triangleright s'}{\hat{R} \Vdash^{\ell \cup \ell'} \text{if } e \text{ then } D_1 \text{ else } D_2 \xrightarrow{\hat{R}'} \text{if } e' \text{ then } D'_1 \text{ else } D_2} \\
\\
\text{(IF-2)} \\
\frac{\hat{R} \Vdash^{\ell} e \xrightarrow{\text{false at } s} e' \quad \hat{R} \Vdash^{\ell'} D_2 \xrightarrow{\hat{R}'} D'_2 \quad \forall s' \in \ell', s \triangleright s'}{\hat{R} \Vdash^{\ell \cup \ell'} \text{if } e \text{ then } D_1 \text{ else } D_2 \xrightarrow{\hat{R}'} \text{if } e' \text{ then } D_1 \text{ else } D'_2}
\end{array}$$

Figure 5: Distributed synchronous semantics (equations)

- rule COMM applies only on values of the form $dv \text{ at } s$, where $s \neq \top$. For any $s' \neq \top$, if $dv \text{ at } s$ is well-formed, then $dv[s'/s] \text{ at } s'$ is well-formed.

□

Lemma 2 states that if a program reacts with the distributed semantics, then it reacts with the centralized one and produces the same values. The proof is left in appendix.

Lemma 2. *For all D, D', \hat{R}, \hat{R}' , if $\hat{R} \Vdash^{\ell} D \xrightarrow{\hat{R}'} D'$, then there exists R, R' such that $R = |\hat{R}|$, $R' = |\hat{R}'|$ and $R \vdash D \xrightarrow{R'} D'$.*

3.4 Spacial Types

For the sake of clarity, we first present a simplified version of the type system. For the projection, we will need a refinement of this first version to take communication channels into account (see Section 4.3).

The syntax of spacial type expressions is:

$$\begin{aligned}
\sigma &::= \forall \alpha_1, \dots, \alpha_n. \forall \delta_1, \dots, \delta_n : C.t \\
t &::= c \mid \alpha \mid t \text{ at } s \mid t \xrightarrow{\ell} t \mid t \times t \\
\ell &::= \{s_1, \dots, s_n\} \\
s &::= \delta \mid A \\
H &::= H \text{ at } A \mid [x_1 : \sigma_1, \dots, x_n : \sigma_n] \\
C &::= \{s_1 \triangleright s'_1, \dots, s_n \triangleright s'_n\}
\end{aligned}$$

We note H the spacial typing environments. $H \text{ at } A$ denotes a located environment, i.e., a typing environment from which every spacial type will be forced to represent a value entirely located on A .

We distinguish spacial type schemes (σ), which can be quantified, from simple spacial types (t). A set of constraints C can be associated to quantification of location variables ($\forall \delta_1, \dots, \delta_n : C.t$). We note $\forall \delta_1, \dots, \delta_n.t$ the scheme $\forall \delta_1, \dots, \delta_n : \emptyset.t$. A simple spacial type can be either a stream type (c), a type variable (α), a located type ($t \text{ at } s$), a node type ($s \xrightarrow{\ell} s$), or a pair type ($s \times s$). ℓ denotes sets of locations.

C is a set of constraints between locations. A constraint $s_1 \triangleright s_2$ means that either $s_1 = s_2$, or there exists a communication link from s_1 to s_2 . Conversely, a declaration of communications links \mathcal{L} leads to the set of constraints $\text{constr}(\mathcal{L})$.

$$\text{constr}(\mathcal{L}) = \{s \triangleright s' \mid (s, s') \in \mathcal{L}\}$$

A value of spacial type t at s is a value located on s . A value of spacial type $t_1 \xrightarrow{-\langle \ell \rangle} t_2$ is a node whose input is of spacial type t_1 , whose output is of spacial type t_2 , and whose computation involves the set of locations ℓ .

The following equalities stand for spacial types:

$$\begin{aligned} (t_1 \times t_2) \text{ at } s &= (t_1 \text{ at } s) \times (t_2 \text{ at } s) \\ (t_1 \xrightarrow{-\langle s \rangle} t_2) \text{ at } s &= (t_1 \text{ at } s) \xrightarrow{-\langle s \rangle} (t_2 \text{ at } s) \\ t \text{ at } s \text{ at } s &= t \text{ at } s \end{aligned}$$

$$\frac{t_1 = t'_1 \quad t_2 = t'_2}{(t_1 \times t_2) = (t'_1 \times t'_2)} \quad \frac{t_1 = t'_1 \quad t_2 = t'_2}{t_1 \xrightarrow{-\langle \ell \rangle} t_2 = t'_1 \xrightarrow{-\langle \ell \rangle} t'_2}$$

A spacial type t is *well-formed* iff

$$\forall t', s_1, s_2, t = t' \text{ at } s_1 \text{ at } s_2 \Rightarrow s_1 = s_2.$$

The instantiation mechanism ensures the localization of a type instantiated from a located environment:

$$(t[t_1/\alpha_1, \dots, t_n/\alpha_n, s/\delta], C[s_1/\delta_1, \dots, s_m/\delta_m]) \leq \forall \alpha_1 \dots \alpha_n \forall \delta_1 \dots \delta_m : C.t$$

$$(t \text{ at } s, C) \leq (H \text{ at } s)(x) \Leftrightarrow (t \text{ at } s, C) \leq H(x)$$

A type t can be generalized w.r.t. a typing environment H and local locations $\mathcal{S} = \{\delta_1, \dots, \delta_p\}$ to a type scheme $\forall \alpha_1, \dots, \alpha_n. \forall \delta_1, \dots, \delta_p : C.t$, provided that the set of location variable generalized $\{\delta_1, \dots, \delta_p\}$ is not free in H , and matches with the local architecture. Generalization w.r.t. an empty local architecture allows the introduction of at most one location variable, with no constraint. This means that a node, without any location constraint given by the programmer, will receive a type of the form $\forall \alpha_1, \dots, \alpha_n. \forall \delta. (t \text{ at } \delta)$, meaning that this node is available and computable at any unique location δ .

We note respectively $\text{FLV}(t)$ and $\text{FTV}(t)$ the set of free location variables and free type variables of the type t . FLV and FTV are straightforwardly extended to typing environments.

A set of constraints C is *compatible* with a set of communication links \mathcal{L} , noted $\mathcal{L} \models C$, iff $s \triangleright s' \in C \wedge s \neq s' \Rightarrow (s, s') \in \mathcal{L}$.

Before presenting our spacial type system, we introduce the following notations:

- For a program P , the notation $\vdash P : t$ means that the program P is of spacial type t .

- For declarations (resp. expressions), the notation $H|G \vdash D : H'/\ell$ (resp. $H|G \vdash e : t/\ell$) means that, in the spacial type environment H and the architecture graph G , the declaration D (resp. the expression e) defines a new environment H' (resp. is of spacial type t), and its computation involves the set of locations ℓ .

The function $\text{locations}(\cdot)$ gives the set of locations involved in the spacial type given as argument. It is defined as:

$$\begin{aligned} \text{locations}(t_1 \times t_2) &= \text{locations}(t_1) \cup \text{locations}(t_2) \\ \text{locations}(t_1 \xrightarrow{\ell} t_2) &= \ell \\ \text{locations}(t \text{ at } s) &= \{s\} \end{aligned}$$

The top-level declaration of a program is typed from the initial environment H_0 , defined as:

$$H_0 = \left[\begin{array}{l} \cdot \text{ fby } \cdot : \forall \alpha. \forall \delta. \alpha \text{ at } \delta \times \alpha \text{ at } \delta \xrightarrow{\{\delta\}} \alpha \text{ at } \delta, \\ \text{fst} \cdot : \forall \alpha, \beta. \forall \delta. \alpha \text{ at } \delta \times \beta \text{ at } \delta \xrightarrow{\{\delta\}} \alpha \text{ at } \delta, \\ \text{snd} \cdot : \forall \alpha, \beta. \forall \delta. \alpha \text{ at } \delta \times \beta \text{ at } \delta \xrightarrow{\{\delta\}} \beta \text{ at } \delta, \\ (+) : \forall \delta. c \text{ at } \delta \times c \text{ at } \delta \xrightarrow{\{\delta\}} c \text{ at } \delta, \\ \dots \end{array} \right]$$

Our spacial type system is formally defined by the axioms and inference rules shown in Figure 6.

Typing a program involves building an architecture graph from the architecture description, and then using it to type the nodes and the main declarations (rule PROG).

An immediate value can be used on any location (rule IMM). Type schemes can be instantiated (rule INST). Typing a pair involves stating that this pair has to be evaluated on the union of the sets of locations on which each member of the pair has to be evaluated (rule PAIR). Typing a located expression ($e \text{ at } A$) involves building a located typing environment (rule AT). Communications are expressed as subtyping (rule COMM).

The spacial type of a node is composed of the spacial types of its inputs, the expression computed by this node, and the set of locations involved in this computation. The type of a node is generalized w.r.t. the set of sites and links introduced by this architecture (rule NODE).

Typing an equation $x = e$ involves building a singleton typing environment (rule DEF). Rule APP states that an application must be evaluated on the union of the

$$\begin{array}{c}
\text{(PROG)} \\
\frac{\langle \emptyset, \emptyset \rangle \vdash \mathcal{A} : G \quad H_0 | G \vdash d : H/\ell \quad H, H_1 | G \vdash D : H_1/\ell'}{\vdash \mathcal{A}; d; D : H_1} \\
\\
\text{(IMM)} \quad H | G \vdash i : c \text{ at } s/\{s\} \qquad \text{(INST)} \quad \frac{(t, C) \leq (H(x)) \quad \mathcal{L} \models C}{H | \langle \mathcal{S}, \mathcal{L} \rangle \vdash x : t / \text{locations}(t)} \\
\\
\text{(PAIR)} \quad \frac{H | G \vdash e_1 : t_1/\ell_1 \quad H | G \vdash e_2 : t_2/\ell_2}{H | G \vdash (e_1, e_2) : t_1 \times t_2/\ell_1 \cup \ell_2} \\
\\
\text{(AT)} \quad \frac{H \text{ at } s | \langle \mathcal{S}, \mathcal{L} \rangle \vdash e : t/\ell \quad s \in \mathcal{S}}{H | \langle \mathcal{S}, \mathcal{L} \rangle \vdash e \text{ at } s : t/\ell} \qquad \text{(COMM)} \quad \frac{H | \langle \mathcal{S}, \mathcal{L} \rangle \vdash e : t \text{ at } s/\ell \quad \mathcal{L} \models s \triangleright s'}{H | \langle \mathcal{S}, \mathcal{L} \rangle \vdash e : t \text{ at } s'/\ell \cup \{s'\}} \\
\\
\text{(NODE)} \quad \frac{H, x : t, H_1 | \langle \mathcal{S}', \mathcal{L}' \rangle \vdash D : H_1/\ell_1 \quad H, x : t, H_1 | \langle \mathcal{S}', \mathcal{L}' \rangle \vdash e : t/\ell_2 \quad \mathcal{S}' = \mathcal{S} \cup \{\delta_1, \dots, \delta_n\} \\
\mathcal{L}' \subseteq \mathcal{L} \cup (\{\delta_1, \dots, \delta_n\} \times \mathcal{S}) \cup (\mathcal{S} \times \{\delta_1, \dots, \delta_n\}) \quad \{\alpha_1, \dots, \alpha_m\} = \text{FTV}(t) - \text{FTV}(H) \\
C = \text{constr}(\mathcal{L}' \setminus \mathcal{L}) \quad \sigma = \forall \alpha_1, \dots, \alpha_m. \forall \delta_1, \dots, \delta_n : C.t \dashv\ell_1 \cup \ell_2 \dashv t_1}{H | G \vdash \text{node } f[\delta_1, \dots, \delta_n](x) = e \text{ with } D : [\sigma/f]/\ell_1 \cup \ell_2} \\
\\
\text{(DEF)} \quad \frac{H | G \vdash e : t/\ell}{H | G \vdash x = e : [t/x]/\ell} \qquad \text{(APP)} \quad \frac{H | G \vdash f : t_1 \dashv\ell_1 \dashv t_2/\ell_2 \quad H | G \vdash e : t_1/\ell_3}{H | G \vdash x = f(e) : [t_2/x]/\ell_1 \cup \ell_2 \cup \ell_3} \\
\\
\text{(AND)} \quad \frac{H | G \vdash D_1 : H_1/\ell_1 \quad H | G \vdash D_2 : H_2/\ell_2}{H | G \vdash D_1 \text{ and } D_2 : H_1, H_2/\ell_1 \cup \ell_2} \qquad \text{(LET)} \quad \frac{H | G \vdash D_1 : H_1/\ell_1 \quad H, H_1 | G \vdash D_2 : H_2/\ell_2}{H | G \vdash \text{let } D_1 \text{ in } D_2 : H_2/\ell_1 \cup \ell_2} \\
\\
\text{(IF)} \quad \frac{H | G \vdash e : c \text{ at } s/\ell \quad H | G \vdash D_1 : H'/\ell_1 \quad H | G \vdash D_2 : H'/\ell_2 \quad \mathcal{L} \models \{s \triangleright s' | s' \in \ell_1 \cup \ell_2\}}{H | G \vdash \text{if } e \text{ then } D_1 \text{ else } D_2 : H'/\ell \cup \ell_1 \cup \ell_2}
\end{array}$$

Figure 6: Spacial type system

set of locations where the node f and its argument e must be evaluated, and the set of locations ℓ_1 involved in the computation of the node f . Parallel and sequential declarations involve, for their computations, the union of the sets and of locations involved in the computation of their compounds (rules AND and LET). Finally, typing an `if/then/else` declaration involves locating the condition expression on a location s , and adding constraints that every location involved in declarations D_1 and D_2 must be accessible from s (rule IF).

A set of locations ℓ is related to an abstract location, as defined in Section 3.3, by:

$$\bar{\ell} = \begin{cases} s & \text{if } \ell = \{s\} \\ \top & \text{else.} \end{cases}$$

We denote by $\hat{v} : t$ the fact that the distributed value \hat{v} has spacial type t :

$$\frac{s \neq \top}{dv \text{ at } s : c \text{ at } s} \qquad \frac{\hat{v}_1 : t_1 \quad \hat{v}_2 : t_2}{(\hat{v}_1, \hat{v}_2) : t_1 \times t_2}$$

We denote by $\hat{R} : H$ the type compatibility between \hat{R} and H :

$$\hat{R} : H \Leftrightarrow \forall x \in \text{dom}(\hat{R}), x \in \text{dom}(H) \qquad \wedge \exists (t, C) \text{ s.t. } (t, C) \leq H(x) \wedge \hat{R}(x) : t$$

Theorem 1 states that if a program reacts with the centralized semantics, and is accepted by the spacial type system, then there exists a spacialized execution such that the distributed values of this execution are equal to the centralized ones. The spacial types are preserved by this spacial execution. The proof is in appendix.

Theorem 1 (Correction). *For all D, D', H, H', R, R', G , if $H|G \vdash D : H'/\ell$ and $R \vdash D \xrightarrow{R'} D'$, then there exists \hat{R}, \hat{R}' such that $\hat{R} \stackrel{\ell}{\Vdash} D \xrightarrow{\hat{R}'} D'$, $\hat{R} : H$, $\hat{R}' : H'$, $|\hat{R}| = R$ and $|\hat{R}'| = R'$.*

3.5 Implementation

The spacial type system presented in the previous section lies on a subtyping mechanism. It corresponds to the case when communications can occur *anywhere* in the code. This situation raises two problems. The first one is that the implementation of type systems with subtyping mechanism is costly: usual algorithms involve the systematic application of the subtyping rule. The second one is that this choice leads

to a situation where the programmer has no control on where the communications can occur. These problems, though orthogonal, can be addressed together: giving some control to the programmer means restricting the points where subtyping can be applied. We show here how to refine our type system in order to address these two problems.

We restrain communicated values to be variables introduced by equations ($x = e$). Thus, within the program given in Section 2.2, only y and z can be communicated from one location to another. Then, we can use a generalization mechanism to infer communication constraints, instead of inferring them by subtyping. This corresponds to restricting the subtyping mechanism at instantiation points.

The refined type system, noted $H|G \vdash_i e : t/\ell$, involves the suppression of the rule COMM and the modification of the rule DEF as follows:

$$\text{(DEF)} \quad \frac{H|G \vdash_i e : t \text{ at } s/\ell}{H|G \vdash_i x = e : [\forall \delta : \{s \triangleright \delta\}.t \text{ at } \delta/x]/\ell}$$

Typing an equation $x = e$ involves locating the result of e on a location s , and building a typing environment allowing x to be on any location accessible from s ($x : \forall \delta : \{s \triangleright \delta\}.t \text{ at } \delta$) (rule DEF). The other rules remain the same.

We then show the correction of the refinement, i.e., that every program accepted by the refined type system is accepted by the original type system.

Theorem 2 (Refined type system correction). *For all H, H', D, G, ℓ , if $H|G \vdash_i D : H'/\ell$, then $H|G \vdash D : H'/\ell$.*

Proof. By induction on the structure of D and e . Whenever the rule INST of the refined type system can be applied, we can apply the rule INST, then the rule COMM, of the original type system. \square

This refined type system has been implemented in the compiler of LUCID SYNCHRONONE.

4 Distribution

4.1 Principle

Once programs have been typed, every expression is annotated with a location that specifies where it has to be computed. Communications are inserted when a value is

produced at a location and used at another. From this typed program, the compiler produces several new programs — one for every location s — erasing the code that is not necessary at this location s . The run-time we have chosen is a classical one for globally asynchronous locally synchronous (GALS) systems: communications are done through FIFOs.

We show below the result of the projection of f on A and B, noted respectively f_A and f_B . The distribution of this node will involve adding a communication between these two computations. This communication will take the form of an additional output (named here c_y) on f_A , together with an additional input on f_B . Here, c_y holds the value y , computed on A and used on B. Original inputs and outputs are not suppressed: $()$ denotes an irrelevant value which will not be used on the current location. It is used here to replace the output z , whose computation is suppressed at A.

```
node f_A(x) = ((), c_y) with
  c_y = g(x)
```

```
node f_B(x, c_y) = z with
  z = h(c_y)
```

The semantically equivalent distributed system is then obtained by connecting the input and output c_y , holding the communicated value y . The program below shows the distributed execution, using a FIFO materialized by `send/receive` primitives, of the result of the projection of the program $y = f(x)$.

$$\begin{array}{l|l} (y_A, c_y) = f_A(x); & \text{receive}(c_y); \\ \text{send}(c_y) & y_B = f_B((), c_y) \end{array}$$

4.2 Example

The result of the projection of the two nodes of the Section 2.5 on the location DSP is given on Figure 7. The projection of the `channel` node shows that the node applications of `filter` and `crc` have been removed, and that a new input `c1` (holding the value of f) and a new output `c2` (holding the value of d) have been added. This implies the addition, on the projection of the `multichannel_sdr` node, of two new inputs (`c2` and `c3`) and two new outputs (`c4` and `c5`), one for each `channel` instance. The new input `c1` of the projected `multichannel_sdr` node holds the value c .

```

node channel(filter,demod,crc,x,c1) = ((,c2) with
  d = demod(c1)
  and c2 = d

node multichannel_sdr(x,s,c1,c2,c3) = (y,c4,c5)
  if c1 then
    (y,c4) = channel(filter_1800,gmsk,conv,x,c2)
  else
    (y,c5) = channel(filter_2000,qpsk,turbo,x,c3)

```

Figure 7: Result of the projection on DSP

4.3 Channel Inference

We define now a refined type system, allowing the inference of communication channels. These channels will be used for the projection, and have been omitted in the first type system for the sake of clarity.

A channel is a location pair associated with a name, noted $A_1 \xrightarrow{n} A_2$: n is the name of the channel, A_1 its source location, and A_2 its destination location. The set of channel names is ordered, so as to keep consistence of inputs and outputs added, from the node definition to node instances. T denotes sets of channels. The union of sets of channels, noted $T_1 \uplus T_2$, is defined iff channel names in T_1 and T_2 are disjoint.

A channel can be renamed: we note by $s \xrightarrow{r(n)} s'$ the channel $s \xrightarrow{n} s'$ renamed by the function r . Given two channel sets T and T' , we note $T' \cong T$ the fact that T' is equal to T modulo a monotone renaming of its channels (i.e., the order between these channels is the same in T and T'):

$$T' \cong T \Leftrightarrow \exists r \text{ s.t. } r \text{ monotone and } T' = \{s \xrightarrow{r(n)} s' \mid s \xrightarrow{n} s' \in T\}$$

The Figures 8 and 9 shows our refined type system: $H|G \vdash e : t/\ell/T$ means that, in the typing environment H , the expression e has spacial type t , and its computation involves the set of locations ℓ and the set of communication channels T . Channels are added for each communication (rule COMM-C), and for application of nodes which need internal channels (rule APP-C). For applications, we have to rename channels, with a renaming preserving the name order. Thus, we can instantiate nodes several times, adding each time new channel names as inputs and outputs

when the projection will be performed. The other rules only perform the disjoint unions (w.r.t. channel names) of sets of channels. The rules defining the program types and the architecture remain the same and are not repeated.

$$\begin{array}{c}
\text{(IMM-C)} \\
\frac{}{H|G \vdash i : c \text{ at } s/\{s\}/\emptyset}
\end{array}
\qquad
\begin{array}{c}
\text{(INST-C)} \\
\frac{(t, C) \leq (H(x)) \quad \mathcal{L} \models C}{H|\langle \mathcal{S}, \mathcal{L} \rangle \vdash x : t/\text{locations}(t)/\emptyset}
\end{array}$$

$$\begin{array}{c}
\text{(PAIR-C)} \\
\frac{H|G \vdash e_1 : t_1/\ell_1/T_1 \quad H|G \vdash e_2 : t_2/\ell_2/T_2}{H|G \vdash (e_1, e_2) : t_1 \times t_2/\ell_1 \cup \ell_2/T_1 \uplus T_2}
\end{array}$$

$$\begin{array}{c}
\text{(AT-C)} \\
\frac{H \text{ at } A|G \vdash e : t/\ell/T}{H|G \vdash e \text{ at } A : t/\ell/T}
\end{array}
\qquad
\begin{array}{c}
\text{(COMM-C)} \\
\frac{H|\langle \mathcal{S}, \mathcal{L} \rangle \vdash e : t \text{ at } s/\ell/T \quad \mathcal{L} \models s \triangleright s'}{H|\langle \mathcal{S}, \mathcal{L} \rangle \vdash e : t \text{ at } s'/\ell \cup \{s'\}/T \uplus \{s \xrightarrow{n} s'\}}
\end{array}$$

Figure 8: Refined type system, with communication channels (expressions)

4.4 Projection

We will now define a type-directed operation of *projection of an expression on a location* A . This operation is defined separately, as it has to be performed on an already annotated program: links between values of each projected program are defined by the channels inferred by the type system.

The projection of a declaration D on a location A is noted $H|G \vdash D : H'/\ell/T \xrightarrow{A} D'$, and results in a new declaration D' , containing only the computations to be performed on A . The projection of an expression e , of spacial type t , on a location A , is noted $H|G \vdash e : t/\ell/T \xrightarrow{A} e'/D$, and results in a new expression e' , as well as a declaration D , containing channels outputs to be defined. A channel named n in an environment channel will be introduced as the variable c_n as input or output of the target program, c assumed to be of different name space than other variables of the source program.

We denote by ϵ the empty declaration, and by $()$ a value which will never be used (i.e., void). For any declaration D , D and $\epsilon = \epsilon$ and $D = D$. For any expression e , we have $(() e) = (e ()) = ()$.

(NODE-C)

$$\frac{\begin{array}{l} H, x : t, H_1 | \langle \mathcal{S}', \mathcal{L}' \rangle \vdash D : H_1 / \ell_1 / T_1 \\ H, x : t, H_1 | \langle \mathcal{S}', \mathcal{L}' \rangle \vdash e : t / \ell_2 / T_2 \quad \mathcal{S}' = \mathcal{S} \cup \{\delta_1, \dots, \delta_n\} \\ \mathcal{L}' \subseteq \mathcal{L} \cup (\{\delta_1, \dots, \delta_n\} \times \mathcal{S}) \cup (\mathcal{S} \times \{\delta_1, \dots, \delta_n\}) \quad \{\alpha_1, \dots, \alpha_m\} = \text{FTV}(t) - \text{FTV}(H) \\ C = \text{constr}(\mathcal{L}' \setminus \mathcal{L}) \quad \sigma = \forall \alpha_1, \dots, \alpha_m. \forall \delta_1, \dots, \delta_n : C. t \dashv \langle \ell_1 \cup \ell_2 / T_1 \uplus T_2 \rangle \rightarrow t_1 \end{array}}{H | G \vdash \text{node } f[\delta_1, \dots, \delta_n](x) = e \text{ with } D : [\sigma / f] / \ell_1 \cup \ell_2 / \emptyset}$$

(DEF-C)

$$\frac{H | G \vdash e : t / \ell / T}{H | G \vdash x = e : [t / x] / \ell / T}$$

(APP-C)

$$\frac{H | G \vdash f : t_1 \dashv \langle \ell_1 / T_1 \rangle \rightarrow t_2 / \ell_2 / T_2 \quad H | G \vdash e : t_1 / \ell_3 / T_3 \quad T'_1 \cong T_1}{H | G \vdash x = f(e) : [t_2 / x] / \ell_1 \cup \ell_2 \cup \ell_3 / T'_1 \uplus T_2 \uplus T_3}$$

(AND-C)

$$\frac{H | G \vdash D_1 : H_1 / \ell_1 / T_1 \quad H | G \vdash D_2 : H_2 / \ell_2 / T_2}{H | G \vdash D_1 \text{ and } D_2 : H_1, H_2 / \ell_1 \cup \ell_2 / T_1 \uplus T_2}$$

(LET-C)

$$\frac{H | G \vdash D_1 : H_1 / \ell_1 / T_1 \quad H, H_1 | G \vdash D_2 : H_2 / \ell_2 / T_2}{H | G \vdash \text{let } D_1 \text{ in } D_2 : H_2 / \ell_1 \cup \ell_2 / T_1 \uplus T_2}$$

(IF-C)

$$\frac{\begin{array}{l} H | G \vdash e : c \text{ at } s / \ell / T \\ H | G \vdash D_1 : H' / \ell_1 / T_1 \quad H | G \vdash D_2 : H' / \ell_2 / T_2 \quad C = \{s \triangleright s' \mid s' \in \ell_1 \cup \ell_2\} \quad \mathcal{L} \models C \end{array}}{H | G \vdash \text{if } e \text{ then } D_1 \text{ else } D_2 : H' / \ell \cup \ell_1 \cup \ell_2 / T \uplus T_1 \uplus T_2 \uplus \text{channels}(C)}$$

Figure 9: Refined type system, with communication channels (equations)

Also, we note $T \uparrow A$ (resp. $T \downarrow A$) the set of channels with origin (resp. destination) A :

$$\begin{aligned} T \uparrow A &= \{A \xrightarrow{n} A' \in T\} \\ T \downarrow A &= \{A' \xrightarrow{n} A \in T\} \end{aligned}$$

The projection rules are given in Figures 10 and 11.

Channels are used at communication points. If an expression e is send from A to A' through the channel $A \xrightarrow{n} A'$, then:

- for the projection on A , the communication involves sending a value: the resulting expression is void, and we add the definition of the channel c_n as the result of the projection of e on A (rule COMM-P-FROM);
- for the projection on A' , the communication involves receiving a value: the resulting expression is the channel holding this value (rule COMM-P-TO).

Finally, if A does not appear in the set of locations involved in its computation, then the expression can be suppressed on A (rule SUPPR-P). Projections of a pair consist in the projection of its compounds (rule PAIR-P).

The projection of a located declaration and parallel declarations involves the projection of its compound (rules AT-P and AND-P).

The projections of applications and node definitions involve adding to the inputs and outputs of the node, the channels used by this node (rules APP-P and NODE-P). Nodes with local architecture are assumed to be inlined. The relevance of the name order appears here, as the order of the added inputs and outputs must be consistent with every instances of these nodes, and for every projection.

Projection of a conditional is divided in two rules:

- one for the projection on a location where the conditional expression is computed: this first rule shows the definition of every channel needed to send this value to other locations where the conditional will be evaluated (rule IF-P-FROM);
- one for the projection on a location where the conditional expression has to be received: this expression is then replaced by the name of the channel holding this value (rule IF-P-TO).

Lemma 3 states that a program accepted by the spacial type system can always be projected on every location of the architecture.

Lemma 3. *For all H, H', D, ℓ, T , if $H|G \vdash D : H'/\ell/T$ then $\forall A \in \mathcal{S}, \exists D'$ s.t. $H|G \vdash D : H'/\ell/T \xrightarrow{A} D'$.*

Proof. Since the projection operation is type-directed, a projection rule can always be applied from the equivalent one of the type system. The property trivially holds. \square

The global meaning of a distributed program is then defined by the parallelization of its projected declarations.

$$\begin{array}{c}
\text{(IMM-P)} \\
H|G \vdash i : c \text{ at } s/\{s\}/\emptyset \xRightarrow{A} i/\epsilon
\end{array}
\quad
\begin{array}{c}
\text{(INST-P)} \\
\frac{(t, C) \leq (H(x)) \quad \mathcal{L} \models C}{H|G \vdash x : t/\ell/\emptyset \xRightarrow{A} x_A/\epsilon}
\end{array}
\quad
\begin{array}{c}
\text{(SUPPR-P)} \\
\frac{A \notin \ell}{H|G \vdash e : t/\ell/T \xRightarrow{A} ()/\epsilon}
\end{array}$$

$$\begin{array}{c}
\text{(COMM-P-FROM)} \\
\frac{H|\langle \mathcal{S}, \mathcal{L} \rangle \vdash e : t \text{ at } A/\ell/T \xRightarrow{A} e'/D \quad \mathcal{L} \models A \triangleright s'}{H|\langle \mathcal{S}, \mathcal{L} \rangle \vdash e : t \text{ at } s'/\ell \cup \{s'\}/T \uplus \{A \xrightarrow{n} s'\} \xRightarrow{A} ()/D \text{ and } c_n = e'}
\end{array}$$

$$\begin{array}{c}
\text{(COMM-P-TO)} \\
\frac{H|\langle \mathcal{S}, \mathcal{L} \rangle \vdash e : t \text{ at } s/\ell/T \xRightarrow{A'} e'/D \quad \mathcal{L} \models s \triangleright A'}{H|\langle \mathcal{S}, \mathcal{L} \rangle \vdash e : t \text{ at } A'/\ell \cup \{A'\}/T \uplus \{s \xrightarrow{n} A'\} \xRightarrow{A'} c_n/D}
\end{array}$$

$$\begin{array}{c}
\text{(PAIR-P)} \\
\frac{H|G \vdash e_1 : t_1/\ell_1/T_1 \xRightarrow{A} e'_1/D_1 \quad H|G \vdash e_2 : t_2/\ell_2/T_2 \xRightarrow{A} e'_2/D_2}{H|G \vdash e_1, e_2 : t_1 \times t_2/\ell_1 \cup \ell_2/T_1 \uplus T_2 \xRightarrow{A} e'_1, e'_2/D_1 \text{ and } D_2}
\end{array}$$

Figure 10: Rules for the projection operation (expressions)

(NODE-P)

$$\frac{H, x : t, H_1 | G \vdash D : H_1 / \ell_1 / T_1 \xrightarrow{A} D' \quad \begin{array}{l} H, x_t, H_1 | G \vdash e : t / \ell_2 / T_2 \xrightarrow{A} e' / D_e \quad T_1 \uplus T_2 \uparrow A = \{A \xrightarrow{n_1} A_1, \dots, A \xrightarrow{n_p} A_p\} \\ T_1 \uplus T_2 \downarrow A = \{A \xrightarrow{m_1} A_1, \dots, A \xrightarrow{m_q} A_q\} \quad n_i < n_{i+1} \quad m_i < m_{i+1} \end{array}}{H | G \vdash \text{node } f(x) = e \text{ with } D : [\text{gen}_H(t \dashv \ell_1 / T_1 \uplus T_2 \dashv t_1) / f] / \ell_1 \cup \ell_2 / \emptyset \xrightarrow{A} \text{node } f_A(x, c_{m_1}, \dots, c_{m_q}) = (e', c_{n_1}, \dots, c_{n_p}) \text{ with } D' \text{ and } D_e}$$

(AT-P) (DEF-P)

$$\frac{H \text{ at } A | G \vdash D : H' / \ell / T \xrightarrow{A} D'}{H | G \vdash D \text{ at } A : H' / \ell / T \xrightarrow{A} D'} \quad \frac{H | G \vdash e : t / \ell / T \xrightarrow{A} e' / D}{H | G \vdash x = e : [t/x] / \ell / T \xrightarrow{A} x_A = e' \text{ and } D}$$

(APP-P)

$$\frac{H | G \vdash f : t_1 \dashv \ell_1 / T_1 \dashv t_2 / \ell_2 / T_2 \xrightarrow{A} f' / D_1 \quad \begin{array}{l} H | G \vdash e : t_1 / \ell_3 / T_3 \xrightarrow{A} e' / D_2 \quad T'_1 \cong T_1 \quad T'_1 \uparrow A = \{A \xrightarrow{n_1} A_1, \dots, A \xrightarrow{n_p} A_p\} \\ T'_1 \downarrow A = \{A_1 \xrightarrow{m_1} A, \dots, A_q \xrightarrow{m_q} A\} \quad n_i < n_{i+1} \text{ and } m_i < m_{i+1} \end{array}}{H | G \vdash x = f(e) : [t_2/x] / \ell_1 \cup \ell_2 \cup \ell_3 / T'_1 \uplus T_2 \uplus T_3 \xrightarrow{A} (x, c_{m_1}, \dots, c_{m_q}) = f'(e', c_{n_1}, \dots, c_{n_p}) \text{ and } D_1 \text{ and } D_2}$$

(AND-P)

$$\frac{H | G \vdash D_1 : H_1 / \ell_1 / T_1 \xrightarrow{A} D'_1 \quad H | G \vdash D_2 : H_2 / \ell_2 / T_2 \xrightarrow{A} D'_2}{H | G \vdash D_1 \text{ and } D_2 : H_1, H_2 / \ell_1 \cup \ell_2 / T_1 \uplus T_2 \xrightarrow{A} D'_1 \text{ and } D'_2}$$

(IF-P-FROM)

$$\frac{\begin{array}{l} H | G \vdash e : c \text{ at } s / \ell / T \xrightarrow{A} e' / D \quad H | G \vdash D_1 : H' / \ell_1 / T_1 \xrightarrow{A} D'_1 \\ H | G \vdash D_2 : H' / \ell_2 / T_2 \xrightarrow{A} D'_2 \quad C = \{s \triangleright s' | s' \in \ell_1 \cup \ell_2\} \\ \mathcal{L} \models C \quad T' = \text{channels}(C) \quad T' \uparrow A = \{A \xrightarrow{n_1} A_1, \dots, A \xrightarrow{n_p} A_p\} \quad x \notin \text{dom}(H) \end{array}}{H | G \vdash \text{if } e \text{ then } D_1 \text{ else } D_2 : H' / \ell \cup \ell_1 \cup \ell_2 / T \uplus T_1 \uplus T_2 \uplus \text{channels}(C) \xrightarrow{A} x = e' \text{ and } c_{n_1} = x \text{ and } \dots \text{ and } c_{n_p} = x \text{ and if } x \text{ then } D'_1 \text{ else } D'_2}$$

(IF-P-TO)

$$\frac{\begin{array}{l} H | G \vdash e : c \text{ at } s / \ell / T \xrightarrow{A} e' / D \\ H | G \vdash D_1 : H' / \ell_1 / T_1 \xrightarrow{A} D'_1 \quad H | G \vdash D_2 : H' / \ell_2 / T_2 \xrightarrow{A} D'_2 \\ C = \{s \triangleright s' | s' \in \ell_1 \cup \ell_2\} \quad \mathcal{L} \models C \quad T' = \text{channels}(C) \quad T' \downarrow A = \{A' \xrightarrow{n} A\} \end{array}}{H | G \vdash \text{if } e \text{ then } D_1 \text{ else } D_2 : H' / \ell \cup \ell_1 \cup \ell_2 / T \uplus T_1 \uplus T_2 \uplus \text{channels}(C) \xrightarrow{A} \text{if } c_n \text{ then } D'_1 \text{ else } D'_2}$$

Figure 11: Rules for the projection operation (equations)

We note $\mathcal{S} = \{A_1, \dots, A_n\}$ the set of defined constant locations where the source declarations are projected. The global meaning of a declaration D , projected on the locations \mathcal{S} , is then defined by:

$$D_1 \text{ and } \dots \text{ and } D_n \\ \text{where } \forall i, H|G \vdash D : H'/\ell/T \xrightarrow{A_i} D_i$$

In order to relate a target program with its source, we define a relation on values, denoted $\cdot \preccurlyeq_t \cdot$, such that $v' \preccurlyeq_t^A v$ means that the value v' , emitted from an expression of type t , represents the value v at the location A . We have:

$$\frac{v' = v}{v' \preccurlyeq_t^A \text{ at } A v} \quad \frac{A \neq A'}{() \preccurlyeq_t^A \text{ at } A' v} \quad \frac{v'_1 \preccurlyeq_{t_1}^A v_1 \quad v'_2 \preccurlyeq_{t_2}^A v_2}{(v'_1, v'_2) \preccurlyeq_{t_1 \times t_2}^A (v_1, v_2)}$$

We can then relate two reaction environments R and R_p w.r.t. a typing environment H :

$$R \stackrel{H}{\cong} R_p \text{ iff } \forall x \in \text{dom}(R), \forall A \in \mathcal{S}, R(x) \preccurlyeq_{H(x)}^A R_p(x_A)$$

Theorem 3 states that the projection operation is correct, i.e., the projection of a source program D into D_i (for every location A_i) defines a new target program $D_1 \text{ and } \dots \text{ and } D_n$, which is semantically equivalent, taking into account spacial types' values, with the source declaration D .

Theorem 3 (Correction of the declarations projection). *For all $H, H', D, D', \ell, T, D_i, R, R'$, if $R \vdash D \xrightarrow{R'} D'$, $H|G \vdash D : H'/\ell/T$ and $\forall i, H|G \vdash D : H'/\ell/T \xrightarrow{A_i} D_i$, then there exists R_p, R'_p, D_p, D'_p such that $D_p = D_1 \text{ and } \dots \text{ and } D_n$, $R \stackrel{H}{\cong} R_p$, $R_p \vdash D_p \xrightarrow{R'_p} D'_p$, and $R' \stackrel{H'}{\cong} R'_p$.*

5 Discussion

5.1 Related Work

Various solutions have emerged in order to use synchronous languages for the design of distributed systems [12]. Some of them operate on a compiled model of the program, by “coloring” atomic instructions with localization informations, inferred from inputs and outputs locations [9]. The whole program is first *compiled* into an intermediate *sequential* format, on which the distribution is then applied. This format consists of a sequence of atomic instructions, representing one computation

step of new values carried on each stream. Then, the distribution involves placing each instruction onto one or several locations, taking into account the consistency of the control flow on each location. Another approach is to directly annotate the source program with *locations*, so as to define the localization of each variable of the program: the distribution is then performed with regard to these annotations. This approach has been applied to SIGNAL [4] as well as LUSTRE [8]. In both cases, the correction of the distribution algorithm has been proved [7, 15], meaning that the combined behavior of the distributed fragments has the same functional and temporal semantics as the initial centralized program. The originality of our method resides in the fact that we use a spacial type system to check the consistency of the distribution specifications inserted by the programmer, and that we perform *modular* distribution, allowing the expression of higher-order features, applied for instance to dynamic reconfiguration of nodes by application of other nodes as inputs. Since a Kahn semantics can be given to our language, the semantical equivalence between the source program and the synchronous product of the fragments resulting from the projection is sufficient to describe an asynchronous distribution. While the language presented has higher-order features, this method can easily be applied to other language with comparable semantics such as Lustre. In contrast, more general frameworks such as Signal cannot be addressed here for this reason.

Several approaches have been considered to solve the problem of data consistency of distributed programs. A translation operation is presented in [17], as well as an effect type system, in order to automatically obtain a multi-tier application from an annotated source program. Our proposition differs by the fact that our type system is not only a specification, but also consists in what the authors called “location analysis”, thus allowing us to perform this analysis in a modular way, and on a program comprising higher-order features. Type systems have been used to ensure memory consistency [19], or for pointer analysis within a distributed architecture [14]. The ACUTE language [18] is an extension of OCAML with typed marshalling. Communication channels between two ACUTE programs can also be considered as typed, as the type of marshalled and unmarshalled data are dynamically verified, at execution time. The consistency considered is between separately-built programs, whereas our approach is to consider the programming of a distributed system as one global program, allowing global static verifications. Finally, Oz and its distributed extension [5] proposes a way to separate the functionality of a distributed application and its distribution structure, by allowing the programmer to give a different distributed semantics to every different object of a program. This language aims at loosely coupled distributed systems without architecture constraints, whereas our approach

concerns strongly coupled architecture, and we aim to ensure the consistency of the distribution w.r.t. one architecture, given the communication constraints.

5.2 Conclusion and Future Work

We have proposed a spacial type system to solve the problem of automatic distribution of dataflow programs. It is based on a core dataflow language, which we have extended with distribution primitives to allow the programmer to specify, on one hand his/her target distributed architecture, and on the other hand where some nodes and/or variables are to be located. The underlying philosophy is the functional distribution, meaning that some functionalities of the program must be computed at some precise location because they require some specific sensors, actuators, and/or computing resources that are available only at this location. In this context, we use type inference to decide at which location each node must be computed, and at which points in the program communication primitives must be inserted. The compilation of a correctly spacially typed program produces one program for each computing location specified by the programmer. We use abstract communication channels to exchange a value between two locations and to synchronize them. Compiling each program and linking with a dedicated library implementing those communication channels then gives one binary code for each location. The refined version of the type system, as well as the operation projection, has been implemented in the compiler of LUCID SYNCHRONE [1].

Future work mainly involves allowing the description of more complex architectures, with hierarchical locations or communication masking (e.g., MPSoCs): our proposal of architecture constraints is not sufficient to catch the complexity of actual architecture of distributed embedded systems. Yet, our current constraints show the interest of a type system for checking the consistency of a distributed program w.r.t. such constraints.

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References

- [1] Lucid synchronone v3. <http://www.lri.fr/~pouzet/lucid-synchronone>.
- [2] Architecture analysis & design language (AADL). SAE Standard (AS5506), Nov. 2004.

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- [3] C. André, F. Boulanger, and A. Girault. Software implementation of synchronous programs. In *International Conference on Application of Concurrency to System Design, ICACSD'01*, pages 133–142, Newcastle, UK, June 2001. IEEE.
 - [4] P. Aubry and P. Le Guernic. On the desynchronization of synchronous applications. In *11th International Conference on Systems Engineering, ICSE'96*, Las Vegas, USA, June 1996.
 - [5] H. E. Bal, J. G. Steiner, and A. S. Tanenbaum. Programming languages for distributed computing systems. *ACM Computing Surveys*, 21(3):261–322, 1989.
 - [6] A. Benveniste, P. Caspi, S. Edwards, N. Halbwachs, P. L. Guernic, and R. de Simone. The synchronous languages twelve years later. *Proc. of the IEEE, Special issue on embedded systems*, 91(1):64–83, Jan. 2003.
 - [7] B. Caillaud, P. Caspi, A. Girault, and C. Jard. Distributing automata for asynchronous networks of processors. *European Journal of Automated Systems*, 31(3):503–524, 1997. Research report Inria 2341.
 - [8] P. Caspi, A. Curic, A. Maignan, C. Sofronis, S. Tripakis, and P. Niebert. From Simulink to Scade/Lustre to TTA: A layered approach for distributed embedded applications. In *International Conference on Languages, Compilers, and Tools for Embedded Systems, LCTES'03*, pages 153–162, San Diego, USA, June 2003. ACM.
 - [9] P. Caspi, A. Girault, and D. Pilaud. Automatic distribution of reactive systems for asynchronous networks of processors. *IEEE Trans. on Software Engineering*, 25(3):416–427, May 1999.
 - [10] J.-L. Colaço, A. Girault, G. Hamon, and M. Pouzet. Towards a Higher-order Synchronous Data-flow Language. In *ACM Fourth International Conference on Embedded Software (EMSOFT'04)*, Pisa, Italy, september 2004.
 - [11] P. Cuoq and M. Pouzet. Modular Causality in a Synchronous Stream Language. In *European Symposium on Programming (ESOP'01)*, Genova, Italy, April 2001.
 - [12] A. Girault. A survey of automatic distribution method for synchronous programs. In F. Maraninchi, M. Pouzet, and V. Roy, editors, *International Workshop on Synchronous Languages, Applications and Programs, SLAP'05*, ENTCS, Edinburgh, UK, Apr. 2005. Elsevier Science.

- [13] F. Jondral. Software-defined radio — basics and evolution to cognitive radio. *EURASIP Journal on Wireless Communications and Networking*, 3:275–283, 2005.
- [14] B. Liblit and A. Aiken. Type systems for distributed data structures. In *POPL '00: Proceedings of the 27th ACM SIGPLAN-SIGACT symposium on Principles of programming languages*, pages 199–213, New York, NY, USA, 2000. ACM Press.
- [15] O. Maffeïs. *Ordonnancements de graphes de flots synchrones ; Application à la mise en œuvre de Signal*. Phd thesis, University of Rennes I, Rennes, France, Jan. 1993.
- [16] J. Mitola. The software radio architecture. *IEEE Communications Magazine*, 33(5):26–38, May 1995.
- [17] M. Neubauer and P. Thiemann. From sequential programs to multi-tier applications by program transformation. In *POPL '05: Proceedings of the 32nd ACM SIGPLAN-SIGACT symposium on Principles of programming languages*, pages 221–232, New York, NY, USA, 2005. ACM Press.
- [18] P. Sewell, J. J. Leifer, K. Wansbrough, F. Z. Nardelli, M. Allen-Williams, P. Habouzit, and V. Vafeiadis. Acute: High-level programming language design for distributed computation. In *Proceedings of ICFP 2005: International Conference on Functional Programming (Tallinn)*, Sept. 2005.
- [19] J.-P. Talpin and P. Jouvelot. Polymorphic type, region and effect inference. *Journal of Functional Programming*, 2(3), 1992.

A Proofs

A.1 Distributed and centralized semantics

(*Lemma 2*). The proof lies in the extension of the $|\cdot|$ operator to the predicates $\hat{R} \stackrel{\ell}{\Vdash} e \xrightarrow{\hat{v}} e'$ and $\hat{R} \stackrel{\ell}{\Vdash} D \xrightarrow{\hat{R}'} D'$. For each rule of the distributed semantics, we can replace the predicate $\hat{R} \stackrel{\ell}{\Vdash} e \xrightarrow{\hat{v}} e'$ by $|\hat{R}| \vdash e \xrightarrow{|\hat{v}|} e'$ and $\hat{R} \stackrel{\ell}{\Vdash} D \xrightarrow{\hat{R}'} D'$ by $|\hat{R}| \vdash D \xrightarrow{|\hat{R}'|} D'$ to obtain an equivalent centralized semantics. \square

A.2 Type system correction

The proof of the theorem 1 is based upon the following lemmas.

The first states that the set of locations needed for the computation of an expression typed from a typing environment located on A is the singleton $\{A\}$.

Lemma 4. *For all H, A, G, D, H', ℓ , if $H \text{ at } A | G \vdash D : H' / \ell$ then $\ell = \{A\}$.*

Proof. From the definition of the instantiation, for all H, A, x, C, t such that $(t, C) \leq (H \text{ at } A)(x)$, $\exists t'$, such that $t = t' \text{ at } A$. Thus $\text{locations}(t) = \{A\}$.

The lemma is then proved by induction on the structure of e . The other rules only perform union of location sets from the spacial types of compounds of e , thus the property holds. \square

The following lemma is the equivalent of the theorem 1 for expressions:

Lemma 5 (Correction for expressions). *For all $e, e', H, G, t, \ell, R, v$, if $H | G \vdash e : t / \ell$ and $R \vdash e \xrightarrow{v} e'$, then there exists \hat{R}, \hat{v} such that $\hat{R} \Vdash e \xrightarrow{\hat{v}} e'$, $\hat{R} : H$, $\hat{v} : t$, $|\hat{R}| = R$ and $|\hat{v}| = v$.*

Proof. By induction on the structure of the typing derivation tree of e . For each applied rule of the type system, we can apply a matching rule of the distributed semantics.

The induction operates on the last applied rule of the type system.

Case of rule IMM: $e = i$. Let s such that $H | G \vdash i : c \text{ at } s / \{s\}$. For any R, \hat{R} such that $|\hat{R}| = R$, $R \vdash i \xrightarrow{i} i \Rightarrow \hat{R} \Vdash i \xrightarrow{i \text{ at } s} i$ and $i \text{ at } s : c \text{ at } s$.

Case of rule INST: $e = x$. Let $H, \mathcal{L}, \mathcal{S}, t, \ell$ such that $H | \langle \mathcal{L}, \mathcal{S} \rangle \vdash e : t / \ell$. Let R, v, e' such that $R \vdash e \xrightarrow{v} e'$.

- Case $|\ell| > 1$. For all \hat{R} such that $|\hat{R}| = R$ and $\hat{R} : H$, then there exists dv such that $\hat{R}(x) = dv \text{ at } \top$. Then the rule INST applies: $\hat{R}, [dv \text{ at } \top / x] \Vdash x \xrightarrow{dv \text{ at } \top} x$. Moreover, $|dv \text{ at } \top| = v$ since $|\hat{R}| = R$, and $dv \text{ at } \top : t$ since $\hat{R} : H$.
- Case $|\ell| = 1$ ($\ell = \{s\} = \text{locations}(t)$). For all \hat{R} such that $|\hat{R}| = R$ and $\hat{R} : H$, then there exists dv such that $\hat{R}(x) = dv \text{ at } s$. The rule INST applies: $\hat{R}, [dv \text{ at } s / x] \Vdash x \xrightarrow{dv \text{ at } s} x$.

Case of rule PAIR: $e = (e_1, e_2)$.

- $H|G \vdash e : t/\ell$ iff $\exists \ell_1, \ell_2, t_1, t_2$ such that $H|G \vdash e_1 : t_1/\ell_1$, $H|G \vdash e_2 : t_2/\ell_2$, $t = t_1 \times t_2$ and $\ell = \ell_1 \cup \ell_2$.
- $R \vdash (e_1, e_2) \xrightarrow{v} e'$ iff $\exists v_1, v_2, e'_1, e'_2$ such that $v = (v_1, v_2)$, $e' = (e'_1, e'_2)$, $R \vdash e_1 \xrightarrow{v_1} e'_1$ and $R \vdash e_2 \xrightarrow{v_2} e'_2$.
- By induction hypothesis, there exists $\hat{R}, \hat{v}_1, \hat{v}_2$ such that $\hat{R} \Vdash^{\ell_1} e_1 \xrightarrow{\hat{v}_1} e'_1$, $\hat{R} \Vdash^{\ell_2} e_2 \xrightarrow{\hat{v}_2} e'_2$, $\hat{v}_1 : t_1$ and $\hat{v}_2 : t_2$. Let $\hat{v}_1 = dv_1$ at s_1 and $\hat{v}_2 = dv_2$ at s_2 .
- Then, rule PAIR applies: $\hat{R} \Vdash^{\ell_1 \cup \ell_2} (e_1, e_2) \xrightarrow{(\hat{v}_1, \hat{v}_2) \text{ at } s_1 \sqcup s_2} (e'_1, e'_2)$, $\overline{\ell_1 \cup \ell_2} = s_1 \sqcup s_2$ and $((\hat{v}_1, \hat{v}_2) \text{ at } s_1 \sqcup s_2) : (t_1 \times t_2)$.

Case of rule FST: $e = \mathbf{fst} e_1$.

- From the definition of H_0 , $H|G \vdash e : t/\ell$ iff $\exists s_1, s_2, t_1, t_2$ such that $H|G \vdash e_1 : (t_1 \text{ at } s_1, t_2 \text{ at } s_2) / \{s_1, s_2\}$.
- From rule FST of the centralized semantics, $R \vdash \mathbf{fst} e_1 \xrightarrow{v_1} \mathbf{fst} e'_1$ iff $\exists v_2, R \vdash e_1 \xrightarrow{(v_1, v_2)} e'_1$.
- By induction hypothesis, there exists \hat{R}, \hat{v} such that $\hat{R} \Vdash^{\{s_1, s_2\}} e_1 \xrightarrow{\hat{v}} e'_1$. $|\hat{v}| = (v_1, v_2)$ then $\exists \hat{v}_1, \hat{v}_2, s'$ such that $\hat{v} = (\hat{v}_1, \hat{v}_2) \text{ at } s'$.
- Applying rule FST: $\hat{R} \Vdash^{\{s_1, s_2\}} \mathbf{fst} e_1 \xrightarrow{\hat{v}_1} \mathbf{fst} e'_1$. $\hat{v} : t_1 \times t_2$, then $\hat{v}_1 : t_1$.

Case of rule SND (same as rule FST): $e = \mathbf{snd} e_1$.

- From the definition of H_0 , $H|G \vdash e : t/\ell$ iff $\exists s_1, s_2, t_1, t_2$ such that $H|G \vdash e_1 : (t_1 \text{ at } s_1, t_2 \text{ at } s_2) / \{s_1, s_2\}$.
- From rule SND of the centralized semantics, $R \vdash \mathbf{snd} e_2 \xrightarrow{v_2} \mathbf{snd} e'_2$ iff $\exists v_1, R \vdash e_2 \xrightarrow{(v_1, v_2)} e'_2$.
- By induction hypothesis, there exists \hat{R}, \hat{v}, s such that $\hat{R} \Vdash^{\{s_1, s_2\}} e_2 \xrightarrow{\hat{v}} e'_2$. $|\hat{v}| = (v_1, v_2)$ then $\exists \hat{v}_1, \hat{v}_2, s'$ such that $\hat{v} = (\hat{v}_1, \hat{v}_2) \text{ at } s'$.
- Applying rule SND: $\hat{R} \Vdash^{\{s_1, s_2\}} \mathbf{snd} e_1 \xrightarrow{\hat{v}_1} \mathbf{snd} e'_1$. $\hat{v} : t_1 \times t_2$, then $\hat{v}_2 : t_2$.

Case of rule FBY: $e = e_1 \text{ fby } e_2$.

- From the definition of H_0 , $H|G \vdash e : t/\ell$ iff $H|G \vdash e_1 : t/\ell$ and $H|G \vdash e_2 : t/\ell$.
- From rule FBY of the centralized semantics, $R \vdash e_1 \text{ fby } e_2 \xrightarrow{v} e$ iff $\exists v_1, v_2, e'_1, e'_2$ such that $v = v_1$, $e' = v_2 \text{ fby } e'_2$, $R \vdash e_1 \xrightarrow{v_1} e'_1$ and $R \vdash e_2 \xrightarrow{v_2} e'_2$.
- By induction hypothesis, there exists $\hat{R}, \hat{v}_1, \hat{v}_2$ such that $\hat{R} \stackrel{\ell}{\Vdash} e_1 \xrightarrow{\hat{v}_1} e'_1$, $\hat{R} \stackrel{\ell}{\Vdash} e_2 \xrightarrow{\hat{v}_2} e'_2$, $|\hat{v}_1| = v_1$, $|\hat{v}_2| = v_2$, $\hat{v}_1 : t$ and $\hat{v}_2 : t$.
- Applying rule FBY: $\hat{R} \stackrel{\ell}{\Vdash} e_1 \text{ fby } e_2 \xrightarrow{\hat{v}_1} |\hat{v}_2| \text{ fby } e'_2$.

Case of rule AT: $e = e_1 \text{ at } s$.

- $H|G \vdash e_1 \text{ at } s : t/\ell$ iff $H \text{ at } s|G \vdash e_1 : t/\ell$.
- $R \vdash e_1 \text{ at } s \xrightarrow{v} e'$ iff $\exists e'_1, e' = e'_1 \text{ at } s$ and $R \vdash e_1 \xrightarrow{v} e'_1$.
- By induction hypothesis, there exists \hat{R}, \hat{v}, s such that $\hat{R} \stackrel{\ell}{\Vdash} e_1 \xrightarrow{\hat{v}} e'_1$, $|\hat{R}| = R$, $|\hat{v}| = v$, $\hat{R} : H$, $\hat{v} : t$.
- From lemma 4, $\ell = \{s\}$.
- Applying rule AT: $\hat{R} \stackrel{\{s\}}{\Vdash} e_1 \text{ at } s \xrightarrow{\hat{v}} e'_1 \text{ at } s$.

Case of rule COMM.

- $H|\langle \mathcal{S}, \mathcal{L} \rangle \vdash e : t \text{ at } s'/\ell$ iff there exists ℓ' such that $\ell = \ell' \cup \{s\}$, $H|\langle \mathcal{S}, \mathcal{L} \rangle \vdash e : t \text{ at } s'/\ell'$ and $\mathcal{L} \models s \triangleright s'$.
- $R \vdash e \xrightarrow{v} e'$.
- By induction hypothesis, there exists \hat{R}, \hat{v}, s such that $\hat{R} \stackrel{\ell}{\Vdash} e_1 \xrightarrow{\hat{v}} e'_1$, $|\hat{R}| = R$, $|\hat{v}| = v$, $\hat{R} : H$, $\hat{v} : t \text{ at } s$.
- Applying rule COMM: $\hat{R} \stackrel{\ell \cup \{s\}}{\Vdash} e_1 \text{ at } s \xrightarrow{\hat{v}} e'_1 \text{ at } s$.

□

(Theorem 1). By induction on the structure of D .

Case $D = (x = e)$.

- $H|G \vdash x = e : H'/\ell$ iff $\exists s, t, H' = [\forall \delta : \{s \triangleright \delta\}.t \text{ at } \delta/x]$ and $H|G \vdash e : t \text{ at } s/\ell$.
- $R \vdash x = e \xrightarrow{R'} D'$ iff $\exists e', v, D' = (x = e'), R' = [v/x]$, and $R \vdash e \xrightarrow{v} e'$.
- By application of lemma 5, $\exists \hat{R}, \hat{v}$ such that $\hat{R} \Vdash^\ell e \xrightarrow{\hat{v}} e', |\hat{R}| = R, \hat{R} : H, |\hat{v}| = v, \hat{v} : t \text{ at } s$.
- Applying rule DEF: $\hat{R} \Vdash^\ell x = e \xrightarrow{[\hat{v}/x]} x = e'. ||[\hat{v}/x]| = [v/x] = R, [\hat{v}/x] : [\forall \delta : \{s \triangleright \delta\}.t \text{ at } \delta/x]$ since $\hat{v} : t \text{ at } s$.

Case $D = (x = f(e))$.

- $H|G \vdash x = f(e) : H'/\ell$ iff $\exists s, t_1, t_2, \ell_1, \ell_2, \ell_3$ such that $\ell = \ell_1 \cup \ell_2 \cup \ell_3, H' = [t_2/x], H|G \vdash f : t_1 \dashv\langle \ell_1 \rangle \rightarrow t_2/\ell_2$ and $H|G \vdash e : t_1/\ell_3$.
- $R \vdash x = f(e) \xrightarrow{R'} D'$ iff $\exists e_1, D_1$ such that $R(f) = \lambda y.e_1$ with D_1 and $R \vdash x = e_1$ and $y = e$ and $D_1 \xrightarrow{R'} D'$.
- $H|G \vdash f : t_1 \dashv\langle \ell_1 \rangle \rightarrow t_2/\ell_2$ iff $\exists H_1, \ell_{11}, \ell_{12}$ such that $\ell_1 = \ell_{11} \cup \ell_{12}, H.[y : t_1]|G \vdash D_1 : H_1/\ell_{11}$ and $H.[y : t_1].H_1|G \vdash e_1 : t_2/\ell_{12}$.
- By induction hypothesis, $\exists \hat{R}, \hat{R}'$ such that $\hat{R} \Vdash^\ell x = e_1$ and $y = e$ and $D_1 \xrightarrow{\hat{R}'} D', |\hat{R}| = R, |\hat{R}'| = \hat{R}', \hat{R} : H, \hat{R}' : H'$.
- Applying rule APP: $\hat{R} \Vdash^\ell x = f(e) \xrightarrow{\hat{R}'} D'$.
- $\hat{R} \Vdash^\ell x = e_1$ and $y = e$ and $D_1 \xrightarrow{\hat{R}'} D'$ iff there exists $\hat{R}_{11}, \hat{v}_{12}, \hat{v}_3, e', D'_1, e'_1$ such that
 - $\hat{R}.[\hat{v}_{12}/x, \hat{v}_3/y] \Vdash^{\ell_{12}} D_1 \xrightarrow{\hat{R}_{11}} D'_1,$
 - $\hat{R}.\hat{R}_{11}.[\hat{v}_3/y] \Vdash^{\ell_{12}} x = e_1 \xrightarrow{[\hat{v}_{12}/x]} x = e'_1,$
 - $\hat{R}.\hat{R}_{11}.[\hat{v}_{12}/x] \Vdash^{\ell_3} y = e \xrightarrow{[\hat{v}_3/y]} y = e'.$
- By induction, $\ell'_{11} = \ell_{11}, \ell'_{12} = \ell_{12}$, and $\ell'_3 = \ell_3$. Then $\ell' = \ell_1 \cup \ell_3$.
- From rule INST of spacial type system, $H|G \vdash f : t_1 \dashv\langle \ell_1 \rangle \rightarrow t_2/\ell_2$ iff $\ell_2 = \text{locations}(t_1 \dashv\langle \ell_1 \rangle \rightarrow t_2) = \ell_1$.

- Then, $\ell = \ell_1 \cup \ell_3 = \ell'$.

Case $D = D_1$ and D_2 .

- $H|G \vdash D_1$ and $D_2 : H'/\ell$ iff $\exists H_1, H_2, \ell_1, \ell_2$ such that $\ell = \ell_1 \cup \ell_2$, $H|G \vdash D_1 : H_1/\ell_1$ and $H|G \vdash D_2 : H_2/\ell_2$.
- $R \vdash D_1$ and $D_2 \xrightarrow{R'} D'$ iff $\exists R_1, R_2, D'_1, D'_2$ such that $R.R_2 \vdash D_1 \xrightarrow{R_1} D'_1$ and $R.R_1 \vdash D_2 \xrightarrow{R_2} D'_2$.
- By induction hypothesis, $\exists \hat{R}, \hat{R}_1, \hat{R}_2$ such that
 - $\hat{R}.\hat{R}_2 \stackrel{\ell_1}{\Vdash} D_1 \xrightarrow{\hat{R}_1} D'_1$,
 - $\hat{R}.\hat{R}_1 \stackrel{\ell_2}{\Vdash} D_2 \xrightarrow{\hat{R}_2} D'_2$,
 - $|\hat{R}| = R, |\hat{R}_1| = R_1, |\hat{R}_2| = R_2$,
 - $\hat{R} : H, \hat{R}_1 : H_1, \hat{R}_2 : H_2$.

- Applying rule AND: $\hat{R} \stackrel{\ell_1 \cup \ell_2}{\Vdash} D_1$ and $D_2 \xrightarrow{\hat{R}_1.\hat{R}_2} D'_1$ and D'_2 . $(\hat{R}_1.\hat{R}_2) : (H_1.H_2)$ and $|\hat{R}_1.\hat{R}_2| = R_1.R_2$.

Case $D = \text{if } e \text{ then } D_1 \text{ else } D_2$.

- $H|G \vdash \text{if } e \text{ then } D_1 \text{ else } D_2 : H'/\ell$ iff there exists s, ℓ', ℓ_1, ℓ_2 such that $\ell = \ell' \cup \ell_1 \cup \ell_2$, $H|G \vdash e : c$ at s/ℓ' , $H|G \vdash D_1 : H'/\ell_1$, $H|G \vdash D_2 : H'/\ell_2$ and $\forall s' \in \ell_1 \cup \ell_2, s \triangleright s'$.
- $R \vdash \text{if } e \text{ then } D_1 \text{ else } D_2 \xrightarrow{R'} D'$ iff there exists e' such that $R \vdash e \xrightarrow{v} e'$, with $v = \text{true}$ or $v = \text{false}$.
 - Case $v = \text{true}$: there exists D'_1 such that $R \vdash D_1 \xrightarrow{R'} D'_1$ and $D' = \text{if } e' \text{ then } D'_1 \text{ else } D_2$.
 - * By induction hypothesis, $\exists \hat{R}, \hat{R}'$ such that $\hat{R} \stackrel{\ell'}{\Vdash} e \xrightarrow{\text{true at } s} e'$, $\hat{R} \stackrel{\ell_1}{\Vdash} D_1 \xrightarrow{\hat{R}'} D'_1$, $|\hat{R}| = R, \hat{R} : H, |\hat{R}'| = R', \hat{R}' : H'$.
 - * Applying rule IF-1: $\hat{R} \stackrel{\ell' \cup \ell_1}{\Vdash} \text{if } e \text{ then } D_1 \text{ else } D_2 \xrightarrow{\hat{R}'} \text{if } e' \text{ then } D'_1 \text{ else } D'_2$.
 - Case $v = \text{false}$: same as case $v = \text{true}$.

□

A.3 Projection correction

The proof of the theorem 3 is based on the following lemma, stating that if the projection of an expression e on every location A_i is the couple e_i/D_i , then the semantical relation between e and e_i is established by evaluating e_i in a reaction environment emitted by D_1 and \dots and D_n . This equivalence relation depends upon the spacial type of e .

Lemma 6 (Projection correction (expressions)). *For all $H, e, v, e', t, \ell, T, e_i, D_i, R$, if $R \vdash e \xrightarrow{v} e'$, $H|G \vdash e : t/\ell/T$ and $\forall i, H|G \vdash e : t/\ell/T \xrightarrow{A_i} e_i/D_i$, then for all R_p such that $R \cong^H R_p$, there exists $v_i, e'_i, D_p, D'_p, R'_p$ such that $D_p = D_1$ and \dots and D_n , $R_p \vdash D_p \xrightarrow{R'_p} D'_p$, $\text{dom}(R'_p) = \{c_n | \exists A, A', A \xrightarrow{n} A' \in T\}$. $R_p, R'_p \vdash e_i \xrightarrow{v_i} e'_i$ and $v_i \preceq_t^{A_i} v$.*

Proof. By induction on the structure of the typing derivation tree of e .

Let $H, e, v, e', t, \ell, T, e_i, D_i, R, R_p$, such $R \vdash e \xrightarrow{v} e'$, $H|G \vdash e : t/\ell/T$ and $\forall i, H|G \vdash e : t/\ell/T \xrightarrow{A_i} e_i/D_i$

Case of rule IMM: $e = i$. Then, $R \vdash i \xrightarrow{i} i$. For all H, G, s, A , $H|G \vdash i : c \text{ at } s/\{s\}/\emptyset \xrightarrow{A} i/\epsilon$. Let $D_p = \epsilon$ and $R'_p = \emptyset$. For all R_p , $R \vdash i \xrightarrow{i} i$ and $i \preceq_c^A \text{ at } s i$.

Case of rule INST: $e = x$. Then, $R \vdash x \xrightarrow{v} x$. $H|G \vdash x : t/\ell/T$, then, $\exists C, \mathcal{S}, \mathcal{L}$ such that $G = \langle \mathcal{S}, \mathcal{L} \rangle$, $(t, C) \leq H(x)$ and $\mathcal{L} \models C$. Then the rule INST-P applies: for all A , $H|G \vdash x : t/\ell \xrightarrow{A} x_A$. Let $D_p = \epsilon$, $R'_p = \emptyset$ and $v_A = R_p(x_A)$. $R_p \vdash x_A \xrightarrow{v_A} x_A$, and $v_A \preceq_t^A v$ since $R \cong^H R_p$.

Case of rule PAIR: $e = (e_1, e_2)$. By induction, and the fact that $H|G \vdash e : t/\ell/T$ iff $\exists \ell_1, \ell_2, T_1, T_2, t_1, t_2$ such that $\ell = \ell_1 \cup \ell_2$, $T = T_1 \uplus T_2$, $t = t_1 \times t_2$, $H|G \vdash e_1 : t_1/\ell_1/T_1$ and $H|G \vdash e_2 : t_2/\ell_2/T_2$. Then for a site A , $H|G \vdash e_1 : t_1/\ell_1/T_1 \xrightarrow{A} e'_1/D_1$ and $H|G \vdash e_2 : t_2/\ell_2/T_2 \xrightarrow{A} e'_2/D_2$, there exists R_p, R'_{p1}, R'_{p2} , such that $R_p \vdash D_1 \xrightarrow{R'_{p21}} D'_1$, $R_p \vdash D_2 \xrightarrow{R'_{p22}} D'_2$, and $\text{dom}(R'_{p1}) \cap \text{dom}(R'_{p2}) = \emptyset$. Then, taking $R'_p = R'_{p1}.R'_{p2}$, $R_p \vdash D_1$ and $D_2 \xrightarrow{R'_p} D'_1$ and D'_2 , and there exists v such that $R_p, R'_p \vdash (e_1, e_2) \xrightarrow{v} (e'_1, e'_2)$.

Case of rules FST and SND: $e = \text{fst } e'$ and $e = \text{snd } e'$: direct induction.

Case of rule COMM: $H|G \vdash e : t/\ell/T$ iff $\exists t', A, A', T', n$ such that the architecture allows the communication from A to A' , $t = t' \text{ at } A'$, $T = T' \uplus \{A \xrightarrow{n} A'\}$, and $H|G \vdash e : t' \text{ at } A_1/\ell/T$.

By induction hypothesis, for every location A_i , there exists e_i, D_i such that $H|G \vdash e : t' \text{ at } A/\ell/T' \xrightarrow{A_i} e_i/D_i$. Let $D'_p = D_1$ and \dots and D_n, D''_p and R''_p such that $R'_p \vdash D'_p \xrightarrow{R''_p} D''_p$.

For each i , let v_i, e'_i such that $R_p, R'_p \vdash e_i \xrightarrow{v_i} e'_i$.

- If $A_i = A$, then by induction hypothesis, $v_i \preceq_{t'}^{A_i} \text{ at } A v$. By application of the rule COMM-FROM, $H|G \vdash e : t' \text{ at } A'/\ell \cup \{A'\}/T \uplus \{A \xrightarrow{n} A'\} \xrightarrow{A_i} ()/D_i$ and $c_n = e_i$. Then, as $A_i \neq A'$, $() \preceq_{t'}^{A_i} \text{ at } A' v$.
- If $A_i = A'$, then by application of the rule COMM-TO, $H|G \vdash e : t' \text{ at } A'/\ell \cup \{A'\}/T \uplus \{A \xrightarrow{n} A'\} \xrightarrow{A_i} c_n/D_i$. From rule COMM-FROM, $\exists v_p, e_p$ such that $D_p = D'_p$ and $c_n = e_p$,

□

Theorem 3. By induction on the structure of D .

Case $D = D_1 \text{ at } A$. Direct induction.

Case $D = (x = e)$.

- $R \vdash D \xrightarrow{R'} D'$ iff there exists $e', v, D' = (x = e')$, $R' = [v/x]$, and $R \vdash e \xrightarrow{v} e'$.
- $H|G \vdash x = e : H'/\ell$ iff $\exists s, t, H' = [\forall \delta : \{s \triangleright \delta\}.t \text{ at } \delta/x]$ and $H|G \vdash e : t \text{ at } s/\ell$.
- Let $\{A_1, \dots, A_n\} = \mathcal{S}$. For all i , $H|G \vdash D : H'/\ell/T \xrightarrow{A_i} D_i$ iff there exists e'_i, D'_i such that $H|G \vdash e : t \text{ at } s/\ell/T \xrightarrow{A_i} e_i/D'_i$.
- Let R_p such that $R_p \stackrel{H}{\cong} R$. By application of lemma 6, there exists $v_i, e'_i, D_e, D'_e, R'_e$, $D_e = D_1$ and \dots and $D_n, R_p \vdash D_e \xrightarrow{R'_e} D'_e, R_p, R'_e \vdash e_i \xrightarrow{v_i} e'_i$ and $v_i \preceq_t^{A_i} \text{ at } s v$.
- Then, by application of rule DEF-P, taking $D_i = (x_{A_i} = e_i)$ and D'_i , and $D_p = D_1$ and \dots and D_n and $R'_p = R'_e.[v_1/x_{A_1}, \dots, v_n/x_{A_n}]$, $R_p \vdash D_p \xrightarrow{R'_p} D'_p$.

Case $D = (x = f(e))$.

- $R \vdash D \xrightarrow{R'} D'$ iff there exists e_1, D_1 such that $R(f) = \lambda y.e_1$ with D_1 and $R \vdash x = e_1$ and $y = e$ and $D_1 \xrightarrow{R'} D'$.
- $H|G \vdash D : H'/\ell/T$ iff $\exists t_1, t_2, \ell_1, \ell_2, \ell_3, T_1, T'_1, T_2, T_3$, such that:

- $\ell = \ell_1 \cup \ell_2 \cup \ell_3$,
- $T = T'_1 \uplus T_2 \uplus T_3$,
- $T_1 \cong T'_1$,
- $H|G \vdash f : t_1 \text{--}(\ell_1/T_1) \text{--} t_2/\ell_2/T_2$,
- $H|G \vdash e : t_1/\ell_3/T_3$,
- $H' = [t_2/x]$.

• Let $\{A_1, \dots, A_n\} = \mathcal{S}$. For all i ,

- $H|G \vdash D : H'/\ell/T \xrightarrow{A_i} D_i$ iff there exists $f', e', D_2, D_3, c_{m_1}, \dots, c_{m_q}, c_{n_1}, \dots, c_{n_p}$ such that
 - * $T'_1 \uparrow A_i = \{A \xrightarrow{n_1} A_{11}, \dots, A_i \xrightarrow{n_p} A_{1p}\}$,
 - * $T'_1 \downarrow A_i = \{A_{21} \xrightarrow{m_1} A, \dots, A_{2q} \xrightarrow{m_q} A_i\}$,
 - * $H|G \vdash f : t_1 \text{--}(\ell_1/T_1) \text{--} t_2/\ell_2/T_2 \xrightarrow{A_i} f'/D_2$,
 - * $H|G \vdash e : t_1/\ell_3/T_3 \xrightarrow{A_i} e'/D_3$,
 - * $D_i = (x, c_{m_1}, \dots, c_{m_q}) = f'(e', c_{n_1}, \dots, c_{n_p})$ and D_2 and D_3 .
- From rules INST-P-HERE and NODE-P, $f' = f_{A_i}$ and f_{A_i} is defined by

$$\text{node } f_{A_i}(x, c_{n'_1}, \dots, c_{n'_p}) = (e'_1, c_{m'_1}, \dots, c_{m'_q}) \text{ with } D'_1$$

Where:

- * $T_1 \uparrow A_i = \{A \xrightarrow{n'_1} A_{11}, \dots, A_i \xrightarrow{n'_p} A_{1p}\}$,
- * $T_1 \downarrow A_i = \{A_{21} \xrightarrow{m'_1} A, \dots, A_{2q} \xrightarrow{m'_q} A_i\}$,
- By application of lemma 6, there exists $v_i, e'_i, D_e, D'_e, R'_e, D_e = D_1$ and \dots and $D_n, R_p \vdash D_e \xrightarrow{R'_e} D'_e, R_p, R'_e \vdash e_i \xrightarrow{v_i} e'_i$ and $v_i \preceq_t^{A_i} \text{ at } s \ v$.
- From the fact that the renamings of channels are monotone, the application of the semantics on each projection keeps the consistency of the communicated values between each projected node and its projected instantiation.

□



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