



HAL
open science

Hardware Resource Control in L4 micro-kernels

Goichon François, Stéphane Frénot, Guillaume Salagnac

► **To cite this version:**

Goichon François, Stéphane Frénot, Guillaume Salagnac. Hardware Resource Control in L4 micro-kernels. Conférence Française en Systèmes d'Exploitation (CFSE 2011), May 2011, Saint-Malo, France. hal-00648488

HAL Id: hal-00648488

<https://inria.hal.science/hal-00648488>

Submitted on 5 Dec 2011

HAL is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers.

L'archive ouverte pluridisciplinaire **HAL**, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d'enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.

Hardware Resource Control in L4 μ -kernels



François Goichon, Guillaume Salagnac, Stéphane Frénot
University of Lyon, INRIA

Motivation

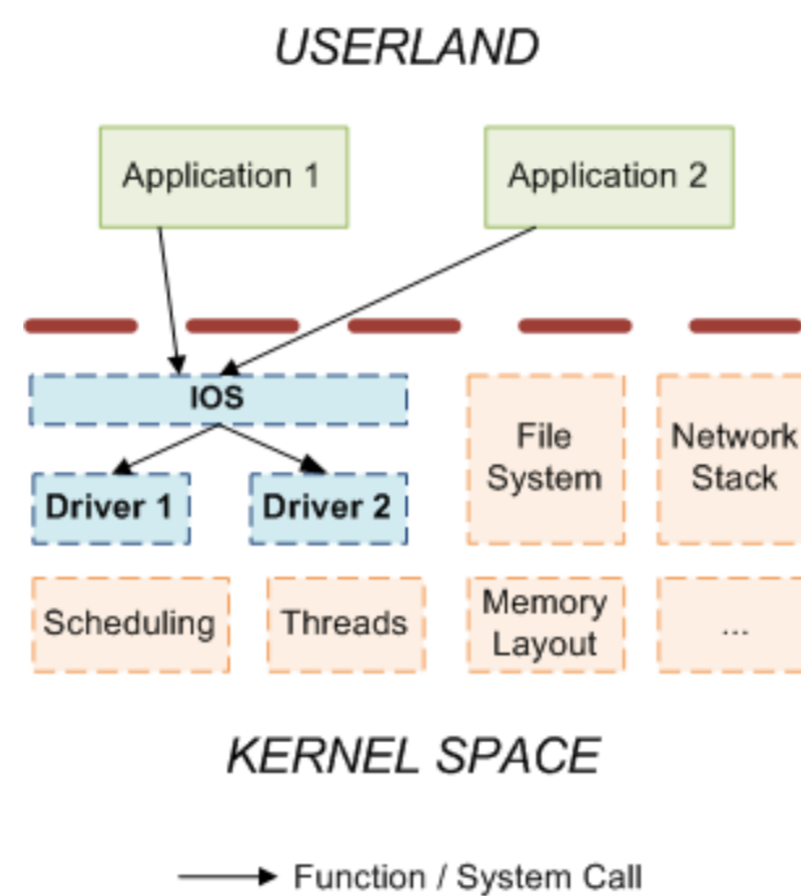
In most operating systems, userland processes have unrestricted access to hardware drivers, by system calls in monolithic kernels or IPCs in μ -kernels such as L4. This unrestricted access can often allow **malicious software to force a denial of service on the driver** or strongly impact its quality of service.

To mitigate this safety issue without impacting much drivers code, **our approach is to extend L4 IPCs by adding a control layer to IPCs aimed at drivers.**

This would allow admission control to the driver, as well as **accounting and managing the driver's occupation by user processes.**

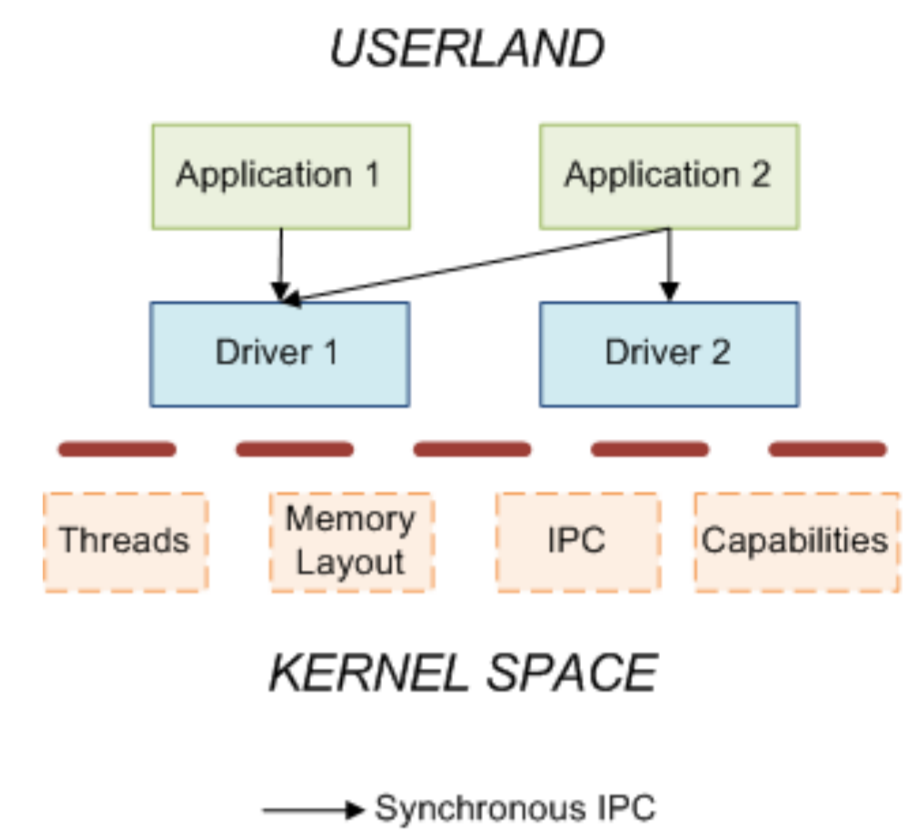
Context: Operating System Kernels

Monolithic Kernels



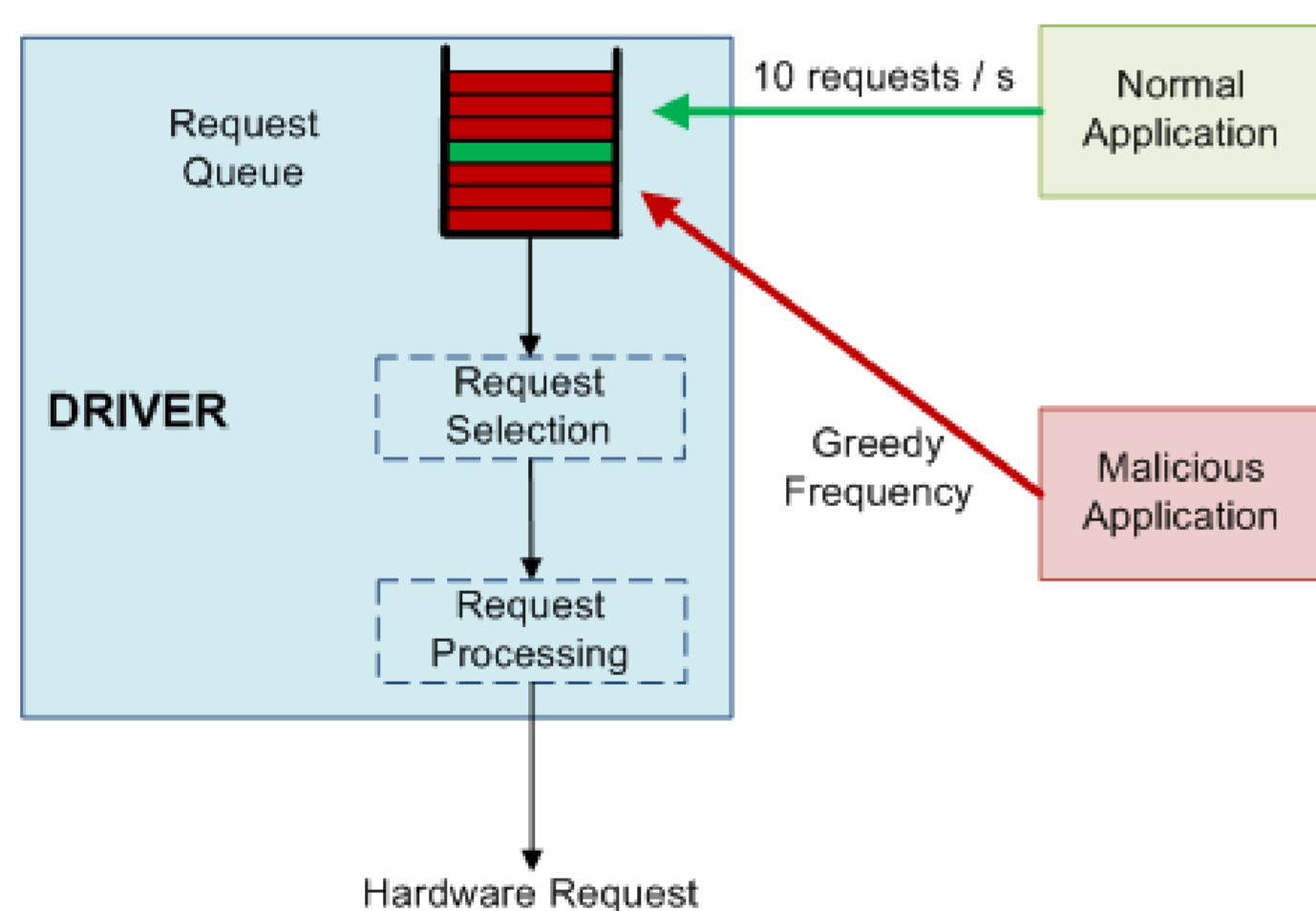
- All privileged code in kernel
- Communication via method calls
- Unified drivers interface (IOS)

L4 μ -kernel

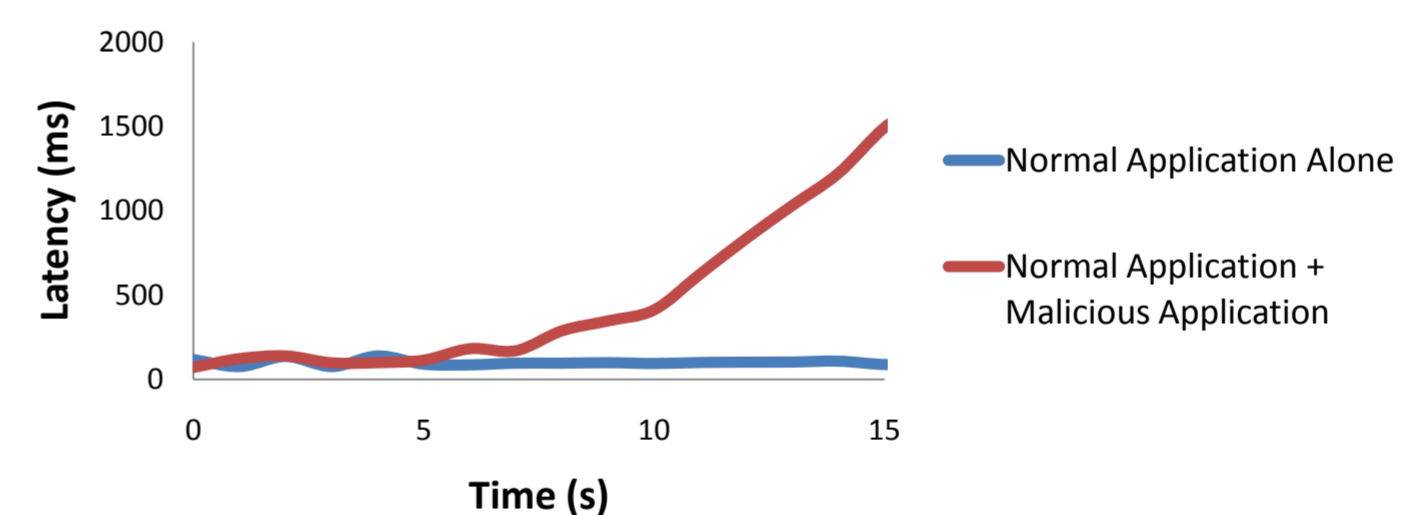


- Minimal kernel
- Communication via synchronous IPCs
- Userland privileges managed by capabilities

An Example of Resource Monopolization



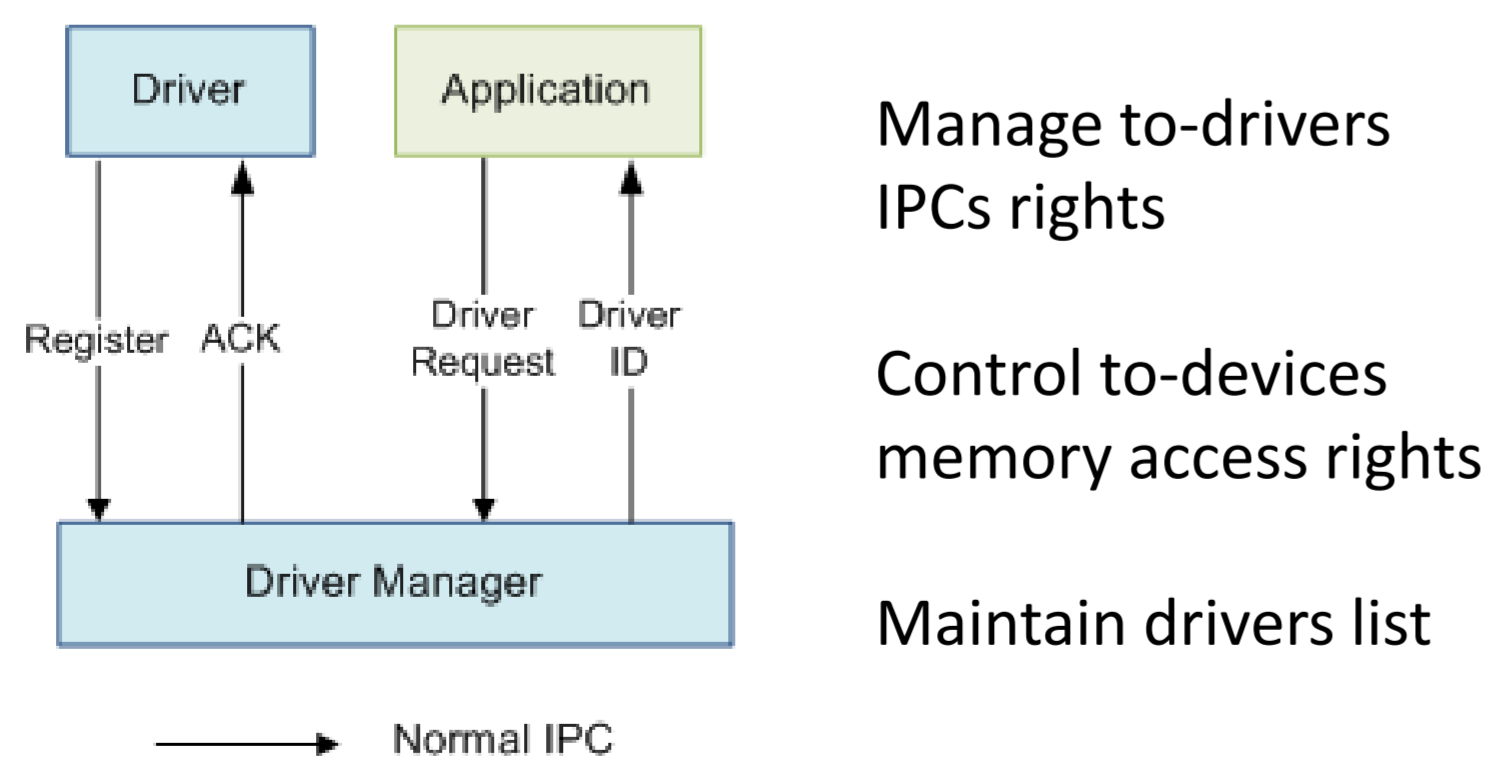
Delay between application deadlines and actual processing



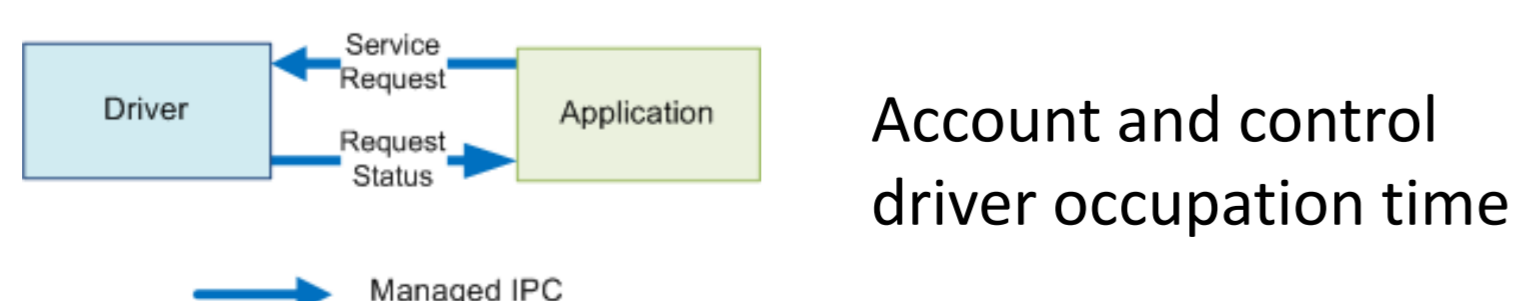
➔ The quality of service provided by the driver to the normal application is severely impacted

Proposition: Extend L4 IPCs

Admission Control



IPC Extension for Resource Control



Expected Benefits

- **Safety:** Prevent malicious threads from monopolizing drivers
- **QoS Management:** Accounting and admission control would allow resource reservation and real-time guarantees

Open Questions

- How about resource control in higher layers?
- Which uniform resource reservation model?
- Robustness of managed IPCs to malicious users?