Using a grid platform for solving large sparse linear systems over GF(2)
Thorsten Kleinjung, Lucas Nussbaum, Emmanuel Thomé

To cite this version:

HAL Id: inria-00502899
https://inria.hal.science/inria-00502899
Submitted on 16 Jul 2010

HAL is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers.

L’archive ouverte pluridisciplinaire HAL, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d’enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.
Using a grid platform for solving large sparse linear systems over $\text{GF}(2)$

Thorsten Kleinjung  
EPFL/IC/Lacal  
ÉPFL  
Lausanne, Switzerland  
thorsten.kleinjung@epfl.ch

Lucas Nussbaum  
Projet Algorille  
LORIA – Université Nancy 2  
Nancy, France  
lucas.nussbaum@loria.fr

Emmanuel Thomé  
Projet Caramel  
INRIA Nancy  
Nancy, France  
Emmanuel.Thome@inria.fr

Abstract—In Fall 2009, the final step of the factorization of rsa768 was carried out on several clusters of the Grid’5000 platform, leading to a new record in integer factorization. This step involves solving a huge sparse linear system defined over the binary field $\text{GF}(2)$. This article aims at describing the algorithm used, the difficulties encountered, and the methodology which led to success. In particular, we illustrate how our use of the block Wiedemann algorithm led to a method which is suitable for use on a grid platform, with both adaptability to various clusters, and error detection and recovery procedures. While this was not obvious at first, it eventually turned out that the contribution of the Grid’5000 clusters to this computation was major.

I. INTRODUCTION

The work described in the present article originates from the rsa768 project, which is an integer factorization challenge. This algorithmic task is of prime interest in cryptographic context, since it is the keystone of the RSA cryptosystem. The cryptographer builds a public key which is an integer $N$, the private key being two prime numbers $p$ and $q$ of roughly equal size satisfying $N = pq$. Factoring $N$ unveils the private key from the public key. It is therefore of prime interest to assess constantly the state of the art in terms of integer factoring, so that deployed cryptographic solutions (e-commerce, EMV credit cards, and many, many more) can be designed with proper parameters. Here, rsa768 denotes a particular 768-bit (232 decimal digits) composite integer published by RSA laboratories as a challenge1. This challenge has been solved in December 2009 as a joint effort between several research teams [1].

Factoring a 768-bit number reveals a large number of stumbling blocks. The preferred, and most efficient algorithm for this task, is the Number Field Sieve (NFS) factoring algorithm [2]. Describing this algorithm is out of scope of the present article. It suffices to know that there are two computationally intensive steps. The first step, called sieving, is a typical resource harvesting task. The second step, which draws our attention in the present article, is radically different. This step is a linear algebra step.

Several solutions to a homogeneous linear system defined over the binary field $\text{GF}(2)$ are to be found. While linear algebra is ubiquitous, the linear system encountered here differs in many fundamental aspects from the systems typically encountered in other contexts – these differences are outlined in Section II. The question of how this linear algebra computation is performed has several aspects. The main contribution of this work is the presentation of an approach which makes it possible to solve a large linear system of this kind (about 192,000,000 equations and unknowns) on a grid platform, with the capability of using not only one, but several unconnected clusters.

Indeed, while part of the rsa768 computation used computer time provided by partners ÉPFL (Switzerland), and NTT (Japan), we describe here how a significant part of the computation has been carried out on the Grid’5000 platform, which is an experimental platform dedicated to research in large-scale and distributed systems, composed of a dozen of clusters and a total of 6000 CPU cores.

Our approach illustrates that the needs in terms of computer resources for a computation such as the rsa768 linear system are quite different from a “supercomputer”, and that even an exclusive access to a dedicated “in-house” cluster is not nec-

1Money rewards used to be offered for solving such challenges, prior to 2007. Since then, RSA laboratories have discontinued this practice.
necessary. As an outcome, the amount of distribution achieved for the rsa768 linear system goes quite a bit beyond what has been previously done.

A second characteristic of linear algebra computations, especially those relying on black box methods (described in Section II) is the crucial importance of the error detection and recovery procedures. We describe how our approach is capable to achieve this fault management in a satisfactory way.

Section II gives details on the linear system to be solved, and lists the relevant algorithms. Section III gives specifically a bird’s eye view on the block Wiedemann algorithm, which has been used in the computation. Section IV discusses how the computation is split into elementary subtasks, while Section V details how these subtasks are scheduled, together with the issues relative to the management of the computation at a central level. Section VI describes how the results were checked against errors. Some experimental results and comparisons are summarized in Table II.

A common topic occurring in linear algebra calculations is not detailed in this present article. Of course, it is important to know how linear algebra operations (in our case, matrix-times-vector multiplications) are performed on a cluster using e.g. the MPI interface, and taking advantage of high-speed networks, topologies, and so on. This is obviously one of the ingredients of our work, but rather orthogonal to our primary focus which is on the use of several unconnected clusters.

II. STATEMENT OF THE PROBLEM

A. The matrix

Throughout the article, we assume that an \( N \times N \) matrix \( M \) defined over the binary field \( GF(2) \) is given, and that we seek elements of the right nullspace\(^2\) of this matrix. More specifically, since we instantiate our work with the rsa768 matrix, we provide the exact characteristics of the matrix under consideration:

- \( N = 192,796,550 \) rows and columns; dimension of the kernel at least \( 1,000 \).
- \( 27,797,115,920 \) non-zero coefficients, i.e. approximately 144 non-zero coefficients per matrix column;
- \( 105 \) GB to store the matrix in a straightforward format.

Of course, as can be judged from the figures above, the matrix \( M \) is extremely sparse. Sparse matrices occur in various contexts, but are probably best known in the context of numerical computations. It is important to stress that the problem we consider here is radically different, notably with respect to the following aspects.

- The field of definition being a finite field (not \( \mathbb{R} \) or \( \mathbb{C} \)), there is no relevant notion of convergence, fixed point, or dominant eigenvalue. Many algorithms rely on such key concepts for solving numerical linear systems, and this is unfortunately of no use in our context.
- While it occurs fairly often that large sparse matrices are symmetric, or perhaps almost symmetric, this is not the case here. The matrix to be solved does have some distinguishing shape, however. The density of the columns is evenly distributed, while the density of rows is not: a small number of rows are extremely heavy, almost dense, while the vast majority of rows are considerably sparser than average. 90% of the rows have less than 110 non-zero coefficients, 66% have less than 36. Thus one clearly has “dense” and “sparse” areas, the sparse parts accounting unfortunately for the largest part of the computation time.
- Numerical stability is not an issue, since all computations are exact. There is a counterpart though. In numerical calculations errors due to bad memory are usually not a problem. A flipped bit in the mantissa does not change the value much and even if a bit in the exponent flips, its effect might be unnoticeable after a few iterations. In contrast, a flipped bit in the calculation over \( GF(2) \) will be a disaster since its effect will spread and, after a few iteration, all components of the vector will be affected, thus invalidating the whole computation.

B. Algorithms

The problem of solving large sparse linear systems over finite fields has emerged a few decades ago in the context of integer factorization and computation of discrete logarithms over finite fields. A classical survey of the early methods is given in [3].

Sparseness being the key characteristic here, the most efficient methods are black box algorithms. The matrix \( M \) defining the linear system to be solved is considered as a black box, to which no internal access is allowed, and which is capable
of performing only one operation, namely the multiplication of the matrix by a vector.

When the linear system to be solved is defined over the finite field $\text{GF}(2)$, the algorithms of choice are so-called “block” methods, alternatively viewed as “single-instruction, multiple data” algorithms: vectors are replaced by blocks of vectors. A block of vectors is a sequence of $N$ tuples of $n$ bits, where the “blocking factor” $n$ is typically 64 or a multiple thereof. This effectively represents $n$ vectors of $N$ coordinates. Operations such as the addition of two vector blocks use the machine-word wide XOR operation when $n = 64$. In return, the number of required block box applications is reduced almost proportionally (up to a certain limit).

Two algorithms are commonly used, known as the block Lanczos and the block Wiedemann algorithm. The block Lanczos algorithm is appealing because it requires slightly less computations than the block Wiedemann algorithm. However it does not scale as well, as outlined in [4], [1]. The block Wiedemann algorithm offers the possibility of making most of the computation into several independent computations, thereby allowing a very desirable coarse-grain parallelism. This advent of parallelism is reduced almost proportionally (up to a certain limit), and hence it requires slightly less computations than the block Wiedemann algorithm. However it does not scale as well, as outlined in [4], [1]. The block Wiedemann algorithm as an alternative to the block Lanczos algorithm appeared first on a significant scale in [5] and [4].

III. THE BLOCK WIEDEMANN ALGORITHM

The block Wiedemann algorithm [6] is a block extension of the Wiedemann algorithm [7]. This method is a priori specific to finite fields. We give here an outline of the algorithm. A complete exposition of the block Wiedemann algorithm can be found in several places [6], [8], [9] and some of the references therein.

The starting point of the block Wiedemann algorithm is a vector block $y$, consisting of $n$ linearly independent $N$-bit vectors. One typically takes $n = 64n'$, so that $n'$ sequences of $N$ 64-bit words are needed to store the vector $y$. A second input is a vector block $x$, similarly consisting of $m$ linearly independent $N$-bit vectors. Here $m$ is a second blocking parameter, not necessarily equal to $n$. Likewise, we typically set $m = 64m'$. The analysis of the algorithm requires the vectors in the block $x$ be random vectors. However for efficiency we content ourselves with $x$ being a set of $m$ unit vectors, while $y$ is indeed random.

A. First step: scalar products

The first step of the block Wiedemann algorithm computes the first $L = \left\lfloor \frac{N}{2} \right\rfloor$ terms of the following power series.

$$A(X) = \sum_{i=0}^{L-1} a_i X^i = \sum_{i=0}^{L-1} i F^i y X^i.$$

The computation of this series can be done by repeating the following iteration:

- Set $y_0 \leftarrow y$, and $i = 0$;
- Output $a_i = i F^i y$. Set $y_{i+1} = M y_i$, and $i \leftarrow i + 1$;
- Repeat previous step until $i = L$.

The key observation here is that column $j$ of the matrices $(a_i)$ does not depend on the columns $j'$ of the starting vector $y$, as long as $j \neq j'$. Therefore, the terms of the series $A(X)$ can be computed piecewise, e.g. 64 columns by 64 columns. This is exactly how the computation is carried out. Using the superscript notation $S^{(j)}$ to denote the submatrix extracted from a matrix $S$ by selecting the 64 columns of indices $64j$ to $64j + 63$, we thus have:

$$a_i^{(j)} = i F^i y^{(j)}$$

$$a_i = \begin{pmatrix} a_i^{(0)} & a_i^{(1)} & \ldots & a_i^{(n'-1)} \end{pmatrix}$$

Producing the terms of $A(X)$ therefore essentially involves repeated application of the block box. Each submatrix $A(X)^{(j)}$ gives rise to a sequence of $m \times 64$ matrices over $\text{GF}(2)$; we have $n'$ such sequences of $L$ terms. A sequence of $\ell$ consecutive terms of such a sequence is thus made of $\ell \times m \times 64$ bits. Note that a sequence $(a_i^{(j)})_{i=0}^{\ell-1}$ requires only prior knowledge of the vector $y_o^{(j)} = M^0 y^{(j)}$.

B. Second step: linear generator

The next (central) step of the block Wiedemann algorithm computes a linear generator for $A(X)$. Description of how this step works is out of scope here, the reader is referred to [9] for an algorithm of complexity $O((m+n)^2 N \log N)$ for this task. This linear generator is an $n \times n$ matrix $F(X)$ of polynomials of degree $\left\lfloor \frac{N}{2} \right\rfloor$ such that

$$w = \sum_{i=0}^{\left\lfloor \frac{N}{2} \right\rfloor} M^i y^i F_0$$

is with high probability an element of the kernel of the matrix $M$. This step is the one whose complexity grows with the blocking factors $m$ and $n$, and furthermore it does not lend itself well to
Indeed, we have:

\[ S = \sum_{i=0}^{\frac{N}{n}} M^i y^i F_i, \]
\[ = \sum_{j=0}^{n'-1} \sum_{i=0}^{\frac{N}{n}} M^i y(i) t^{(j)}_i, \]

where the superscript notation \((j)\) is as in Section III-A. It follows that \( S = w \) can be computed as a sum of several partial sums. The formula above explicitly already \( n' \) clearly identified partial sums, but in fact, assuming a few vectors \( y(i)_n \) have been kept from the first step of the computation, it is possible to compute the partial sum:

\[ S_{(i_0, \ldots, i_0+\ell-1)} = \sum_{i=i_0}^{i_0+\ell-1} M^{i-i_0} y(i) t^{(j)}_{i_0}. \]

Such a sum is a vector block of \( Nn \) bits. Note the considerable difference with the size of the results from the first step. Here, partial sums are large and do not depend on the length \( \ell \) of the partial computation.

IV. SPLITTING INTO SMALL TASKS

A. Steps 1 and 3 into pieces

Having developed the outline of the block Wiedemann algorithm, we now wish to identify a decomposition of steps 1 and 3 into subtasks. The intent is that one such subtask is run on one cluster, and several subtasks can run concurrently on different, unconnected clusters.

Computations of steps 1 and 3 relate to \( n' \) different recurring sequences \( y(0), \ldots, y(n'-1) \). Focusing on sequence number \( j \), the data computed from the sequence elements \( y(i) \) to \( y(i+\ell-1) \) is thus derived from the knowledge of the starting iteration \( y(i_0) \), as well as some possible auxiliary data.

More specifically, we can instantiate the data sizes at stake here with the rsa768 matrix sizes. The blocking factors chosen for the rsa768 computation were \( m = 1024 \) (hence \( n' = 16 \)) and \( n = 512 \) (hence \( n' = 8 \)).

First, iterations \( i_0 \) to \( i_0 + \ell - 1 \) of step 1 on sequence \( j \) require the input vector \( y(i)_0 \). This represents \( N \) machine words of 64 bits, which is 1.43 GB. The output of this subtask is the collection of tiny matrices \( \{txy(i)_j, i_0 \leq i < i_0 + \ell \} \). Each such matrix is an \( m \times 64 \) matrix, which occupies 8 kB of memory. The output of the subtask is therefore \( 8\ell \) kB, plus the ending iteration \( y(i_0+\ell) \). Step 1 has to run until iteration number \( L \approx 565,000 \).

Iterations \( i_0 \) to \( i_0 + \ell - 1 \) of step 3 on sequence \( j \) also require the input vector \( y(i)_0 \) (which may have been kept from the step 1 runs). Furthermore, the linear generator coefficients \( L^{(j)} \) are required to compute the result \( \sum_{i=i_0}^{i_0+\ell-1} y(i) t^{(j)}_i \). The amount of linear generator data to be read is \( \ell \) coefficient matrices of size \( n \times 64 \), which corresponds to \( 4\ell \) kB. The output of the subtask is the partial sum, which is \( n' \) times as large as a vector, hence about 12 GB. The ending iteration \( y(i_0+\ell) \), as before, is also needed. Step 3 has to run until iteration number \( \frac{N}{n'} \approx 375,000 \).

B. Concurrent computations

We have already mentioned that computations relative to different recurring sequences are completely unrelated, so that they can run on different unconnected computing resources. Also, note that step 3 uses iterates \( y(i)_n \) which have already been computed by step 1. While storing all iterates would obviously be a bad idea, it appears that storing a few of them increases the potential number of concurrent subtasks that can run during step 3.

C. Constraints for choosing clusters

The choice of an appropriate cluster for running a subtask is driven by several criteria. Most of the time spent in the computation is related to the matrix-times-vector multiplications. Therefore it is natural, among the available clusters, to favour those with a high-speed network available (Myrinet, Infiniband for example). We also require obviously that the matrix, as well as all required temporary buffers, fit in RAM on the cluster nodes. As a rule of thumb, for the rsa768 matrix, this requires a total of 200 GB of RAM. As briefly
argued in the introduction, the specifics of how the matrix-times-vector multiplication is performed on a cluster are not developed here.

For a cluster to be used, the number of CPU cores used must also match some constraints. It must be decomposable in the form $p \times q$, where $p$ and $q$ are preferably of comparable size so that the cost of communication is not too unbalanced in the matrix-times-vector multiplications. It is also necessary to prepare the matrix for a computation on such a grid of $pq$ jobs (in the MPI sense). This is a relatively expensive precomputation, whose output is a collection of files of approximate total size 75 GB.

D. Roadmap of a subtask

A subtask, when specified as an abstract description such as “work with iterations $i_0$ to $i_0 + \ell - 1$ of step 1”, and when run on a specific (sub-) cluster, goes through several steps. When a job starts on the computer cluster, no persistent storage exists, so that the complete required data set must be imported from central storage before computations can actually begin. This includes the precomputed matrix data mentioned above, the starting iteration $y_{i_0}^{(j)}$, as well as for step 3 the linear generator data $\{ F_i^{(j)}, i_0 \leq i < i_0 + \ell \}$.

Likewise, all results must be saved to central storage, because all local data is lost when the job terminates.

V. SEVERAL CLUSTERS FOR SEVERAL SUBTASKS

The crux of our approach is the capability to adapt to a changing set of available clusters. The clusters which, as per the criteria developed in the previous section, are eligible for running a subtask at a given moment, are not always the same. Table II at the end of this article gives an indication on the number of different configurations which have been used. This dynamic set of available resources contrasts with the situation of “in-house” clusters which in favourable cases can be tailored specifically to the needs of the computation.

The management of the computation at the “central” level (above the “subtask” level) has two facets. Of course, this includes the dispatching of subtasks on the different computer clusters. This dispatching must be efficient, and must avoid exhausting the computing resources (this concern appears because Grid’5000 is an experimentation grid). Another facet is related to one of the key differences between a dedicated cluster and a shared computing resource. As alluded to in Section IV, we cannot rely on persistent local storage on the nodes. This implies that storage is an issue which must be handled at the central level.

A. Scheduling grid jobs for subtasks

The complete rsa768 linear algebra computation lasted about four months, but each subtask is limited to the maximum job length allowed in the platform (about 60 hours for Grid’5000). Therefore, organizing subtasks is a matter of scheduling grid jobs via the job submission systems. Since the rsa768 linear system was solved as a joint effort with other groups from EPFL (Switzerland) and NTT (Japan), only 4 of the 8 sequences were available for use on Grid’5000. This means that at a given time, up to 4 jobs (subtasks) could be running on the platform. A job has to be bound to a particular cluster, so as to make best use of the resources. While the block Wiedemann algorithm as a whole has some asynchronism capability, subtasks want to work as synchronously as possible.

Of course, as Grid’5000 is a shared resource, it was not reasonable to use some of its clusters exclusively during the whole computation. Instead, after discussion with the steering committee, we compromised on using two types of jobs:

- reservations a few hours beforehand on some clusters, or submission bound to start immediately – in that case, the resources were assigned to us for the whole duration of the job. This was done in general at most a couple of hours beforehand so as to let other users get a chance of using the clusters, and only during nights and week-ends;
- “best-effort” jobs – which are automatically killed if another user requests the resources with a higher priority. This allowed us to use the platform when no other user was asking for the resources, and to release the resources as soon as they are requested by another job.

The characteristics we considered for submitting jobs were mostly speed, and sometimes reliability. We built a table of the speeds achieved by given clusters for our application. Also, some clusters were large enough to be able to fit several jobs, e.g. three 24-node jobs. Given the clusters available at a given time, this chart indicates the preferred choice, maximizing the cumulated number of iterations computed per second. This selection step evolved from manual choice at the beginning of the rsa768 computation, towards a largely automated procedure.

As remarked in paragraph IV-B, step 3 is less constrained in terms of the number of concurrent
jobs running, therefore the optimization is relaxed. This implies that clusters which are a priori much slower than others, notably by lack of a fast interconnect, can become of some use.

Including “best-effort” jobs in the computation is by no means obvious. It relies on the possibility of achieving quick startup times and efficient periodic checkpoints through the use of adequate storage. This is explained in the following paragraphs.

B. Storage points, data sets, and transfers

Terabytes are cheap. However, terabytes of efficient, fast storage, near computing resources, are considerably more expensive to install and to maintain. We elected to aim at carrying out the rsa768 computation with a limited amount of storage close to the computing resources, while offloading to servers outside of Grid’5000. The amount of required data to remain reasonable, we chose to restrict to splittings \( 8 \times 8, 12 \times 12, 14 \times 14, \) and \( 16 \times 16; \) this already requires 300 GB of storage, which need to be available on the storage nodes because at any job startup, one of these data sets must be imported. This restricts the available cluster sizes, while still offering a reasonable variety of possible configurations.

Once started, a job must import its input data, begin the computation, optionally save its results periodically, and if possible trap a notification of approaching termination so as to get time to save a last checkpoint and the corresponding data.

The input data to be imported is the 75 GB precomputed balancing data, the starting vector \( y_{1}^{(j)} \), and for step 3 the relevant linear generator file \( F_{i}^{(j)} \). How to import these files? The question of how not to do it is easily settled. Compute nodes have access to site storage via the NFS protocol, but the data throughputs are bad. After having tried several options, we chose the following solution:

- On each of the storage nodes on which we had a storage allowance, an rsync daemon listens on a custom port. This daemon, in order to limit the load on the machine, honors only a limited number of concurrent requests using the max connections parameter. The storage node from which a compute node tries to import a given file is selected at random. The compute node keeps trying servers at random until one accepts its request.

- An optimization is useful in the case where several jobs of identical size are expected to run at a given site (quite frequent within step 3). Each node holding a local copy of some submatrix file announces it with a phony symbolic link on the NFS partition (the link target being the node name), and listens with an rsync daemon on the same usual custom port. Compute nodes willing to retrieve the same file have therefore a way to first check for the availability of the corresponding file. This optimization helped reducing the data import times dramatically, when applicable of course.

Once the data file has been imported, computations can start. The key question is when and how to trigger saving of intermediate checkpoints and results. Triggering one checkpoint implies the production of an amount of data equal to:

- 1.43 GB + 8\( \ell \) KB within step 1, where \( \ell \) is the distance to the previous checkpoint.
- 1.43 GB + 12 GB within step 3, irrespective

\[
\begin{array}{|c|c|c|c|}
\hline
\text{data} & \text{size} & \text{output from} & \text{input to} \\
\hline
\text{Input matrix} & 105 \text{ GB} & \text{none} & \text{balancing} \\
\text{Balancing} & 75 \text{ GB} & \text{balancing} & 1+3 \\
\text{Vectors } y_{1}^{(j)} & 1.43 \text{ GB} & 1+3 & 1+3 \\
\text{Matrices } a_{i}^{(j)} & 8 \text{ kB} & 1 & 2 \\
\text{Matrices } F_{i}^{(j)} & 4 \text{ kB} & 2 & 3 \\
\text{g}_{i_{0}...i_{0}+\ell-1}^{(j)} & 12 \text{ GB} & 3 & \text{none} \\
\hline
\end{array}
\]

**Table I**

FILES USED WITHIN THE COMPUTATION. STEPS 1, 2, 3 REFER TO THE BLOCK WIEMEANN STEPS. STEP 2 IS NOT COMPUTED ON THE GRID PLATFORM.
of the distance to the previous checkpoint.

The choice of the checkpoint frequency is related to the lifetime of a job. Bar possible errors, this lifetime is decided at submission time, typically several hours, e.g. 12h. The lifetime of a best-effort job is unpredictable (and can be arbitrarily small). We used the following rules:

- Always trigger a checkpoint at least at multiples of 4096 iterations.
- For best-effort jobs, trigger periodic checkpoints so as to avoid losing the output of long-running jobs. We chose an 80 minutes period.
- Trigger a checkpoint 10 minutes (for step 1) to 20 minutes (for step 3) before job end.

Saving of checkpoint data and results by compute nodes to storage servers was done with the same rsync scheme that was used for importing input data. However, since saving results has higher priority than importing data, we used a second rsync port, with a larger number of allowed connections. Each storage server had thus typically 4 to 8 maximum outbound connections, and 8 to 12 maximum inbound connections.

It must be noted that the aggregated bandwidth consumed by saving checkpoints in step 3 can be quite considerable if the grid platform is used at the peak of its possibilities. For the rsa768 computation, up to 15 concurrent subtasks could run during step 3. Assuming all are run by best-effort jobs, the total amount of data produced reaches in such a case about 40 MB/s on the whole grid. Depending on which kind of data path has to absorb this traffic (e.g. by offloading the data to remote servers), this can be an issue. This “peak” problem has not occurred in the course of the rsa768 computation. As a partial solution, assembling the partial sums of step 3 (replacing $S_{a...b-1}$ and $S_{b...c-1}$ by their sum $S_{a...c-1}$) can be done from within the grid platform, which avoids the possibly slower link from the grid platform to the outside. To do so, we first need to gain confidence in the partial sum files, which is the purpose of the following section.

VI. LIVING WITH ERRORS

The amount of data computed requires that some checks for data integrity be performed. It turns out that it is possible to do such checks with the block Wiedemann algorithm quite easily.

Step 1 checks boil down to verifying that

$$ (a^{(j)}_{i_0})_{i_0+\ldots+i_0+\ell-1} \quad \text{and} \quad y^{(i)}_{i_0+\ell} \quad \text{are both consistent with the vector} \quad y^{(j)}_{i_0}. $$

We do this check for constant length $\ell$, such that all the iterates whose index is a multiple of $\ell$ are saved (given the checkpoint frequencies indicated above, we choose $\ell = 4096$).

For this check, we fix a dimension $\mu$ arbitrarily (e.g. $\mu = m$), and precompute random $\mu \times m$ matrices $r_0, \ldots, r_{\ell-1}$, as well as the vectors $V_r = \sum_{k=0}^{\ell-1} r_k \cdot M^k$, and $C_r = r \cdot M^\ell$. Computing vectors $V_r$ and $C_r$ has a mild computational cost.

The check is whether $V_r y^{(j)}_{ij_0} = \sum_{k=0}^{\ell-1} r_k a_{ij_0+k}^{(j)}$, and also whether $C_r y^{(j)}_{ij_0+\ell}$. This check is only a dot product, and can be done with very limited resources. Errors are detected with satisfactory probability.

For step 3, a similar procedure can be used to check that a partial sum $S^{(j)}_{(i_0\ldots i_0+\ell-1)}$ is valid. This check does not require that the length $\ell$ be fixed beforehand. This is particularly handy because the partial sum files turn out to be very large, and the need to assemble them into files of equal sizes, but representing a larger work length $\ell$, is desired. This can be done only if the different partial files are validated. The check is done as follows. Pick an arbitrary small integer $\delta$, such that $t x M^\delta$ has no zero coordinate. The check is the following:

$$ t x M^\delta S^{(j)}_{(i_0\ldots i_\ell-1)} = \sum_{i=i_0}^{i_0+\ell-1} a_{i_0+i} \cdot t(x^{(j)}). $$

This check is fast and accurate enough for our purposes. As for the previous check, performing the verification is not expensive.

During the computation, some true errors have been detected using these checks. Running out of storage space was the principal source of errors. Data files were truncated in such occasions, or corrupted with blocks of zeroes. On a computation of this scale, the error-checking procedures described above were very efficient at detecting such events.

VII. CONCLUSION

The main unknown associated with the rsa768 computation was whether the linear algebra would be doable with reasonable expenses and resources. While it was not obvious at first, and triggered many obstacles, we succeeded in proving that a distributed platform such as Grid’5000, despite being composed of a variety of clusters with different CPU and network interconnects, provided an effective answer. We were able to use 6 different clusters on Grid’5000, with 16 different configurations, as is illustrated by the timing results in Table II. Most unexpectedly, we were also able to adapt to the constraints of
best-effort jobs, and use them effectively through appropriate use of checkpoints and distributed data storage.

Timings given in Table II indicate the performance obtained in Fall 2009 for the computation as it has been actually run, during a period of approximately three months. The total CPU time spent for this computation was slightly above 100 CPU core-years. The computation described in this article was carried out as a compromise between the time for developing the infrastructure of programs for managing the individual tasks, and the goal of eventually obtaining the solution to the linear system. Some aspects of the approach have been identified as candidates for possible improvements, notably the “peak bandwidth” problem mentioned at the end of Section V. Further experiments of this kind would have to take this problem into account, possibly in a different way. Overall though, little doubt is left: moderately larger matrices are clearly within reach using this approach.

REFERENCES


<table>
<thead>
<tr>
<th>(a)</th>
<th>(b)</th>
<th>(c)</th>
<th>(d)</th>
<th>(e)</th>
<th>(f)</th>
<th>(g)</th>
<th>(h)</th>
<th>(i)</th>
<th>(j)</th>
<th>(k)</th>
<th>(l)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lausanne</td>
<td>56</td>
<td>2× AMD 2427</td>
<td>2.2</td>
<td>12</td>
<td>16</td>
<td>ab20g</td>
<td>12</td>
<td>144</td>
<td>4.3</td>
<td>4.8</td>
<td>40%</td>
</tr>
<tr>
<td>Tokyo</td>
<td>110</td>
<td>2× Pentium-1</td>
<td>5.0</td>
<td>2</td>
<td>5</td>
<td>eth1g</td>
<td>110</td>
<td>220</td>
<td>5.8</td>
<td>7.8</td>
<td>%</td>
</tr>
<tr>
<td>Grenoble</td>
<td>34</td>
<td>2× Xeon E5420</td>
<td>2.5</td>
<td>8</td>
<td>8</td>
<td>ab20g</td>
<td>24</td>
<td>144</td>
<td>3.7</td>
<td>30%</td>
<td></td>
</tr>
<tr>
<td>Lille</td>
<td>46</td>
<td>2× Xeon E5440</td>
<td>2.8</td>
<td>8</td>
<td>8</td>
<td>mx10g</td>
<td>36</td>
<td>144</td>
<td>3.1</td>
<td>3.3</td>
<td>31%</td>
</tr>
<tr>
<td>Nancy</td>
<td>92</td>
<td>2× Xeon L5420</td>
<td>2.5</td>
<td>8</td>
<td>16</td>
<td>ab20g</td>
<td>64</td>
<td>256</td>
<td>2.2</td>
<td>2.4</td>
<td>41%</td>
</tr>
<tr>
<td>Orsay</td>
<td>120</td>
<td>2× AMD 250</td>
<td>2.4</td>
<td>2</td>
<td>2</td>
<td>mx10g</td>
<td>98</td>
<td>196</td>
<td>2.8</td>
<td>3.9</td>
<td>32%</td>
</tr>
<tr>
<td>Rennes</td>
<td>96</td>
<td>2× Xeon 5148</td>
<td>2.3</td>
<td>4</td>
<td>4</td>
<td>mx10g</td>
<td>64</td>
<td>256</td>
<td>2.5</td>
<td>2.7</td>
<td>37%</td>
</tr>
<tr>
<td>Rennes</td>
<td>64</td>
<td>2× Xeon L5420</td>
<td>2.5</td>
<td>8</td>
<td>32</td>
<td>eth1g</td>
<td>49</td>
<td>196</td>
<td>6.2</td>
<td>67%</td>
<td></td>
</tr>
</tbody>
</table>

Table II

DIFFERENT PER-ITERATION TIMINGS ON VARIOUS CLUSTERS. (a) CLUSTER LOCATION; (b) TOTAL CLUSTER SIZE (NUMBER OF NODES); (c) CLUSTER CPU TYPE; (d) NODE CPU FREQUENCY; (e) CORES PER NODE; (f) RAM PER NODE (GB); (g) CLUSTER INTERCONNECT (ETH1G: GIGABIT ETHERNET, MX10G: 10Gbps MYRINET, IB20G: 20Gbps INFINIBAND); (h) JOB SIZE (NUMBER OF NODES); (i) NUMBER OF CORES USED PER JOB; (j) TIME PER ITERATION IN SECONDS (STAGE 1); (k) TIME PER ITERATION IN SECONDS (STAGE 3); (l) PERCENTAGE USED FOR COMMUNICATION.