Structural Translation From Time Petri Nets to Timed Automata

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From Time Petri Nets to Timed Automata

page 1/16

Contents

- 1. Context & Related Work
- 2. Time Petri Nets & Timed Automata
- **3. Translation: TPN to TA**
- 4. Conclusion

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From Time Petri Nets to Timed Automata

page 2/16

Petri Nets with time

- Timed Petri Nets ([Ramchandani, 1974]) sharp timing constraints
 P-Timed PN = T-Timed PN
- Time Petri Nets (TPN) ([Merlin, 1974]) interval timing constraints
 T-TPN ≠ P-TPN
 Timed PN ⊆ T-TPN and in P-TPN
 - **TPN** \subseteq **Time Stream Petri Nets ([Diaz & Senac, 1994])**

Petri Nets with time

Timed Petri Nets ([Ramchandani, 1974]) – sharp timing constraints

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T-Time Petri Nets (TPN) ([Merlin, 1974]) – interval timing constraints
 T-TPN ≠ P-TPN
 Timed PN ⊆ T-TPN and in P-TPN
 TPN ⊆ Time Stream Petri Nets ([Diaz & Senac, 1994])

Main Results & Tools for T-TPNs [Berthomieu & Diaz, 1991]

- Boundedness for TPNs undecidable
- Reachability for bounded TPNs decidable
- Tools: computation of the state class graph (SCG)
 - Tina [Berthomieu, 2003] Computes the SCG, untimed CTL* model-checking
 - Roméo [Gardey et al., 2003] Computes the SCG, Region Graph, Reachability

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 Computes the SCG, Region Graph, Reachability
- Timed Automata [Alur & Dill, 1994] Finite Automata extended with real-valued clocks

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Main Results & Tools for Timed Automata ([Alur & Dill, 1994]):

- Reachability + Timed CTL model-checking decidable
- Tools:
 - Uppaal [Pettersson & Larsen, 2000]
 - Kronos [Yovine, 1997]
 - Cmc [Laroussinie et al, 1998]

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From Time Petri Nets to Timed Automata

page 3-e/16

- From 1-safe TPN to TA [Sifakis & Yovine, 1996]
- From bounded TPN to TA [Sava, 2001] No correctness proof (equivalence of the semantics ?)
- From TPN to TA [Lime & Roux, 2003] correctness proof (timed bisimilarity) Enriched SCG = TA ⇒ heavy computation Needs a dedicated tool ([Gardey et al., 2003])

Previous approaches:

- Either restricted to 1-safe TPN
- No formal correctness proof of the translation
- Or need to compute the state space of the TPN

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Our aim:

- **Structural** translation (no heavy computation)
- **Correctness proof** of the translation (behavioural equivalence)

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- Or need to compute the state space of the TPN

Our aim:

- Structural translation (no heavy computation)
- **Correctness proof** of the translation (behavioural equivalence)

Results:

- **Structural** translation
- Applies to non safe TPNs
- Correctness proof of the translation (behavioural equivalence)
- Model-checking of TCTL for bounded T-TPN
- Allows to use efficient tools for analysis of TA

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From Time Petri Nets to Timed Automata

Contents

- 1. Context & Related Work
- 2. Time Petri Nets & Timed Automata
- **3. Translation: TPN to TA**
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From Time Petri Nets to Timed Automata

page 5/16



Initially: $P_0 = P_2 = 1$ at $\delta = 0$

 $(P_0P_2, 0)$

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From Time Petri Nets to Timed Automata

page 6-a/16



Initially: $P_0 = P_2 = 1$ at $\delta = 0$ $\delta \in [1, 4]$: T_0 enabled; fire T_0 at $\delta = 3.7$

$$(P_0P_2, 0) \xrightarrow{3.7} (P_0P_2, 3.7) \xrightarrow{T_0} (P_1P_2, 3.7)$$

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From Time Petri Nets to Timed Automata

page 6-b/16



Initially: $P_0 = P_2 = 1$ at $\delta = 0$ $\delta \in [1, 4]$: T_0 enabled; fire T_0 at $\delta = 3.7$ "untimed" T_1 is enabled \implies clock for T_1 starts

$$(P_0P_2, 0) \xrightarrow{3.7} (P_0P_2, 3.7) \xrightarrow{T_0} (P_1P_2, 3.7)$$

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From Time Petri Nets to Timed Automata

page 6-c/16



Initially: $P_0 = P_2 = 1$ at $\delta = 0$ $\delta \in [1, 4]$: T_0 enabled; fire T_0 at $\delta = 3.7$ "untimed" T_1 is enabled \implies clock for T_1 starts

after 3 t.u. "timed" T_1 enabled and must fire before 5 t.u.

$$(P_0P_2, 0) \xrightarrow{3.7} (P_0P_2, 3.7) \xrightarrow{T_0} (P_1P_2, 3.7) \xrightarrow{3 \le t \le 5} (P_1P_2, 3.7 + t)$$

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From Time Petri Nets to Timed Automata

page 6-d/16



Initially: $P_0 = P_2 = 1$ at $\delta = 0$

- $\bullet \ \delta \in [1, 4]: T_0 \text{ enabled; fire } T_0 \text{ at } \delta = 3.7$
 - "untimed" T_1 is enabled \implies clock for T_1 starts
- after 3 t.u. "timed" T₁ enabled and must fire before 5 t.u.
 fire T₁ and time-elapsing

$$(P_0P_2, 0) \xrightarrow{3.7} (P_0P_2, 3.7) \xrightarrow{T_0} (P_1P_2, 3.7) \xrightarrow{3 \le t \le 5} (P_1P_2, 3.7 + t)$$
$$\xrightarrow{T_1} (\emptyset, 3.7 + t) \xrightarrow{t' \ge 0} (\emptyset, 3.7 + t + t')$$

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From Time Petri Nets to Timed Automata

page 6-e/16



T a TPN

- Semantics of $\mathcal{T} = \llbracket \mathcal{T} \rrbracket = sequence of alternating$
 - Discrete step
 - Time step
- **[**T] = Timed Transition System (TTS)

Timed Automata [Alur & Dill, 1994] $\begin{array}{c} 0 \quad x > 1; a; x := 0 \\ x < 2 \end{array}$

Finite structure + real-valued clocks

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From Time Petri Nets to Timed Automata

page 7-a/16

Timed Automata [Alur & Dill, 1994] x > 1; a; x := 01

Finite structure + real-valued clocks

 $x \leq 2$

Invariant - Label - Guard - Reset

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Timed Automata [Alur & Dill, 1994] x > 1; a; x := 0(1)

Finite structure + real-valued clocks
 Invariant - Label - Guard - Reset

 $x \leq 2$

$$(0, x = 0)$$

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$$0 \qquad x > 1; a ; x := 0 \qquad 1$$

$$x \le 2$$

Finite structure + real-valued clocks
 Invariant - Label - Guard - Reset

$$(0, x = 0) \xrightarrow{1.65} (0, x = 1.65)$$

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From Time Petri Nets to Timed Automata

page 7-d/16

$$0 \qquad x > 1; a ; x := 0 \qquad 1$$

$$x \le 2$$

Finite structure + real-valued clocks
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$$(0, x = 0) \xrightarrow{1.65} (0, x = 1.65) \xrightarrow{a} (1, x = 0) \xrightarrow{t \ge 0} (1, x = t)$$

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From Time Petri Nets to Timed Automata

page 7-e/16

$$0 \xrightarrow{x > 1; a ; x := 0} 1$$

$$x \le 2 \xrightarrow{p \ge 2; p := p + 2}$$

Finite structure + real-valued clocks
 Invariant - Label - Guard - Reset

$$(0, x = 0) \xrightarrow{1.65} (0, x = 1.65) \xrightarrow{a} (1, x = 0) \xrightarrow{t \ge 0} (1, x = t)$$

+ (arrays of) integer variables

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page 7-f/16

$$0 \xrightarrow{x > 1; a ; x := 0} 1$$

$$x \le 2 \xrightarrow{p \ge 2; p := p + 2}$$

- Timed Automata (TA) + **bounded** integer variables
- Semantics of a $TA = \llbracket A \rrbracket = sequence$ of alternating
 - Discrete step
 - Time step
 - Semantics: $\llbracket A \rrbracket =$ Timed Transition System (TTS)

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From Time Petri Nets to Timed Automata

page 7-g/16

Contents

- 1. Context & Related Work
- 2. Time Petri Nets & Timed Automata
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- 4. Conclusion

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From Time Petri Nets to Timed Automata

page 8/16

Given: T a TPN with n transitions and m places

p: array of integers; $\mathbf{p}[i] = \mathbf{tokens}$ in place $i, i \in [1..m]$

 A_i a TA with one clock associated to transition T_i

 $[\Longrightarrow$ TA for a transition]

SU a cyclic supervisor (4 states) – computes new marking

 \implies **TA** of the SU]

Synchronization: $\mathcal{A} = (SU \mid \mathcal{A}_1 \mid \cdots \mid \mathcal{A}_n)$ \mathcal{A} has n clocks

Discrete step in $\llbracket \mathcal{T} \rrbracket = 4$ discrete steps in $\llbracket \mathcal{A} \rrbracket$

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From Time Petri Nets to Timed Automata

page 9-a/16

- Given: T a TPN with n transitions and m places
- **p**: array of integers; $\mathbf{p}[i] = \mathbf{tokens}$ in place $i, i \in [1..m]$
- Synchronization: $\mathcal{A} = (SU \mid \mathcal{A}_1 \mid \dots \mid \mathcal{A}_n)$ \mathcal{A} has n clocks
 - Discrete step in $\llbracket \mathcal{T}
 rbracket = 4$ discrete steps in $\llbracket \mathcal{A}
 rbracket$

Results

- 1. Theorem 3.2: $[\![\mathcal{A}]\!]$ and $[\![\mathcal{T}]\!]$ are timed bisimilar
- 2. ${\mathcal T}$ is bounded iff ${\bf p}$ is bounded
- 3. \mathcal{T} has k reachable markings $\implies \mathcal{A}$ has $\leq 4 \cdot k \cdot n$ discrete states

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From Time Petri Nets to Timed Automata

page 9-b/16

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Consequences

- Quantitative logic TCTL decidable on bounded TPNs
- Efficient translation to Uppaal: Roméo [Gardey et al., 2003]

Results

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Consequences

- Quantitative logic TCTL decidable on bounded TPNs
- Efficient translation to Uppaal: Roméo [Gardey et al., 2003]
- Number of clocks ?
 - Useful clocks: only for enabled transitions
 - Active clock reduction
 - Use of active clocks feature in Uppaal

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From Time Petri Nets to Timed Automata

page 9-d/16

Contents

- 1. Context & Related Work
- 2. Time Petri Nets & Timed Automata
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- 4. Conclusion

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From Time Petri Nets to Timed Automata

page 10/16

Conclusion & Future Work

Summary

- Formal semantics for TPNs
- **Structural** translation to **TA**
 - Correctness proof
 - Model-checking TCTL

Future Work

- Use on real case studies (with Uppaal)
- **Expressiveness:** from TA to TPNs ?

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Automaton for one Transition



(a) The automaton \mathcal{A}_i for transition t_i

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page 14/16

Automaton for the Supervisor



(b) Supervisor SU

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page 15/16

About Active Clocks



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page 16/16