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David Cachera, Thomas Jensen, Arnaud Jobin, Pascal Sotin. Long-Run Cost Analysis by Approximation of Linear Operators over Dioids. [Research Report] RR-6338, 2007, pp.35. inria-00182338v2

# HAL Id: inria-00182338 https://inria.hal.science/inria-00182338v2

Submitted on 26 Oct 2007 (v2), last revised 25 Jan 2008 (v4)

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INSTITUT NATIONAL DE RECHERCHE EN INFORMATIQUE ET EN AUTOMATIQUE

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# N° 6338

Octobre 1997



ISSN 0249-6399 ISRN INRIA/RR--6338--FR+ENG



# Long-Run Cost Analysis by Approximation of Linear Operators over Dioids

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Thème SYM — Systèmes symboliques Projet Lande

**Abstract:** We present a static analysis technique for modeling and approximating the long-run resource usage of programs. The approach is based on a quantitative semantic framework where programs are represented as linear operators over dioids. We show how to extract the long-run cost of a program from the matrix representation of its semantics. An essential contribution is to provide abstraction techniques which make it feasible to compute safe overapproximations of this cost. A theorem is proved stating that such abstractions yield correct approximations of the program's long-run cost. The theoretical developments are illustrated on a concrete example taken from the analysis of the cache behaviour of a simple bytecode language.

**Key-words:** Programming Language Semantics, Static Analysis, Resource Analysis, Long-Run COst, Dioids

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# Analyse de coût moyen par approximation d'opérateurs linéaires sur des dioïdes

**Résumé :** Nous présentons une technique d'analyse statique pour modéliser et calculer de façon approchée l'utilisation asymptotique moyenne de ressources par un programme. L'approche se fonde sur un modèle de sémantique quantitative où les programmes sont représentés par des opérateurs linéaires sur un dioïde. Nous montrons comment extraire le coût moyen asymptotique d'un programme de la représentation matricielle de sa sémantique. Une contribution essentielle consiste à fournir des techniques d'abstraction permettant de calculer effectivement une surapproximation de ce coût. Nous prouvons que de telles abstractions fournissent des approximations correctes. Nous illustrons ces notions sur un exemple d'analyse de défauts de cache pour un langage simple de bytecode.

**Mots-clés :** Sémantique des langages de programmation, analyse statique, analyse de consommation de ressources, coût moyen asymptotique, dioïdes

### Contents

1	Introduction	3
2	Linear operator semantics         2.1       Cost dioid         2.2       Semantics as linear operators over dioids         2.3       Running example: quantitative semantics	4 5 7 7
3	Abstraction3.1Abstraction over Idempotent Dioids3.2Induced abstract semantics3.3Running example: abstraction	<b>9</b> 9 10 11
4	Long-run cost4.1Ensuring correctness4.2Running example: long-run cost	<b>12</b> 14 14
<b>5</b>	Related work	15
6	Conclusion	16
$\mathbf{A}$	Some rules of a bytecode quantitative semantics	19
в	Trace semantics and long run cost	20
С	Correctness of the long-run cost	<b>25</b>

# 1 Introduction

This article is concerned with the semantics-based program analysis of quantitative properties pertaining to the use of resources (time, memory, ...). Analysis of such non-functional properties relies on an operational model of program execution where the cost of each computational step is made explicit. We take as starting point a standard small-step operational semantics expressed as a transition relation  $\sigma \to^q \sigma'$  between states  $\sigma, \sigma' \in \Sigma$  with costs  $q \in Q$  associated to each transition. The set  $\Sigma$  of states is supposed to be finite, but potentially large, and the set Q of costs supposed to have two operations for composing costs: a "product" operator that combines the costs along an execution path, and a "sum" operator that combines costs coming from different paths. These operators make Q a complete idempotent dioid. The sum operator induces a partial order on costs that will serve as a basis for approximating costs. From such a rule-based semantics, there is a straightforward way to obtain a transition matrix, in which a matrix entry represents the cost of passing from one state of the program to another. This expresses the semantics of a program as a linear operator on  $Q(\Sigma)$ , the moduloid of vectors of elements of Q indexed over Σ.

Using these mathematical structures allows for defining the notion of *long*run cost for a program. This cost is the maximum average of costs accumulated along a cycle of the semantics graph, and corresponds to an asymptotic average behaviour of the program. This cost is computed from the traces of the successive iterates of the cost matrix, up to the number of accessible states. The quantitative operational semantics operates on finite, but potentially large state spaces so quantitative semantic models, like their qualitative counterparts, are usually not tractable. Hence, it is necessary to develop techniques for abstracting this semantics, in order to return an approximation of the overall program cost that is feasible to compute. In line with the semantic machinery used to model programs, abstractions are also defined as linear operators from the moduloid over the concrete state space into the moduloid over the abstract one. Given such an abstraction over the semantic domains, we then have to abstract the transition matrix of the program itself into a matrix of reduced size. We give a sufficient condition for an abstraction of the semantics to be correct, *i.e.* to give an over-approximation of the real cost, and show how an abstract semantics that is correct by construction can be derived from the concrete one. The long-run cost of a program is thus safely approximated by an abstract long-run cost, with respect to the order relation induced by the summation operator of the dioid.

The framework proposed here covers a number of different costs related to resource usage (time and memory) of programs. As a running example, we have chosen a slightly unusual cost model pertaining to the analysis of cache behaviour and the number of cache misses in programs. We illustrate the notions of quantitative semantics, abstraction and long-run cost on a program written in a simple, intermediate bytecode language (inspired by Java Card) onto which we impose a particular cache model.

The paper is structured as follows. Section 2 defines the quantitative semantics as a linear operator over a moduloid. We give the general form of this semantics, and precisely define the notion of dioid we use throughout the paper. Section 3 defines the notion of abstraction together with its correctness, and shows how we can derive an abstract semantics that is correct by construction. Section 4 defines the notion of long-run cost, relating it to the asymptotic behaviour of the trace semantics, and shows how a correct abstraction yields an over-approximation of the concrete long-run cost of a program. Section 5 lists related work and Section 6 concludes and discusses future research directions.

### 2 Linear operator semantics

We begin by giving a general framework for expressing quantitative operational semantics. Transitions of these semantics will be equipped with quantities (or costs) depending on the accessed states. Let P be a program, and  $\Sigma$  the semantic domain of possible states for this program. We shall suppose that the set of states is finite. The quantitative operational semantics of P is given as a transition relation, defined by inference rules of the following form:  $\sigma \to^q \sigma'$  where  $\sigma, \sigma'$  are states of  $\Sigma$ , and q is the cost attached to the transition from  $\sigma$  to  $\sigma'$  (q is function of  $\sigma$  and  $\sigma'$ ). The set Q of costs and its structure will be made precise in the next subsection. Among the set of states, there is a distinguished set I of initial states for the program P. We define the trace semantics of P as:

$$\llbracket P \rrbracket_{tr} = \{ \sigma_1 \to^{q_1} \dots \sigma_{n-1} \to^{q_{n-1}} \sigma_n \mid \sigma_1 \in I, \sigma_i \to^{q_i} \sigma_{i+1} \}$$

4

#### 2.1 Cost dioid

The small-step, quantitative operational semantics induces a labelled transition system over  $\Sigma$  with labels in Q and a transition relation  $\rightarrow \subseteq \Sigma \times Q \times \Sigma$ , written  $\sigma \rightarrow^q \sigma'$ . Such a transition states that a direct (one-step) transition from  $\sigma$  to  $\sigma'$  costs q. These unitary transitions can be combined into big-step transitions, using two operators:  $\otimes$  for accumulating costs and  $\oplus$  to get a maximum of different costs. These operators will form a dioid on Q, as explained below. Costs can be defined in more general ways (for instance, one could use a more general algebra of costs as in [3]) but the present definition covers a number of different costs and has interesting computational properties, since it can be used with the linear operator semantic framework presented below.

The operator  $\otimes$  on Q defines the global cost of a sequence of transitions,  $\sigma \rightarrow^{q_1} \ldots \rightarrow^{q_n} \sigma'$  simply as  $q = q_1 \otimes \ldots \otimes q_n$ . This is written  $\sigma \stackrel{x}{\Rightarrow}^q \sigma'$  where x is a sequence of states that has  $\sigma$  (resp.  $\sigma'$ ) as first (resp. last) state.

There may be several ways to reach a state  $\sigma'$  from a state  $\sigma$ , due to the presence of loops and non-determinism in the semantics. Let the set of possible paths be  $X_{\sigma,\sigma'} = \{x \mid \sigma \stackrel{x}{\Rightarrow}^{q_x} \sigma'\}$ . The global cost between  $\sigma$  and  $\sigma'$  is defined, using the operator  $\oplus$  on Q, to be  $q = \bigoplus_{x \in X_{\sigma,\sigma'}} q_x$ . Formally, the two operators have to fulfill the conditions of a (commutative) dioid.

**Definition 1** A commutative dioid is a structure  $(Q, \oplus, \otimes)$  such that

- 1. Operator  $\otimes$  is associative, commutative and has a neutral element e. Quantity e represents a transition that costs nothing.
- 2. Operator  $\oplus$  is associative, commutative and has  $\perp$  as neutral element. Quantity  $\perp$  represents the impossibility of a transition.
- 3.  $\otimes$  is distributive over  $\oplus$ , and  $\perp$  is absorbing element for  $\otimes$   $(\forall x.x \otimes \perp = \perp \otimes x = \perp)$ .
- 4. The preorder defined by  $\oplus$  ( $a \leq b \Leftrightarrow \exists c : a \oplus c = b$ ) is an order relation (i.e. it satisfies  $a \leq b \land b \leq a \Rightarrow a = b$ ).

By nature, a dioid cannot be a ring, since there is an inherent contradiction between the fact that  $\oplus$  induces an order relation and the fact that every element has an inverse for  $\oplus$ . Dioids are naturally equipped with a (Sup-)topology, namely the topology of limits of ascending chains [18]. Everywhere in this paper, continuity will be related to that topology. The following lemma is a classical result of dioid theory [18, Proposition 6.1.7].

**Lemma 1**  $\oplus$  and  $\otimes$  preserve the order  $\leq$ , *i.e.*, for all  $a, b, c \in Q$  with  $a \leq b$ ,

 $a \otimes c \leq b \otimes c$  and  $a \oplus c \leq b \oplus c$ 

If several paths go from some state  $\sigma$  to a state  $\sigma'$  at the same cost q, we will require that the global cost is also q, *i.e.* the global costs between two states does not depend on the number of paths joining them. In other words, we work with idempotent dioids.

**Definition 2** A dioid  $(Q, \oplus, \otimes)$  is idempotent if  $q \oplus q = q$  for all q in Q.

carrier set	$\oplus$	$\otimes$	$\sqrt[n]{q}$
$\mathbb{R}_+ \cup \{+\infty\}$	max	×	$q^{\frac{1}{n}}$
$\mathbb{Q} \cup \{+\infty, -\infty\}$	$\max$	+	$\frac{q}{n}$
$\mathbb{R} \cup \{+\infty, -\infty\}$	$\min$	+	$\frac{\tilde{q}}{n}$
$\mathbb{Q}\cup\{+\infty,-\infty\}$	$\min$	$\max$	q
$\mathbb{R} \cup \{+\infty, -\infty\}$	$\max$	$\min$	q
$\mathcal{P}(S)$	$\cap$	U	q

Figure 1: Some examples of cost dioids

For instance,  $(\overline{\mathbb{R}}, \max, +)$  and  $(\overline{\mathbb{R}}, \min, +)$  are idempotent dioids, where  $\overline{\mathbb{R}}$  stands for  $\mathbb{R} \cup \{-\infty, +\infty\}$ . The induced orders are, respectively, the orders  $\leq$  and  $\geq$  over real numbers, extended to  $\overline{\mathbb{R}}$  in the usual way. Note that in an idempotent dioid  $a \leq b \Leftrightarrow a \oplus b = b$ . Idempotent dioids are also called tropical semirings in the literature, and the notions of idempotent dioid and idempotent semirings are often confounded.

The use of residuation theory in Section 3 impose the assumption that our dioids are complete [7], in order to be able to define a pseudo-inverse for linear operators.

**Definition 3** An idempotent dioid is complete if it is closed with respect to infinite sums<sup>1</sup>, and the distributivity law holds also for an infinite number of summands.

A complete dioid is naturally equipped with a top element, that we shall write  $\top$ , which is the sum of all elements in the dioid.

The notion of long-run cost we will define in Section 4 relies on the computation of an average cost along the transitions of a cycle. This requires the existence of a nth root function.

**Definition 4** A dioid  $(Q, \oplus, \otimes)$  is equipped with a nth root function if for all qin Q, equation  $X^n = q$  always has a unique solution in Q, which will be denoted by  $\sqrt[n]{q}$ . Moreover, this function is required to be continuous.

A sequence containing *n* transitions, each costing, on the average,  $\sqrt[n]{q}$ , will thus cost *q*. Some examples of *n*th root can be found in Figure 1. We summarize the required conditions for our structure in the following definition.

**Definition 5** A cost dioid is a complete and idempotent commutative dioid, equipped with an nth root operation.

For instance,  $(\mathbb{R}, \max, +)$  is a cost dioid that may be used for the definition of the Worst Case Execution Time: when two states can be joined by several sequences of transitions which cost different times, the worst time is taken. To compute the cost of a sequence of transitions, we sum the costs of each transition. Figure 1 list some examples of cost dioids.

<sup>&</sup>lt;sup>1</sup>If we see the operator  $\oplus$  as a least upper bound, this notion is analogous to that of a *complete semi-lattice* in classical lattice theory.

#### 2.2 Semantics as linear operators over dioids

The upshot of using the adequate cost dioid is that the cost computation can be defined in terms of matrix operations in this dioid. The set of one-step transitions can be equivalently represented by a *transition matrix*  $M \in \mathcal{M}_{\Sigma \times \Sigma}(Q)$  with

$$M_{\sigma,\sigma'} = \begin{cases} q & if \ \sigma \to^q \sigma' \\ \perp & otherwise \end{cases}$$

Here,  $\mathcal{M}_{\Sigma \times \Sigma}(Q)$  stands for the set of matrices with rows and columns indexed over  $\Sigma$ , and values in Q. This set of matrices is naturally equipped with two operators  $\oplus$  and  $\otimes$  in the classical way: operator  $\oplus$  is extended pointwise, and operator  $\otimes$  corresponds to the matrix product (note that the iterate  $M^n$  embed the costs for paths of length n). The resulting structure is also an idempotent and complete dioid. The order induced by  $\oplus$  corresponds to the pointwise extension of the order over Q:  $M \leq M' \Leftrightarrow \forall i, j.M_{i,j} \leq M'_{i,j}$ . A transition matrix may also be seen as a linear operator on the moduloid  $Q(\Sigma)$ , as defined below.

**Definition 6** Let  $(E, \oplus, \otimes)$  is a commutative dioid. A moduloid over E is a set V equipped with an internal operation  $\oplus$  and an external operation  $\odot$  such that

- 1.  $(V, \oplus)$  is a commutative monoid, with 0 as neutral element;
- 2. the  $\odot$  operator ranges from  $E \times V$  to V, and verifies

$$\begin{aligned} (a) \ \forall \lambda \in E, \forall (x,y) \in V^2, \lambda \odot (x \oplus y) &= (\lambda \odot x) \oplus (\lambda \odot y), \\ (b) \ \forall (\lambda,\mu) \in E^2, \forall x \in V, (\lambda \oplus \mu) \odot x &= (\lambda \odot x) \oplus (\mu \odot x), \\ (c) \ \forall (\lambda,\mu) \in E^2, \forall x \in V, \lambda \odot (\mu \odot x) &= (\lambda \otimes \mu) \odot x, \\ (d) \ \forall x \in V, e \odot x &= x \ and \perp \odot x &= 0, \\ (e) \ \forall \lambda \in E, \lambda \odot 0 &= 0. \end{aligned}$$

For instance, if n is a given integer,  $E^n$ , set of vectors with n components in E, is a moduloid where the internal vector sum is the pointwise extension of the sum of E. If E is an idempotent dioid, then any moduloid V over E is also an idempotent dioid, equipped with a canonical order defined from the  $\oplus$  operation.

#### 2.3 Running example: quantitative semantics

We illustrate the notions of cost and quantitative semantics on a simple bytecode language, inspired by the Java Card language. Figure 2 shows part of the factorial program written in this language (together with its source code). The quantity we are interested in is the number of cache misses related to read accesses (read miss behaviour). In order to describe the read miss behaviour of programs, we extend the semantics of a simple bytecode language [21, 16, 6] with a cache model and with quantities expressing the number of read misses.

A state in our semantics contains a heap, a call stack of frames, and within each frame an instruction pointer for the current method, an array of local variables and an operand stack. In addition to these standard elements, a state also contains a set of *logical addresses*, representing which values are present in the cache at this point of the execution. This set is managed similarly to the cache. For example, the maximum size of this set will correspond to the size of the physical cache, and the replacement policy will model the one provided by the cache (e.g. LRU, FIFO). The cache description is hidden in a function C' = update(C, [access]) where C and C' denote the cache before and after a transition, respectively, and where [access] is a list of memory accesses.

Memory is accessed with two operators (a read or write access) which take two parameters, specifying what volume of data is to be accessed, and where these data are stored. For example,  $read_{\tau}(\texttt{heap}.3.x)$  means that data of type  $\tau$  is read at the address heap. 3.x, *i.e.* field x of the third object in the heap. In the same way, stack.frameId.n points to the n-nth element in the operand stack of a given frame, and local. frameId.local points to a local variable in a certain frame. We give an example of a semantic rule: the load instruction, which loads a typed local variable, indexed by i, on the top of the operand stack. The first two hypotheses of the rule correspond to the standard semantics. The third and fourth rules define how the cache evolves when executing a load. The fifth hypothesis computes the cost. Some other rule examples can be found in appendix A.

\_\_\_

Source	Bytecode	
x=1;	1: push 1	
	2: store x	
for (i=2;	3: push 2	
	4: store i	
$\ldots i <= n; \ldots$	5: load i	
	6: load n	
	7: if $\leq$ goto 14	
$x = x^* i;$	8: load x	
	9: load i	
	10: numop mul	
	11: store x	
$\dots i++)$	12: inc i	
	13: goto 5	
return x;	14: load x	
	15: return	

Figure 2: Factorial program

$$\begin{split} InstrAt(m,ip) &= \texttt{load} \ \tau \ i \land L[i] = d \\ S' &= d :: S \land size(S) = t \\ access &= [read_{\tau}(\texttt{local}.f.i); write_{\tau}(\texttt{stack}.f.t+1)] \\ C' &= update(C, access) \\ q &= nbRmiss(C, access) \\ \hline \langle H, \ll f, m, ip, L, S \gg :: fr, C \rangle \rightarrow^{q} \langle H, \ll f, m, ip+1, L, S' \gg :: fr, C' \rangle \end{split}$$

The number of read misses depends on the current state of the cache and the way it is accessed. This is defined precisely by the function nbRmiss(C, access)that computes the number of read misses generated by the list of memory accesses *access* if the cache at the beginning of the instruction is C. Here is the pseudocode of function *nbRmiss*.

$$nbRmiss \ c \ [] = 0$$
$$nbRmiss \ c \ [a/r] = nbRmiss \ (update \ c \ [a]) \ r + \begin{cases} 1 \ if \ a = read \ m \ and \ m \notin c \\ 0 \ otherwise \end{cases}$$

# 3 Abstraction

The transition matrix representing a program is of finite dimension but can still be so large that its representation in memory and computation involving it become infeasible. To overcome this problem, we define an abstract matrix that can be used to approximate the computations of the original matrix. For example, if we compute the minimum memory needed to run a program, a correct approximation of this quantity must be greater than the effective minimum. In this section, we give a sufficient condition for this approximation to be correct with the ordering induced by the dioid. To prove the correctness of an abstraction with respect to a concrete semantics, we re-state the classical abstract interpretation theory [11] in terms of dioids.

#### 3.1 Abstraction over Idempotent Dioids

In the following, C will denote a set of *concrete* states and D a set of *abstract* states. Let M be the linear operator on the concrete domain C, over the cost dioid  $(Q, \oplus, \otimes)$ , corresponding to the transition system of a program P. An abstraction function maps concrete states in C to their abstraction in D. Given an abstraction function  $\alpha$ , we can lift it to a linear abstraction operator  $\alpha^{\uparrow} \in \mathcal{M}_{D \times C}(Q)$  by setting

$$\alpha_{d,c}^{\uparrow} = \begin{cases} e \text{ if } \alpha(c) = d \\ \perp \text{ otherwise} \end{cases}$$

In what follows,  $\alpha^{\uparrow}$  will be denoted by  $\alpha$  when no confusion can arise and  $\leq_D$  will stand for the order defined on  $\mathcal{M}_{D \times D}(Q)$  in Section 2.2.

**Definition 7** Let  $(C, \leq_C)$  and  $(D, \leq_D)$  be two partially ordered sets. Two mappings  $\alpha : C \mapsto D$  and  $\gamma : D \mapsto C$  satisfying  $\forall c \in C, \forall d \in D, c \leq_C \gamma(d) \iff \alpha(c) \leq_D d$ , are called a Galois connection  $(C, \alpha, \gamma, D)$ .

As above,  $\alpha$  is an abstraction function, while  $\gamma$  is called a concretization function. An equivalent definition is:

**Definition 8** Let  $(C, \leq_C)$  and  $(D, \leq_D)$  be two partially ordered sets, and  $\alpha$ :  $C \mapsto D$  and  $\gamma : D \mapsto C$  two monotonic mappings. Then  $(C, \alpha, \gamma, D)$  forms a Galois connection if and only if  $\alpha \circ \gamma \leq Id_D$  and  $Id_C \leq \gamma \circ \alpha$ .

In our setting, the partial orders will be the orders induced by the  $\oplus$  operators over vectors in a moduloid. The question that naturally arises is that of the existence of a concretization function, given an abstraction  $\alpha$ . In [14], Di Pierro and Wicklicky, describe the framework of Probabilistic Abstract Interpretation. The abstraction function is a linear operator over the semiring of probabilities and they obtain a concretization function through the Moore-Penrose pseudoinverse. Here, we will use the theory of *residuation* to get a kind of inverse for  $\alpha$ . As we are not able to define an exact inverse in the general case, nor to apply the Moore-Penrose pseudo-inverse, we use the following definition.

**Definition 9** Let E and F be two partially ordered sets, f a mapping from E to F, and b an element of F. If the set  $\{x \in E \mid f(x) \leq b\}$  has a least upper bound, and if f is continuous then  $f^{\dagger}(b) = lub\{x \in E \mid f(x) \leq b\}$  is the greatest element x such that  $f(x) \leq b$  (the greatest sub-solution of equation f(x) = b).

If such a  $f^{\dagger}(b)$  exists for all b in F, then we say that f is residuable, and  $f^{\dagger}$  is its sup-pseudo-inverse (or pseudo-inverse for short).

The following propositions are immediately derived from this definition.

**Proposition 1** If  $f^{\dagger}$  (as defined above) exists, then it is the unique monotonic function such that  $f \circ f^{\dagger} \leq Id_F$  and  $Id_E \leq f^{\dagger} \circ f$ . As a consequence, we have:

 $f\circ f^{\dagger}\circ f=f \qquad and \qquad f^{\dagger}\circ f\circ f^{\dagger}=f^{\dagger}$ 

This last property [5] is particularly interesting for our purpose, since it will alow us for giving a pseudo-inverse for linear operators.

**Proposition 2** If E and F are complete ordered sets of respective smallest elements  $\perp_E$  and  $\perp_F$ , a monotonic mapping f from E to F is residuable iff f is continuous and  $f(\perp_E) = \perp_F$ .

Recall that the set of vectors over a complete idempotent dioid is itself a complete idempotent dioid, thus a complete ordered set under the canonical order. In the case of a linear abstraction function, we will immediately get the wanted result.

**Theorem 1** Let C and D be the domains of concrete and abstract states,  $\alpha$  a mapping from C to D, and  $\alpha^{\uparrow} \in \mathcal{M}_{D \times C}(Q)$  the linear mapping obtained by lifting  $\alpha$ . There exists a unique monotonic  $\alpha^{\dagger}$  such that

 $\alpha^{\uparrow} \circ \alpha^{\dagger} \leq Id \qquad and \qquad Id \leq \alpha^{\dagger} \circ \alpha^{\uparrow}.$ 

**Proof.** As  $\alpha^{\uparrow}$  is linear, it trivially fulfills the requirements of Proposition 2.

#### 3.2 Induced abstract semantics

The set of possible abstractions has to be restricted in order to ensure consistency w.r.t. the initial semantics. The following definition of a correct abstraction will ensure that the long-run cost of a program, as defined in the next section, will be correctly over-approximated during the abstraction process.

**Definition 10 (Correct abstraction)** Let  $M \in \mathcal{M}_{C \times C}(Q)$  be a linear operator in the concrete domain C,  $M^{\sharp} \in \mathcal{M}_{D \times D}(Q)$  a linear operator in the abstract domain D, and  $\alpha$  an abstraction from C to D. The triple  $(M, M^{\sharp}, \alpha)$  is a correct abstraction from C to D if

$$\alpha \circ M \leq_D M^{\sharp} \circ \alpha$$

The classical framework of abstract interpretation gives a way to define a best correct abstraction for a given concrete semantic operator. In the same way, given an abstraction  $\alpha$  and a concrete semantics linear operator, we can define an abstract semantics operator that is correct by construction, as expressed by the following proposition.

**Proposition 3** Let  $\alpha$  be an abstraction from C to D, and  $M \in \mathcal{M}_{C \times C}(Q)$  be a linear operator over the concrete moduloid. We set

$$M^{\sharp} = \alpha \circ M \circ \alpha^{\dagger}$$

Then  $(M, M^{\sharp}, \alpha)$  is a correct abstraction from C to D.

**Proof.** The proof immediately follows from the fact that  $Id \leq \alpha^{\dagger} \circ \alpha$ .  $\Box$ 

#### 3.3 Running example: abstraction

In 2.3, we introduced a quantitative semantics describing the number of cache misses in read access. M is the matrix describing this quantitative semantics for the factorial program (see Figure 2 for the code). The exact computation of the semantics would be too costly. In this subsection, we are using the abstractions techniques in order to compute an abstract semantics  $M^{\sharp}$  from the matrix M.

We abstract a concrete state by the instruction pointer and the k last data accessed. Within this abstract domain, the loss of information lies in three points:

- Values (*i.e.* locals, stack and heap) are forgotten. This prevents us from determining the value of branching condition.
- The cache size is reduced to k elements. When k grows, precision increase, and so do the cost of the analysis.
- The method call stack is forgotten. We turn the analysis into an intraprocedural one, not for efficiency but for clearer notation, as our factorial function involves only one non-recursive function.

We write the abstract state as  $(ip, [v_1, \ldots, v_k])$  where ip is the instruction pointer and  $[v_1, \ldots, v_k]$  is a list of logical addresses of the last data accessed,  $v_k$  being the most recent. **s**.0 refers to the bottom element of the local stack, **1**.f refers to the local variable called x in the source code. We model the maximum number of read misses using the dioid  $Q = (\overline{\mathbb{R}}, \max, +)$ .

We construct the abstract matrix associated to our abstract system. Its size is bounded in terms of the cardinality of  $\mathcal{I}$ , the set of all instruction pointers appearing in program P, and the number of of up-to-k-combinations of the different logical data used in this function (which form a finite set  $\mathcal{L}$ ). A value  $q^{\sharp}$  of this matrix, standing at row  $a^{\sharp}$  and column  $b^{\sharp}$  ( $a^{\sharp}$  and  $b^{\sharp}$  are two abstract states), is computed in this way: let A and B be the set of concrete states abstracted by  $a^{\sharp}$  and  $b^{\sharp}$ . Then  $q^{\sharp} = \bigoplus \{q | a \to {}^{q} b, a \in A, b \in B\}$ . For example

•  $\sigma = (8, [1.x]) \rightarrow^0 (9, [1.x, s.0]) = \sigma',$ 

Whatever the concrete state and its precise values, if it is abstracted by  $\sigma$ , then it can turn into a state abstracted by  $\sigma'$  for a cost of 0 read miss.

 σ = (8, [s.0]) →<sup>1</sup> (9, [1.x, s.0]) = σ', In the same way, all states abtracted by σ can generate up to a read miss on their next instruction, turning into states abstracted by σ'.

We recall that in the factorial program, instruction 8 is load x. The load rule of the semantics describes these accesses to the cache.

$$[read_{\tau}(\texttt{local}.f.i); write_{\tau}(\texttt{stack}.f.t[+1])]$$

with i the local variable, t the current stack height and f the current frame.

A  $\perp$ -transition denotes an incompatibility between the two abstract states, either in its control flow or its the cache evolution. Most of the matrix will be filled by  $\perp$ . This kind of matrix is called sparse matrix, and permits the use

of particularly small representations together with efficient algorithms. We give here a submatrix of the abstract matrix.  $M^{\sharp} \in \mathcal{M}_{(\mathcal{I} \times \{\emptyset \cup \mathcal{L} \cup \mathcal{L}^2\})^2}(Q)$ .

	(	$9, [1.x, s.0]$	$\dots$ 9, [1. <i>i</i> , 1. <i>x</i> , s.0]	<u></u> )
	:			
	8, []	1	$\perp$	
	8, [1.x]	0	$\perp$	
M <sup>♯</sup>	8, [s.0]	1	$\perp$	
<i>IVI</i> " —	8, [s.0, 1.x]	0	$\perp$	
	8, [1.x, s.0]	0	$\perp$	
	8, [1.i, 1.x]	$\perp$	0	
	8, [1.i]	$\perp$	1	
				)

### 4 Long-run cost

So far, we have seen that all single-transition costs can be summarized in a transition matrix. We now use this matrix and the mathematical results of dioid algebra to define a notion of long run cost for a whole program. In [22] we proposed a notion of global cost of a program, representing its cost from initial to final states. It correctly deals with programs which are meant to terminate, but for some program and abstraction, the global cost turned to be  $\top$ . The cost  $\top$  is rather unsatisfactory as it means that the concrete cost can be anything. For this case and for the case of programs which are not meant to terminate (as reactive systems), we propose the notion of *long-run cost*, that represents a maximal average cost over cycles of transitions. The notion of long-run behaviour is taken from [1, 9], in the context of probabilistic processes modelled by Markov decision processes. Behaviour patterns of interest (described by labelled graphs) are associated to real numbers representing the success or the duration of the pattern, and extensions of branching time temporal logics are proposed in order to measure their long-run average outcome.

The average cost of a finite path is defined as the arithmetical mean (w.r.t. the  $\otimes$  operator) of the costs labelling its transitions. In other words, it is the *n*th root of the global cost of the path, where *n* is its length. We write  $\tilde{q}(\pi) = \sqrt[|\pi|]{q(\pi)}$  for the average cost of  $\pi$ , where  $q(\pi)$  is the global cost of the path  $\pi$ , and  $|\pi|$  its length. If this path is a cycle, we will talk about the *cycle average cost*. The "maximum" average cost of all cycles in the graph will be the quantity we are interested in: this quantity will be called *long-run cost*. The following example illustrates these notion on a simple graph.



Average cost of path abc = (8+3)/2 = 5.5Cycle bcdb average cost = (3+4+5)/3 = 4Cycle cc average cost = 2/1 = 2Long-run cost = max(4, 2) = 4

By the properties of the dioids, the matrix  $M^k$  sums up the transition costs of all paths of length k. The diagonal of this matrix thus contains the costs of all cycles of length k. If we sum up all the elements on this diagonal, we get the trace of the matrix. This observation gives rise to the following definition. **Definition 11** Let P be a program with cost matrix M and let R be M restricted to the set of reachable states  $\Sigma$  of P. The long-run cost of program P is defined by

$$\rho(P) = \bigoplus_{k=1}^{|\Sigma|} \sqrt[k]{tr \, R^k}$$

where  $tr R = \bigoplus_{i=1}^{|\Sigma|} R_{i,i}$ 

For instance, if we work in the dioid (*Time*, max, +), where *Time* is isomorphic to  $\overline{\mathbb{R}}$ ,  $\rho(P)$  is the maximal average of time spent per instruction, where the average is computed on any cycle by dividing the total time spent in the cycle by the number of instructions in this cycle. We note in the passing that the definition of the long-run cost coincides with the definition of the maximum of eigenvalues of the matrix, in the case of an irreducible matrix in an idempotent semiring [17].

In order to relate the matrix definition of long-run cost with the trace semantics, we consider two properties of the nth-root operation.

Lemma 2 All dioids displayed on Figure 1 respect

- (i)  $\forall a, b \in Q, \forall n > 0, \sqrt[n]{a \oplus b} = \sqrt[n]{a} \oplus \sqrt[n]{b},$
- (*ii*)  $\forall a, b \in Q, \forall n, m > 0, \sqrt[n]{a} \oplus \sqrt[m]{b} \ge \sqrt[n+m]{a \otimes b}.$

The following proposition establishes a link between the computable matrix definition of  $\rho(P)$  and the cycles of the semantics.

**Proposition 4** Let  $\Gamma$  be the set of cycles appearing in the trace semantics of P, defined over a cost dioid with Properties i and ii of Lemma 2. Then

$$\rho(P) = \bigoplus_{c \in \Gamma} \sqrt[|c|]{q(c)}$$

The idea of the proof is to show that the cycles of length less than  $|\Sigma|$  are enough to know average costs, and that a partition of these cycles is related with the different iterates of the matrix appearing in Definition 11.

Now, Theorem 2 is proved for dioids which operator  $\otimes$  is the arithmetical +, so which *n*th root operator is the division per *n*. The theorem relates the meaning of  $\rho(P)$  to the asymptotic behaviour of the program, in terms of the maximum of average transition cost for traces which length tends to infinity. To establish this result, we have to show that the cost of a prefix of a trace becomes negligible when this trace becomes arbitrarily long. We thus impose the following hypothesis:

**Hypothesis 1** All transitions  $\delta$  which do not belong to a cycle are such that:

 $q(\delta) \neq \top$ 

Hypothesis 1 excludes certain pathological matrices with atomic operations that have infinite costs. If a cycle contains a  $\top$  transition, the  $\rho$  value indicates it.

{a}

**Theorem 2** Let  $\llbracket P \rrbracket_{tr}^n$  be the subset of all traces of length n in the trace semantics of P, defined over a cost dioid which operation  $\otimes$  is the arithmetical +. With Hypothesis 1 and Properties i and ii of Lemma 2, we have

$$\rho(P) = \lim_{n \to \infty} \bigoplus_{t \in [\![P]\!]_{tr}^n} \sqrt[|t|]{q(t)}$$

This theorem establishes a link between the semantics and a computable definition of the long-run cost. The key points of the proof are to ensure that this limit exist, and to show that a small part of a trace can be neglected for very long traces. This is proved by bounding  $\bigoplus_{t \in \llbracket P \rrbracket_{tr}^n} \sqrt[|t|]{q(t)}$ . (Proofs of Proposition 4

and Theorem 2 can be found in appendix B.)

A dioid which could not fulfill Theorem 2 is  $(\mathcal{P}(S), \cap, \cup)$ . This dioid could be used *e.q.* to analyse if certain portions of

the code are executed for sure; however, in this setting,  $\rho(P)$  does not coincide with the asymptotic behaviour of P. The diagram on the right illustrates this problem: While  $\rho(P) = \emptyset$ , all traces long enough include {**a**}, and so does the intersection of their cost (the black dot is the initial state).

#### 4.1 Ensuring correctness

The question that naturally arises is to know if the notion of long-run cost is preserved by abstraction. The following theorem states that a correct abstraction gives an over-approximation of the concrete long-run cost.

**Theorem 3** If  $(M, M^{\sharp}, \alpha)$  is a correct abstraction, then

$$\rho(M) \leq_Q \rho(M^\sharp)$$

The proof of theorem relies on the fact that the correctness is preserved when the concrete and abstract matrices are iterated simultaneously. *Proof can be found in appendix C.* 

#### 4.2 Running example: long-run cost

To illustrate the use of the long-run cost  $(\rho)$ , we will consider a cache which can contain 4 integers; its replacement policy is still LRU. Such a small size could seem weird to the reader and unrealistic for a cache size, but the term cache can be interpreted here as some kind of registers. The semantics of the factorial program is abstracted as described in Section 3.3, with k = 4.

Using Definition 11, we compute  $\rho(M^{\sharp}) = 2/9$ , meaning that in an execution long enough, we observe on average 2 cache misses each 9 instructions.

Note that if we now consider a FIFO replacement policy for the same 4 integer registers, we obtain a different long-run cost. The FIFO policy implementation is cheaper in electronic components than the LRU one, but the analysis of the factorial function says that  $\rho = 4/9$ , *i.e.*, that we now have on average 4 cache misses for 9 instruction executed. Such a slowdown is coherent with the observations that small cache memory requires more advanced cache policies.

## 5 Related work

The present work is inspired by the quantitative abstract interpretation framework developed by Di Pierro and Wiklicky [14]. We have followed their approach in modeling programs as linear operators over a vector space, with the notable technical difference that their operators act over a semiring of probabilities whereas ours work with idempotent dioids. Working with idempotent dioids means that we have been able to exploit known results from Discrete Event Systems theory which makes intensive use of such structures. Another difference with respect to [14] lies in the kind of program being analyzed: we have been considering an intermediate bytecode language rather than declarative languages (probablistic concurrent constraint programming and the lambda calculus [13]).

In Di Pierro and Wiklicky's work, the relation with abstract interpretation is justified by the use of the pseudo-inverse of a linear operator, similar to a Galois connection mechanism, enforcing the soundness of abstractions. In a different way, our approach can also be seen as abstract interpretation, since it follows the core principle of expressing all semantics (concrete or abstract) as fixpoints of monotonic operators in partially ordered structures [20]. In Sections 2, the order is  $\leq_D$ , the operator M modeling the program is monotonic for it, and the  $M^+$  can be seen as a fixpoint. Similarly, in Section 3 we extend the basic principle that semantics designed by abstraction are guaranteed to be sound, by giving guarantees of soundness under the assumption  $\alpha \circ M \leq_D M^{\sharp} \circ \alpha$ , which is a classical requirement in abstract interpretation.

Several other works make use of idempotent semiring for describing quantitative aspects of computations, namely under the form of constraint semirings [8]. Recently, these have been used in the field of Quality of Services [12], in particular with systems modelled by graph rewriting mechanisms [19]. In all these approaches, the  $\oplus$  and  $\otimes$  operators of the constraint semiring are used for combining constraints. In [4], Aziz makes use of semirings in a mobile process calculus derived from the  $\pi$ -calculus, in order to model the cost of communicating actions. He also defines a static analysis framework, by abstracting "concrete" semirings into abstract semirings of reduced cardinality, and defining abstract semiring operators accordingly. For instance, the ( $\mathbb{R}_+ \cup \{+\infty\}$ , min, +) semiring can be abstracted by a ( $\{low, medium, high\}$ , min, max) one. This is similar to our use of dioids, but none of these approaches makes use of a notion of long-run cost to express an average quantitative behaviour of a system.

Our running example of estimating cache usage is meant for illustrative purposes and is based on a rather abstract view of cache analysis, compared e.g. to the detailed modeling and cache abstraction of Alt, Ferdinand, Martin, and Wilhelm [2] who have proposed a cache behaviour prediction by abstract interpretation. In our proposition, the whole cache model is hidden behind the function *update*, which still has to be defined. Three points of their work could be almost directly used in our framework: the various models of cache (e.g. direct-mapped, A-way associative) to implement our update function, their abstract domain, in order to design our quantitative abstractions, and their observations about caches and writing, in order to develop an accurate model.

# 6 Conclusion

We have shown how to abstract the long-run cost of programs whose operational semantics is defined as transition systems labelled by costs taken from a particular kind of dioids. In such cases, we have shown that the semantics is a linear operator over the moduloid associated to this dioid. We have used a well-known characterization of the asymptotic behaviour of a discrete event system to define the notion of long-run cost of such a semantics, and proposed a novel way of analyzing the long-run behaviour of the program. We have characterized this long-run cost as being a maximal average cost per transition on very long traces of the semantics. Computing the exact long-run cost of a program is in general too expensive, so we have extended the linear operator framework with a notion of abstraction of the semantics which is also expressed as a linear operator. A correctness relation between concrete and abstract semantic matrices ensures that the cost computed from the abstract semantics is an over-approximation of the concrete one. The notions of dioids, quantitative semantics, abstraction and long-run cost have been illustrated all along the paper through a cache miss analysis on a program written in a simple bytecode language.

**Future work** The examples of cost computations in the paper have been done by hand. Future work includes means of getting a fully-automated computation, by integrating the analysis technique with existing libraries e.g. for max-plus algebras, that offer efficient ways to compute the asymptotic behaviour (through sparse matrices and Howard's algorithm for eigenvalues [10]).

An interesting avenue for further work would be to relax the correctness criterion so that the abstract estimate is "close" to (but not necessarily greater than) the exact quantity. For certain quantitative measures, a notion of "closeness" might be of interest, as opposed to the qualitative case where static analyses must err on the safe side. Work in this direction has been reported by Di Pierro and Wiklicky [15], where the difference between the concrete and abstract operator is estimated through a metric. However, such a notion still has to be defined for the dioids considered here.

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# A Some rules of a bytecode quantitative semantics

We have developed a semantics for a small object-oriented bytecode language, inspired from the Carmel formalization of the Java Card instructions. We haven't dealt with features like exceptions and subroutines but we have treated enough instructions to provide a representative set of rules. Three of these rules are given in below.

#### Unconditional jump.

The goto a instruction causes an unconditional jump, within the current method. Execution proceeds at an offset a from the address of this goto instruction.

$$\frac{InstrAt(m,ip) = \texttt{goto } a}{\langle H, \ll f, m, ip, L, S \gg :: fr, C \rangle \rightarrow^0 \langle H, \ll f, m, ip + a, L, S \gg :: fr, C \rangle}$$

#### Setfield for the current object.

The setfield this  $\tau$   $i_{s}$  instruction sets the field, typed  $\tau$ , pointed at by  $i_{s}$ , of the current object (this) to the top operand stack value. In Java, a reference to the current object is stored in the local variables at index 0.

#### Typed return.

The **return**  $\tau$  instruction ends the current method call by returning the control and a value of type  $\tau$  to the invoker. The current stack frame is popped after its top operand has been placed on the operand stack of the invoker.

$$\begin{aligned} &InstrAt(m2, ip2) = \texttt{return } \tau \land S2 = v_{\tau} :: Sr2\\ &S1' = v_{\tau} :: S1 \land C' = update(C, access)\\ &q = nbRmiss(C, access)\\ \hline &< H, \ll f2, m2, ip2, L2, S2 \gg :: \ll f1, m1, ip1, L1, S1 \gg :: fr, C >\\ &\rightarrow q < H, \ll f1, m1, ip1 + 1, L1, S1' \gg :: fr, C' >\\ &\rightarrow q < H, \ll f1, m1, ip1 + 1, L1, S1' \gg :: fr, C' > \end{aligned}$$
Where 
$$\begin{cases} access = [read_{\tau}(\texttt{stack}.f2.t2); write_{\tau}(\texttt{stack}.f1.t1[+1])]\\ size(S2) = t2 \land size(S1) = t1 \end{cases}$$

### **B** Trace semantics and long run cost

This appendix contains the proof that the two alternatives formulations of long run cost given in 4 (Proposition 4 and Theorem 2) are equivalent to the original definition (Definition 11):

Let P be a program with cost matrix M and let R be M restricted to the set of reachable states  $\Sigma$  of P. The long-run cost of program P is defined by

$$\rho(P) = \bigoplus_{k=1}^{|\Sigma|} \sqrt[k]{tr \, R^k}$$

where  $tr R = \bigoplus_{1}^{|\Sigma|} R_{i,i}$ 

We first consider Proposition 4 which states

Let  $\Gamma$  be the set of cycles appearing in the trace semantics of P, defined over a cost dioid with Properties i and ii of Lemma 2. Then

$$\rho(P) = \bigoplus_{c \in \Gamma} \sqrt[|c|]{q(c)}$$

To improve the readability of the proofs, we introduce the following notation: for any path or cycle  $\pi, \tilde{q}(\pi)$  stands for  $|\Pi| \sqrt{q(\pi)}$ . To prove the proposition, we first establish Lemma 3 and Lemma 4.

By definition, a cycle is a path which starts and finishes in the same state, and contain at least one transition. As C is defined with respect to the trace semantics, it only contains reachable states all included in  $\Sigma$ , the finite set of reachable states of the semantics. Let  $\Gamma_{\leq |\Sigma|}$  be the set of cycles which length is less or equal than  $|\Sigma|$ .

#### Lemma 3

$$\forall c \in \Gamma, \exists \Gamma_c \subseteq \Gamma_{\leq |\Sigma|}, \quad \tilde{q}(c) \leq \bigoplus_{c_e \in \Gamma_c} \tilde{q}(c_e)$$

**Proof.** By strong induction on the length of *c*,

- If  $|c| \leq |\Sigma|$ , then  $\Gamma_c = \{c\}$  and the inequality trivially holds.
- If  $|c| > |\Sigma|$ , then there exists a state  $\sigma'$  such that

$$c = \sigma \stackrel{\pi_1}{\Rightarrow} \sigma' \stackrel{\pi_2}{\Rightarrow} \sigma' \stackrel{\pi_3}{\Rightarrow} \sigma \quad \text{with} \quad \left\{ \begin{array}{ccc} \pi_1 \pi_3 \in \Gamma & \wedge & |\pi_1 \pi_3| < |c| \\ \pi_2 \in \Gamma & \wedge & |\pi_2| < |c| \end{array} \right.$$

With these notations, we have

$$\tilde{q}(c) = \tilde{q}(\pi_1 \pi_2 \pi_3) = \sqrt[|c|]{q(\pi_1 \pi_2 \pi_3)} \\
= \sqrt[|c|]{q(\pi_1 \pi_3) \otimes q(\pi_2)} \\
\leq \sqrt[|\pi_1 \pi_3]{q(\pi_1 \pi_3)} \oplus \sqrt[|\pi_2]{q(\pi_2)} = \tilde{q}(\pi_1 \pi_3) \oplus \tilde{q}(\pi_2) \quad (2)$$

Inequality (1) is justified by the commutativity of  $\otimes$  and the definition of the cost of a path with respect to its decomposition. Inequality (2) follows

from Property ii of Lemma 2. The property we want to prove then holds by induction, with

$$\Gamma_c = \Gamma_{\pi_1 \pi_3} \cup \Gamma_{\pi_2}$$

Lemma 4

$$\bigoplus_{c\in\Gamma}\tilde{q}(c) = \bigoplus_{c_e\in\Gamma_{\leq |\Sigma|}}\tilde{q}(c_e)$$

**Proof.** We add some elements to the right-side of the inequality of Lemma 3, complementing the sum up to  $\Gamma_{\leq |\Sigma|}$  (remind that  $a \leq b \Rightarrow a \leq b \oplus c$ ). Hence

$$\forall c \in \Gamma, \tilde{q}(c) \le \bigoplus_{c_e \in \Gamma_c} \tilde{q}(c_e) \le \bigoplus_{c_e \in \Gamma_{\le |\Sigma|}} \tilde{q}(c_e)$$

Summing for all  $c \in \Gamma$  (remind the idempotency of  $\oplus$ ), we obtain that the maximal average transition cost for all cycles is lower or equal than the maximal average transition cost of a bounded subset of cycles, those no longer than  $|\Sigma|$ .

$$\bigoplus_{c\in\Gamma} \tilde{q}(c) \le \bigoplus_{c_e\in\Gamma_{\le|\Sigma|}} \tilde{q}(c_e) \tag{3}$$

As  $\Gamma_{\leq |\Sigma|} \subseteq \Gamma$ , the opposite inequality is also true, and we trivially have

$$\bigoplus_{c_e \in \Gamma_{\le |\Sigma|}} \tilde{q}(c_e) \le \bigoplus_{c \in \Gamma} \tilde{q}(c) \tag{4}$$

Combining Inequalities (3) and (4) proves the lemma. We now prove Proposition 4 itself.

**Proof.**  $\Gamma_{\leq |\Sigma|}$  can be partitioned into sets of cycles having same length (n) and same state as first vertex of a cycle  $(\sigma)$ . Note that  $n \geq 1$ , and that  $\sigma$  is reachable. We write  $C_n^{\sigma}$  for such a set.

$$\Gamma_{\leq |\Sigma|} = \bigcup_{\substack{n \leq |\Sigma| \\ \sigma \in \Sigma}} \mathcal{C}_n^{\sigma}$$

We have

$$\bigoplus_{c \in \Gamma_{\leq |\Sigma|}} \tilde{q}(c) = \bigoplus_{c \in \Gamma_{\leq |\Sigma|}} \sqrt[|c|]{q(c)} = \bigoplus_{n \leq |\Sigma|} \bigoplus_{\sigma \in \Sigma} \bigoplus_{c \in \mathcal{C}_n^{\sigma}} \sqrt[n]{q(c)}$$
$$= \bigoplus_{n \leq |\Sigma|} \sqrt[n]{\bigoplus_{\sigma \in \Sigma} \bigoplus_{c \in \mathcal{C}_n^{\sigma}} q(c)}$$

This step is allowed by Property i of Lemma 2, and by the fact that  $\Sigma$  is finite.

If  $\sigma$  is reachable, then all states in path starting from  $\sigma$  are reachable. Let R be matrix M where indices are restricted to  $\Sigma$ , the reachable states of P. We have:

$$\begin{split} R^n_{\sigma,\sigma} &= \bigoplus_{c \in \mathcal{C}^n_n} q(c) \\ \bigoplus_{c \in \Gamma} \sqrt[|c|]{q(c)} &= \bigoplus_{n \leq |\Sigma|} \sqrt[\eta]{\bigoplus_{\sigma \in \Sigma} R^n_{\sigma,\sigma}} \\ &= \bigoplus_{n \leq |\Sigma|} \sqrt[\eta]{\operatorname{tr} R^n} \\ &= \rho(P) \end{split}$$

We now prove Theorem 2, which states

$$\rho(P) = \lim_{n \to \infty} \bigoplus_{\substack{t \in \llbracket P \rrbracket \\ |t| = n}} \sqrt[n]{q(t)}$$

**Proof.** We prove the theorem by bounding  $\bigoplus_{t \in \llbracket P \rrbracket_{tr}^n} |t| \sqrt{q(t)}$ . The proof is written with "usual" arithmetic, in order to be more readable and intuitive. The  $\otimes$  operator is set to +, which implies that n times the same value q is n.q and that the nth root operator corresponds to division by n. The  $\oplus$  operator, and its associated order  $\leq$  may be interpreted either by max and  $\leq$  or by min and

 $\geq$ . We first handle the case where of  $\top$  value appears in the (reachable) cycles of the trace semantics. In that case, there exists a length  $n_0$  such that, for any n greater than  $n_0$ , it is possible to construct a trace of length n including this transition of maximum cost. The global and average cost of that trace are also equal to  $\top$ , which is equal to  $\rho(P)$  as well. The equality is thus trivially verified.

We now consider the case where  $\top$  does not appear at all in the reachable transitions of the semantics, and we bound  $\bigoplus_{t \in \llbracket P \rrbracket_{tr}^n} \frac{q(t)}{|t|}$ . Given a trace t of size  $n \ge N$ , we can decompose t

$$t = p_0.c_0.p_2 = 1.c_1...c_{k-1}.p_k \text{ where } \begin{cases} \text{ each } c_i \text{ is a cycle} \\ r = \sum_i |p_i| < N \\ \text{ each } p_i \text{ is acyclic} \end{cases}$$

For simplicity in the notations, we treat the case were k = 1, the general case is treated the same way. Trace t may thus be written as

$$t = p.c.s$$
 where  $\begin{cases} p = p_0, c = c_0, s = p_1 \\ r = |p| + |s| < N \end{cases}$ 

Proposition 4 says that the mean cost per transition in the cycles of P is upperbounded by the long run cost of P,  $\rho(P)$ . For a given program whose semantic graph contains cycles, let  $q_{-}$  and  $q_{+}$  be lower and upper bounds of all one-step transition costs of P. By definition of a possible transition,  $q_{-} \neq \bot$  and by hypothesis,  $q_{+} \neq \top$ . We have

$$q(t) = (q(p) + q(s)) + q(c) \le r.q_{+} + (n - r).\rho(P)$$
(5)

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This inequality is guaranteed by the fact that  $q_+$  and  $\rho(P)$  are upper bounds of, respectively, any single transition cost and the average transition cost in a cycle. By definition, r and n - r are positives. We thus have

$$q(t) = (q(p) + q(s)) + q(c) \le N. |q_+| + (n - A).\rho(P)$$
(6)

A being either N or -N, depending on the sign of  $\rho(P)$ . We sum (6) for all t of size n to get (7).

$$\bigoplus_{t \in \llbracket P \rrbracket_{tr}^n} q(t) \le N. |q_+| + (n - A).\rho(P)$$
(7)

This gives the upper bound.

t

Since we have restricted ourselves to the set of reachable states, we remark that for all n, there exists a trace in  $\llbracket P \rrbracket_{tr}^n$  such that, if this trace contains cycles, then they are critical, *i.e.* their average cost equals  $\rho(P)$ . We consider one of those traces. Let  $t_{\max} = p.c.s$ , such that p is cycle-free, the cycle c is only composed of critical cycles and suffix s is cycle-free. We have

$$q(t_{\max}) \le \bigoplus_{t \in \llbracket P \rrbracket_{tr}^n} q(t) \tag{8}$$

This inequation is trivially true, since  $t_{\max} \in \llbracket P \rrbracket_{tr}^n$  and  $\oplus$  is idempotent. We then explicit the cost of the trace and similarly find an under-approximation.

$$q(t_{\max}) = (q(p) + q(s)) + q(c)$$
  

$$q(t_{\max}) \ge N. |q_{-}| + (n - A).\rho(P)$$
(9)

This inequality is guaranteed by the fact that  $r.q_{-}$  is a lower bound for q(p.s), and that  $(n-r).\rho(P)$  is the exact cost of the path c. Inequalities (8) and (9) lead to

$$N. |q_-| + (n - A).\rho(P) \le \bigoplus_{t \in \llbracket P \rrbracket_{tr}^n} q(t)$$

$$\tag{10}$$

We combine inequalities (10) and (7) to get

$$N. |q_{-}| + (n - A).\rho(P) \le \bigoplus_{t \in [\![P]\!]_{tr}^n} q(t) \le N. |q_{+}| + (n - A).\rho(P)$$

which turns into

$$F(n) = \frac{N \cdot |q_-|}{n} + \frac{n-A}{n} \cdot \rho(P) \le \frac{\bigoplus_{t \in \llbracket P \rrbracket_{tr}^n} q(t)}{n} \le \frac{N \cdot |q_+|}{n} + \frac{n-A}{n} \cdot \rho(P) = G(n)$$

by dividing by n. Finally, as  $\frac{a}{n} \oplus \frac{b}{n} = \frac{a \oplus b}{n}$ , this yields

$$F(n) \le \bigoplus_{t \in \llbracket P \rrbracket_{tr}^n} \frac{q(t)}{n} \le G(n)$$
(11)

We now study the limit behavior of (11) when n tends to infinity.

$$\lim_{n \to \infty} F(n) = \lim_{n \to \infty} G(n) = \rho(P)$$

RR n° 6338

It implies

$$\lim_{n \to \infty} \bigoplus_{t \in [\![P]\!]_{tr}^n} \frac{q(t)}{n} = \rho(P)$$

written with "usual" arithmetic, *i.e.* using the semiring arithmetical notations

$$\lim_{n \to \infty} \bigoplus_{t \in \llbracket P \rrbracket_{tr}^n} \sqrt[n]{q(t)} = \rho(P)$$

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# C Correctness of the long-run cost

We prove Theorem 3 which states that in the case of a correct abstraction (Definition 10), we compute a correct long-run cost (Definition 11), *i.e.* 

$$\alpha \circ M \leq_D M^{\sharp} \circ \alpha \Rightarrow \rho(M) \leq_Q \rho(M^{\sharp})$$

We first prove Lemma 5 and 6.

**Lemma 5** If  $(M, M^{\sharp}, \alpha)$  is a correct abstraction then  $(M^n, (M^{\sharp})^n, \alpha)$  is a correct abstraction for all  $n \geq 1$ .

**Proof.** Lemma 5 states that:

$$\forall n \ge 1 \qquad \qquad \alpha \circ M \le_D M^{\sharp} \circ \alpha \Rightarrow \alpha \circ M^n \le_Q (M^{\sharp})^n \circ \alpha \qquad (12)$$

We prove (12) by induction on n. The case where n = 1 is trivial. We then assume that  $\alpha \circ M \leq_D M^{\sharp} \circ \alpha$  and  $\alpha \circ M^n \leq_D (M^{\sharp})^n \circ \alpha$ . Lemma 1 gives that

$$(\alpha \circ M^n) \circ M \leq_D ((M^{\sharp})^n \circ \alpha) \circ M$$

We then have

$$\begin{aligned} \alpha \circ M^{n+1} &= \alpha \circ (M^n \circ M) \leq_D (M^{\sharp})^n \circ (\alpha \circ M) & (associativity of \circ) \\ &\leq_D (M^{\sharp})^n \circ (M^{\sharp} \circ \alpha) & (hypothesis) \\ &\leq_D (M^{\sharp})^{n+1} \circ \alpha & (associativity of \circ) \end{aligned}$$

**Lemma 6** For all correct abstraction  $(M, M^{\sharp}, \alpha)$  and  $\alpha$  lifted as stated in Section 3,

$$\forall c \in C, d \in D, \qquad \alpha(c) = d \Rightarrow M_{c,c} \leq_Q (M^{\sharp})_{d,d}$$

**Proof.** We have  $\alpha \circ M \leq_D M^{\sharp} \circ \alpha$ , and so in particular  $(\alpha \circ M)_{d,c} \leq_Q (M^{\sharp} \circ \alpha)_{d,c}$  which rewrites into

$$\bigoplus_{c'\in C} (\alpha_{d,c'} \otimes M_{c',c}) \leq_Q \bigoplus_{d'\in D} ((M_{d,d'}^{\sharp}) \otimes \alpha_{d',c})$$

Decomposing both summations, this yields

$$\bigoplus_{c'\in\alpha^{-1}(d)} (\alpha_{d,c'}\otimes M_{c',c}) \oplus \bigoplus_{c'\notin\alpha^{-1}(d)} (\alpha_{d,c'}\otimes M_{c',c}) \\
\leq_Q ((M_{d,d}^{\sharp})\otimes\alpha_{d,c}) \oplus \bigoplus_{d'\neq d} ((M_{d,d'}^{\sharp})\otimes\alpha_{d',c})$$

Given the properties of  $\alpha$ , which is lifted from a function (see Section 3), that simplifies into  $\bigoplus_{c' \in \alpha^{-1}(d)} M_{c',c} \leq_Q M_{d,d}^{\sharp}$ . As c belongs to  $\alpha^{-1}(d)$ , we also have the inequality

$$M_{c,c} \leq_Q \bigoplus_{c' \in \alpha^{-1}(d)} M_{c',c} \leq_Q M_{d,d}^{\sharp}$$

RR n° 6338

Thanks to this result and to idempotency, we deduce that for any d,

$$\bigoplus_{c \in \alpha^{-1}(d)} M_{c,c} \leq_Q M_{d,d}^{\sharp}$$

By summing over d, we finally get

$$\bigoplus_{c \in C} M_{c,c} \leq_Q \bigoplus_{d \in D} M_{d,d}^{\sharp}$$

We now prove Theorem 3 itself.

**Proof.** By combining Lemma 5 and 6, we have for any  $k \ge 1$ ,

$$\bigoplus_{c \in C} M_{c,c}^k \leq_Q \bigoplus_{d \in D} (M^{\sharp})_{d,d}^k$$

$$tr M^k \leq_Q tr (M^{\sharp})^k$$
(13)

$$\sqrt[k]{tr\,M^k} \le_Q \sqrt[k]{tr(M^\sharp)^k} \tag{14}$$

Inequality 13 simply uses the definition of a trace. Inequality 14 holds because the *n*th root operation is order preserving. Summing this last inequality for all k from 1 to  $|\Sigma|$  yields

$$\bigoplus_{k=1}^{|\Sigma|} \sqrt[k]{tr \, M^k} \leq_Q \bigoplus_{k=1}^{|\Sigma|} \sqrt[k]{tr (M^\sharp)^k}$$

and hence we have that  $\rho(M) \leq_Q \rho(M^\sharp)$ 



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