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# Oz/K: A kernel language for component-based open programming

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INSTITUT NATIONAL DE RECHERCHE EN INFORMATIQUE ET EN AUTOMATIQUE

# ***OZ/K: A kernel language for component-based open programming***

Michael Lienhardt, Alan Schmitt, Jean-Bernard Stefani

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## OZ/K: A kernel language for component-based open programming

Michael Lienhardt, Alan Schmitt, Jean-Bernard Stefani

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**Abstract:** Programming in a distributed and open environment remains challenging because it requires combining modularity, security, concurrency, distribution, and dynamicity. This has led recently to interesting programming language developments such as Alice, Acute, OZ, JoCaml, ArchJava, etc, however the combination of all the above features with dynamicity, i.e. the ability to build and modify systems during execution, still remains an open question. In this paper, we propose an approach to open distributed programming that exploits the notion of locality, which has been studied intensively during the last decade, with the development of several process calculi with localities, including e.g. Mobile Ambients,  $D\pi$ , and Seal. We suggest to use the locality concept as a general form of component, that can be used, at the same time, as a unit of modularity, of isolation, and of mobility. Specifically, we introduce in this paper OZ/K, a kernel programming language, that adds to the OZ computation model a notion of locality borrowed from the Kell calculus. We present an operational semantics for the language, and several examples to illustrate how OZ/K supports open distributed programming.

**Key-words:** programming language, distributed programming, open programming, localities, component-based programming

## **OZ/K: Un langage noyau pour la programmation répartie à composants**

**Résumé :** Programmer dans un environnement ouvert reste difficile car cela impose de considérer à la fois des questions de modularité, de sécurité, de concurrence, et de répartition. Ces questions ont donné lieu récemment au développement de plusieurs langages tels qu’Alice, Acute, OZ, JoCaml, ou ArchJava, mais il semble que combiner tous ces aspects avec la dynamique, c’est-à-dire la possibilité de construire et de modifier des systèmes en cours d’exécution, reste une question ouverte. Dans ce rapport, nous proposons une approche de la programmation répartie ouverte qui exploite la notion de “localité” telle qu’elle a été étudiée dans plusieurs calculs de processus, comme, par exemple, le calcul des Ambients, le  $\pi$ -calculus réparti ( $D\pi$ ), et le Seal calculus. Nous proposons d’utiliser le concept de localité comme une forme générale de composant logiciel, qui peut être exploité, à la fois, comme unité de modularité, d’isolation, et de mobilité. Nous présentons à cette fin, dans ce rapport, le langage OZ/K, un langage de programmation noyau qui étend le modèle de programmation d’OZ avec une notion de localité tirée du Kell calcul. Nous définissons une sémantique opérationnelle formelle pour le langage, et nous illustrons, grâce à plusieurs exemples, l’intérêt de OZ/K pour une programmation répartie ouverte.

**Mots-clés :** langage de programmation, programmation répartie, programmation ouverte, localités, programmation à composants

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# 1 Introduction

Open environments involve distributed users that access and combine multiple services. These services interact, fail, and evolve constantly. Programming in such environments remains challenging because it requires, as pointed out in [91] by the designers of the Alice programming language, the combination of several features, notably: (i) *modularity*, i.e. the ability to build systems by combining and composing multiple elements; (ii) *security*, i.e. the ability to deal with unknown and untrusted system elements, and to enforce if necessary their isolation from the rest of the system; (iii) *distribution*, i.e. the ability to build systems out of multiple elements executing separately on multiple interconnected machines, which operate at different speed and under different capacity constraints, and which may fail independently; (iv) *concurrency*, i.e. the ability to deal with multiple concurrent events, and non-sequential tasks; and (v) *dynamicity*, i.e. the ability to introduce new systems, as well as to remove, update and modify existing ones, possibly during their execution.

Each of these features has been, and continues to be, the subject of active research on its own. Combining them into a coherent and practical programming language, however, is still an open question, despite interesting developments in the past two decades, including languages such as Acute [100], Alice [91], ArchJava [3], Classages [71], Erlang [10], E [80], Java [12], JoCaml [48], Kali Scheme [33], Klaim [18], Nomadic Pict [114], Oz [111], Scala [83]. For instance, the combination of strong objective mobility (i.e. the ability to move an executing component from one location to another) and dynamic linking with sandboxing (i.e. the ability to isolate an untrusted component from the rest of a computation, and to exercise discretionary control over its communication) is either not available in these different languages, or only through the use of relatively complex constructions and programming environment libraries with no formally defined semantics. Among these languages, Acute, Alice, and Oz (the latter with its MOZART environment [57, 93]) provide the most extensive support for open programming, but they still fall short, we argue below, of providing enough support for isolation and dynamic reconfiguration.

In this paper, we propose an approach to open programming that exploits the notion of *locality*. This notion has been studied in several families of process calculi such as Mobile Ambients [31],  $D\pi$  [58], Klaim [18], or the Seal calculus [32]. We suggest to use the locality concept as a primitive form of *component* that can be used simultaneously as a unit of *modularity*, of *isolation*, and of *passivation* (we call passivation the ability to freeze and marshall a component during its execution). Conflating these different kinds of units into a single notion provides a way to address the different concerns of open programming with few programming constructs.

Specifically, we introduce the OZ/K kernel programming language, that extends the OZ kernel language with a notion of locality, called *kell*<sup>1</sup>, borrowed from the Kell calculus [96], together with a passivation operation, borrowed from the M-calculus [95]. The layered design of the OZ kernel language, and the fact that it supports multiple programming paradigms make it a good substrate for our study, namely the use of locality as a basis for open programming. Because of the multi-paradigm character of the OZ language, extending OZ with localities can provide guidance for similar combinations using different language substrates (e.g. Ocaml [88] for functional and object-oriented programming, Haskell [87] for functional programming and lazy evaluation).

With respect to OZ and MOZART, OZ/K makes a number of contributions: (i) it generalizes the pickling operation in MOZART (i.e. the ability to make values in the language persistent – e.g. for storing them in a file or for sending them in a message) to cover not only stateless values but also complete execution structures; (ii) it allows to define different distributed programming abstractions without depending on a single, pre-defined distribution semantics for the different language entities as is currently the case in MOZART; (iii) it enhances security in OZ through first-class isolation units, and the ability to program sandboxes and security wrappers; (iv) it extends the classical exception handling mechanisms in OZ with failure handling facilities that operate at the component level; and (v) it provides basic support for strong mobility and dynamic reconfiguration through passivation.

Technically, the main contributions of this paper are: (i) the introduction of an extension of the *kell* concept from the Kell calculus [96] and the Kell calculus with sharing [61], with the ability to control communication channels of subordinate kells; (ii) the introduction of a passivation operation, called *packing*, which generalizes the passivation operator of the M-calculus [95] to an execution model with a shared store and logic variables; (iii) the

<sup>1</sup>Localities in the Kell calculus are called *kells*, in a loose analogy to biological *cells*.

introduction of operations on *packed values* (i.e. values resulting from the packing of kells) that provide support for dynamic linking and component replacement; (iv) the introduction of failure handling mechanisms that can deal with thread and component-level failures; (v) a formal operational semantics for the addition of the above constructs to the OZ kernel language.

The paper is organized as follows. Section 2 motivates and introduces our approach in the design of OZ/K. Section 3 defines the abstract syntax and provides an informal overview of the OZ/K kernel language. Section 4 presents several simple examples of open programming in OZ/K. Section 5 defines a formal operational semantics for OZ/K. Section 6 discusses various design decisions and issues. Section 7 discusses related work. Section 8 concludes the paper.

## 2 Extending OZ for open programming

### 2.1 OZ and MOZART limitations

The OZ language and its MOZART environment already provide several features for open programming. These include in particular: first class *modules* (records that group together related language entities such as procedures) and *functors* (functions that take modules and functors as arguments, and return modules); *module managers*, that allow access to modules referenced by URLs; *pickles*, that can be used to save complete values (i.e. values that do not contain unbound variables) to files; *tickets*, that constitute references to arbitrary language entities; *connections*, that support the establishment of communication links between remote sites using tickets for cross-site references; a distributed semantics (described in [57, 93]) that assigns sites to certain language entities such as variables, and cells, together with associated *communication protocols* tailored for achieving network transparency with the different kinds of language entities, namely stateless entities (e.g. base values, records, procedures, functors), and stateful entities (e.g. variables, cells). Despite these features, we can single out three main areas where OZ and MOZART fall short of supporting open programming: isolation, support for dynamic reconfiguration, and distribution semantics.

**Isolation.** Systems operating in an open environment should be ready to deal with unknown, potentially malicious components. A basic strategy to deal with untrusted components is to set up *sandboxes*, as formalized e.g. by the notion of *wrappers* in the Boxed- $\pi$  calculus [102]. A sandbox is an execution context that isolates encapsulated computations from the rest of their environment, and that prevents unwanted or suspicious communication attempts. More generally, isolating different parts of a running system from one another is required for performance isolation and for preventing denial of service (e.g. to prevent a component interfering with the execution of another one merely through inordinate resource consumption).

The current OZ language and its MOZART environment fail to support sandboxes formalized as Boxed- $\pi$  wrappers, which allow a strict control of communications between a module or component and its environment. For instance, while it is possible, through the subclassing of the base MOZART module manager, to forbid a downloaded module to access local resources on installation, it is not possible to control the communication of a module with its environment while it executes, and thus to prevent it from discovering – and accessing – forbidden resources in the process.

**Support for dynamic reconfiguration.** An open distributed environment is a highly dynamic one, where failures, updates, adaptations, and unplanned changes can occur all the time. A language for open distributed programming should provide the means to change a system’s structure and behavior on-the-fly, with no need to stop the whole system in order to perform modifications. Dynamic reconfiguration typically involves: the ability to circumscribe the part of a system which needs changing (the *target*); the ability to suspend the execution of the target in a well-defined state; the ability to replace the suspended target by a different subsystem.

The higher-order character of the OZ language allows to program systems as collections of components (e.g. in the form of *port objects* as described in [111]), and to program these components so that their behavior include

some operation to change their state (see for instance the upgradable compute server in Chapter 11 of [111]). However, it is not possible to suspend the execution of a component or to delete it (e.g. if some unwanted behavior like unwarranted resource consumption is detected), unless such behavior is already part of the component program. Thus replacing a faulty or malicious component that does not support the appropriate update behavior is not possible in OZ. In addition, it is not possible to capture as a value the state of an ongoing execution (e.g. to take a checkpoint or to reinstate a failed system from a saved checkpoint).

**Distribution semantics.** An open environment is essentially heterogeneous, with a wide variety of networks and protocols, supporting different communication semantics and providing different guarantees. Furthermore, depending on the application, different levels of distribution transparency and different views of a networked infrastructure may need to be provided. For instance, a deployment application will likely require an explicit view of the individual sites in the target network, so as to control the placement, installation, and configuration of different software components on different sites. This view may be quite detailed, depending on deployment requirements. For instance, one could consider separate spaces for different users, separate component containers for different applications, different tiers in site clusters, with different interconnection schemas, different sub-networks for fault-tolerance and enhanced performance, etc.

It is this very diversity that has lead the designers of the Acute language to abstain from incorporating in their language any specific means of remote interaction. In their words, “a general-purpose distributed programming language should not have a built-in commitment to any particular means of interaction” [101, 99]. The current MOZART environment relies on a predefined distribution semantics. We wish to avoid that dependency to keep in line with the above philosophy, and, in contrast to OZ and MOZART, to allow the definition of a distribution semantics and its supporting protocols within the language itself.

## 2.2 Our approach

To deal with the above issues, we extend the OZ kernel language with a locality construct. The aim is to provide a small and uniform formal basis for open programming capabilities that subsume those of the MOZART environment. As a consequence, open programming features in MOZART which are not expressible in the OZ kernel language (e.g. distribution protocols, or module placement), can now be defined in OZ/K. The OZ kernel language is built using a layered approach, with successive layers adding expressive power and capabilities. The first layer combines logic variables and higher-order procedures. The second layer adds explicit concurrency, in the form of threads. The third layer adds explicit state, in the form of updatable memory cells. The last layer adds lazy execution, in the form of by-need triggers. Our approach adds a new layer to the language, consisting of three main features: (i) a primitive form of component, which we call *kell*; (ii) a primitive operation for passivating kells, which we call *packing*; and (iii) a set of primitive operations for communication between kells, and for manipulating packed values.

A kell acts as a unit of modularity (kells encapsulate data and behavior behind well defined interfaces, called *gates*), a unit of isolation (a kell may fail independently of other kells, and a kell can act as a sandbox for its *subkells*, i.e. for kells that it contains), and a unit of reconfiguration (a kell can be passivated, independently from other kells, then moved, replaced, or deleted). The conflation of these different units in the single notion of kell is the key element of our approach. A kell encapsulates both *activity*, in the form of threads and other (sub) kells, and *state*, in the form of a private data store. Kells can thus be understood as hierarchically organized components, with the same granularity as port objects or active objects in OZ.

In order to achieve isolation, means of communication between kells are restricted to the emission and receipt of messages on *gates*, which are similar to channels in the (synchronous)  $\pi$ -calculus. As a consequence, logic variables, memory cells, and by-need triggers remain private to a kell and cannot be shared between different kells. This design choice is similar to the one made in the Erlang language, where processes, which are the unit of modularity and isolation, only communicate through mailboxes. It is also similar to the one made in the E language, where vats, which are units of concurrency and isolation, only communicate through asynchronous

message exchanges (with futures). There are several reasons for this choice, including those well-documented in disfavor of shared state concurrency (see e.g. [69], [9] for a discussion in the context of the Erlang language, and [80] for a discussion in the context of the E language). The overarching consideration in OZ/K is to avoid any form of shared state between kells to guarantee isolation.

OZ/K does not come equipped with a predefined distribution semantics. Instead, kells provide a basic notion of separation, from which different forms of remote interaction can be built, in line with the Acute philosophy discussed above. Communication on gates, which takes the form of atomic rendez-vous, should thus be seen as local communication. Remote interaction in OZ/K can be modeled by a program mediating communications between two or more peer kells (communication can take place via gates between a thread situated in a kell and a thread situated in the immediate parent kell). The net effect of our approach is to replace the network awareness principle that presided to the design of the OZ distribution semantics described in [57, 93, 92], which assigns localities (called *sites*) to language constructs, by a *network independence* principle that makes localities explicit, and does not define a fixed semantics for interaction over a network. A consequence of this design principle is that the distributed semantics developed for OZ is no longer primitive, but can be implemented in OZ/K as a set of abstractions for distributed programming. As a result, an OZ/K virtual machine<sup>2</sup> does not embed any assumption concerning supporting network services. Previous work on a Kell calculus abstract machine [21] has showed that this was an effective approach.

One may ask why we did not consider adding this last layer to OZ as a library instead of language extension. The reason can be given as a three-pronged argument: (i) we wish to have a simple formal semantics for our kernel language; (ii) we consider that a library ought to be programmable (even if not actually implemented) in terms of its host language, so as to avoid introducing constructs that are not definable in the host language semantics; (iii) the isolation achieved by kells, and the passivation operation cannot strictly be expressed in OZ. Consideration (ii) ensures that different forms of remote interaction can be defined and understood by OZ/K programmers as programs that relay information between peer kells.

### 3 Syntax and overview

The OZ/K kernel programming language retains the OZ general computation model at its core (with some amendments), and extends it with a notion of component directly inspired by the notion of kell in the Kell calculus. We provide below a brief overview of the main constructs in OZ/K. We leave aside in this overview constructs pertaining to lazy evaluation (by-need synchronization).

#### 3.1 OZ core

The basis for OZ/K is the OZ kernel language [111], featuring logical variables (single assignment variables), higher-order procedures, cells (which support multiple assignments), exception handling, concurrent threads, and by-need triggers. A tutorial on OZ is available online [56]. We just recall here the main constructs of the language. The OZ execution model consists of *dataflow threads* that operate on a *shared store*. Threads contain *statement* sequences and communicate through shared references in the store.

The syntax of the OZ kernel language constructs we use in this paper is given in Table 1, where  $s$  and its decorated variants denote statements;  $P, X, Y, C$ , and their decorated variants denote variable identifiers;  $v$  denotes base values (integers and literals – i.e. names or atoms); and  $\mathcal{J}$  denotes patterns. We assume that in any statement defining a lexical scope for a list of variable identifiers, the identifiers in the list are pairwise distinct. Specifically, in statements of the form:

```

local X1 ... Xn in S end
proc{X X1 ... Xn} S end
case X of V(V1:X1 ... Vn:Xn) then S1 else S2 end

```

<sup>2</sup>The operational semantics developed in this paper constitutes the specification of an OZ/K virtual machine, which should be implemented by considering all the different OZ/K primitives, including communication on gates, as actions local to a single site.

we must have  $x_i \neq x_j$ , for all  $i \neq j, i, j \in \{1, \dots, n\}$ .

We use the term *variable identifier* to refer to syntactical entities that denote variables. We use the term *variables* to refer to single-assignment variables, or logical variables (semantical entities).

<code>S ::= skip</code>	<i>empty statement</i>
<code>  S1 S2</code>	<i>sequential composition</i>
<code>  thread{X} S end</code>	<i>thread creation</i>
<code>  local X1 ... Xn in S end</code>	<i>variable introduction</i>
<code>  X = Y</code>	<i>imposing equality</i>
<code>  X = v</code>	<i>binding to base value</i>
<code>  X = l(f1:X1 ... fn:Xn)</code>	<i>binding to record</i>
<code>  {Unify X Y}</code>	<i>unification</i>
<code>  if X then S1 else S2 end</code>	<i>branch statement</i>
<code>  case X of J then S1 else S2 end</code>	<i>pattern matching</i>
<code>  {NewName X}</code>	<i>name creation</i>
<code>  proc{P X1 ... Xn} S end</code>	<i>procedure definition</i>
<code>  {P X1 ... Xn}</code>	<i>procedure call</i>
<code>  {IsDet X Y}</code>	<i>testing bound status</i>
<code>  {NewCell X C}</code>	<i>cell creation</i>
<code>  {Exchange C X Y}</code>	<i>cell read-and-update</i>
<code>  {WaitNeeded X}</code>	<i>by-need synchronization</i>
<code>  Y = !!X</code>	<i>read-only variable</i>
<code>  raise X end</code>	<i>exception handling</i>
<code>  try S1 catch X then S2 end</code>	
<code>  {FailedValue X Y}</code>	
<code>  ...</code>	

Table 1: Syntax: OZ core

The syntax for patterns is given in Table 2.

<code>J ::= V</code>	<i>base value pattern</i>
<code>  V(V1:X1 ... Vn:Xn)</code>	<i>record pattern</i>
<code>V ::= v</code>	<i>base value</i>
<code>  !X</code>	<i>base value of variable</i>

Table 2: Pattern syntax

**Variables and values.** References in the store are through *logic variables* (or *variables*, for brevity) that can be bound or unbound. An unbound variable does not yet refer to a value. A bound variable  $x$  refers to a definite value, which can be a base value (an integer, an atom or a name), or a record.

Atoms are values whose identity is determined by a sequence of printable characters. A record takes the form  $\text{lab}(f1:X1 \dots fn:Xn)$ , where  $\text{lab}$  is the *label* of the record,  $f1, \dots, fn$  are the *features* of the record, and variables  $x1, \dots, xn$  (which can be bound or unbound) are the *fields* of the record. Assume  $R$  is a record, with feature  $f$ . Record selection is written  $R.f$ , i.e. if  $R = \text{lab}(\dots f:X \dots)$ , then  $R.f$  evaluates to  $X$ . Records are

used to construct usual concrete types. Thus, tuples are records with consecutive integer features, starting with 1. A tuple  $\text{lab}(x_1 \dots x_n)$  corresponds precisely to the record  $\text{lab}(1:x_1 \dots n:x_n)$ . Lists are defined as tuples built from the atom  $\text{nil}$ , denoting the empty list, and the record label “[”, which can be used as an infix operator. Thus,  $H|T$  denotes a list whose first element is  $H$ , and whose tail is the list  $T$ . Lists can also be written in extension:  $[A_1 \dots A_n]$  stands for the list of  $n$  elements  $A_1, \dots, A_n$ . Pairs can be built as tuples with label #, using an infix notation. Thus  $X\#Y$  corresponds to the tuple ‘#’(X Y).

New logical variables are introduced with the statement:

```
local X1 ... Xn in S end
```

where  $S$  is an arbitrary statement, and  $x_1, \dots, x_n$  are the  $n$  new variables being introduced<sup>3</sup>. Variables just introduced are unbound. To bind a variable to a value, one can use equality statements of the forms (where  $v$  is an arbitrary base value, and  $x_1, \dots, x_q$  are variables):

```
X = v
X = l(f1:x1 ... fq:xq)
```

Note that in statement:  $X = l(f_1:x_1 \dots f_q:x_q)$  variables  $x_1, \dots, x_q$  may be unbound. This allows potentially infinite data structures to be represented in OZ and OZ/K. Two variables  $x$  and  $y$  can be constrained to be bound to the same value through the statement:

```
X = Y
```

Determining the bound or unbound status of a variable  $x$  is possible via the statement:

```
{IsDet X B}
```

which binds variable  $B$  to `true` or `false` if  $x$  is bound or not.

**Names.** Names are unforgeable constants, which are typically used to identify various execution entities, such as procedures and threads. There are three special names with reserved keywords: **unit**, **true**, **false**. Names **true** and **false** denote the boolean values true and false, respectively. The name **unit** is typically used as a synchronization token. Names can be created with the statement:

```
{NewName N}
```

which binds the variable  $N$  to a fresh name, guaranteed to be unique among OZ and OZ/K computations.

**Cells.** New assignable memory cells are created with the statement:

```
{NewCell X C}
```

which binds the variable  $C$  to a fresh cell, and stores variable  $x$  in the newly created cell. An atomic read-and-update of a cell is provided by the statement:

```
{Exchange C A N}
```

which atomically binds the content of cell  $C$  to variable  $A$ , and updates the content of cell  $C$  with variable  $N$ . Using `Exchange`, one can define an assignment operation to a cell  $C$ , noted  $C := X$ , which updates the content of the cell  $C$  to the variable  $X$ , and an operation to access the content of a cell  $C$ , noted  $X = @C$ , which binds the content of cell  $C$  to variable  $X$ .

<sup>3</sup>We keep in OZ/K the OZ syntactic constraint that variables start with an upper case letter. A lexical token which is not a keyword and starts with a lower case letter is deemed to be an atom. Thus `Var` is an identifier for a variable, whereas `var` denotes the atom ‘var’.

**Procedural abstraction.** A procedure definition takes the form:

```
proc{P X1 ... Xn} S end
```

where the variable  $P$  is bound to the name of the newly created procedure, the identifiers  $X_1 \dots X_n$  correspond to the formal parameters of the procedure, and  $S$  is a statement that constitutes the body of the newly created procedure. Identifiers  $X_1 \dots X_n$  are bound in the procedure definition, and their scope is the body  $S$  of the procedure. A procedure definition can also be written:

```
P = proc{$ X1 ... Xn} S end
```

where  $\$$  is an anonymous marker, to emphasize the fact that a procedure definition binds the name of the newly created procedure to variable  $P$ , and puts a procedure value (a closure) in the store. Note that formal parameters of a procedure can be input parameters (variables which are bound prior to the procedure execution) or output parameters (variables which are bound during the procedure execution). This means that a procedure may return any number of results, including none.

A call to the procedure named  $P$  takes the form:

```
{P A1 ... An}
```

where  $A_1 \dots A_n$  are variables corresponding to the actual parameters of the call. Since a variable can be bound to a procedure name, procedures in OZ and in OZ/K, are higher-order. For instance, the following program leads to an infinite execution:

```
local P in
  proc{P X} {X X} end
  {P P}
end
```

**Control flow and concurrency.** Statements can be composed sequentially. The statement:  $S_1 S_2$ , is the sequential composition of statement  $S_1$  with statement  $S_2$ . The empty statement is **skip**. The statement

```
thread{T} S end
```

creates a new thread that executes statement  $S$ , and binds its (freshly generated) name to variable  $T$ .

The statements above are non-blocking. The basic conditional statement in OZ and OZ/K:

```
if X then S1 else S2 end
```

blocks until variable  $X$  is bound to a boolean value **true** or **false** (and then executes statement  $S_1$  or statement  $S_2$ , respectively).

The pattern matching statement in OZ and OZ/K:

```
case X of J then S1 else S2 end
```

also blocks till variable  $X$  is bound to a value. The pattern  $J$  is then matched with this value. If the match is successful, i.e. if unification between the value and  $J$  succeeds, the pattern variables in  $J$  are bound and statement  $S_1$  is executed. The identifiers in  $J$  that correspond to pattern variables are bound in the **case** statement; their scope is the statement  $S_1$ . For instance, if  $X$  is bound to the record `rec(a:v1 b:v2)`, then the statement

```
case X of rec(a:X1 b:X2) then {P X1 X2} else skip end
```

evaluates to `{P v1 v2}` (pattern variables  $X_1$  and  $X_2$  are bound during pattern matching to  $v_1$  and  $v_2$ , respectively).

Exception handling in OZ is standard, and available through the statements

```
raise X end
try S1 catch X then S2 end
```

### 3.2 OZ/K constructs

To the OZ core, OZ/K adds three main elements: *kells*, *gates*, and *packing*. The syntax for the OZ/K-specific constructs is given in Table 3, where  $\kappa$ ,  $x$ ,  $Y$ ,  $Z$ ,  $G$  denote variable identifiers.

S ::= ...		
<b>kell</b> { $\kappa$ } S <b>end</b>		<i>kell creation</i>
{NewGate X}		<i>gate creation</i>
{Send G X}		<i>emitting message X on gate G</i>
{Receive G X}		<i>receiving message X on gate G</i>
{Open $\kappa$ G}		<i>grant kell <math>\kappa</math> access to gate G</i>
{Close $\kappa$ G}		<i>revoke access to gate G for kell <math>\kappa</math></i>
{Pack $\kappa$ X}		<i>packing kell <math>\kappa</math></i>
{Unpack X Y}		<i>unpacking packed value X</i>
{Mark X Y Z}		<i>marking X with names in Y</i>
{Status $\kappa$ X}		<i>get status of thread <math>\kappa</math></i>

Table 3: Syntax: OZ/K extensions

**Kells.** A *kell* is a computational location, i.e. a form of concurrent component, which associates a *named locality* to part of an OZ/K computation. Localities are organized in a tree where each node contains a (logically) private store and several running threads. Kells are created via statements of the form:

```
kell{ $\kappa$ } S end
```

where  $\kappa$  is bound to the (freshly generated) name of the newly created kell. Statement  $S$  corresponds to the body of the kell. Upon creation of kell  $\kappa$ , the execution of  $S$  starts in a new thread running within  $\kappa$ . In order to ensure isolation,  $S$  must contain only *strict* variables (except for  $\kappa$  which is bound during the creation of the kell). Strict variables are variables which are bound to strict values, i.e. values which, recursively, do not contain unbound variables. In effect, kells partition OZ/K computations into isolated subsets, organized in a tree, that can only communicate through *gates*.

As an example, consider the statement

```
kell{Server} {Serve In Out} end
```

This statement creates a new kell named `Server`. Once created, the kell starts executing its body (in this case, a call to the procedure `Serve`) in a new thread. Procedure `Serve` can be defined e.g. as follows:

```
proc{Serve In Out}
  Message Response Handle in
    proc{Handle M R} ... end
    {Receive In Message}
    {Handle Message Response}
    {Send Out Response}
    {Serve In Out}
end
```

`Serve` first receives a message `Message` on gate `In`. The message is then handled by procedure `Handle`, which returns a result `Response`. The result is then sent on gate `Out`, and procedure `Serve` calls itself recursively, which will trigger the handling of the following input message on gate `In`. This example illustrates that kells can typically be programmed in much the same way as active objects or port objects in OZ, or as processes in Erlang.

**Gates and communication.** A *gate* in OZ/K denotes an interaction point for a kell. It is similar to a  $\pi$ -calculus channel: communication is pairwise and bidirectional, and gate names can be sent across gates. A new gate  $G$  can be created via a call of the form:

```
{NewGate G}
```

Once the gate  $G$  has been created, it can be used to send values via the statement

```
{Send G X}
```

or to receive them, via the statement

```
{Receive G X}
```

Communication through gates is by atomic rendez-vous: a `Receive` statement is successful only if there is a matching `Send` statement available in a different thread. This mode of communication on gates, together with the isolation property, allows locality passivation (packing) to take place at any point in time during an execution. Having an atomic rendez-vous as a primitive form of communication allows to derive other forms of interaction, including ones that implement flow control between emitters and receivers. In particular, component connectors can be realized as kells that mediate communication between two or more peer kells. Only *strict* values can be sent through a gate. This restriction ensures that kells remain isolated during execution, and that gates form the only means of communication between kells. Communication on gates should be understood as local, i.e. as taking place on a single machine. Remote communication in OZ/K can be *modeled*, as illustrated in the next section, by programs that relay information between two or more peer kells, using two or more gates.

**Controlling communication.** In order to support sandboxing, kell boundaries can impose restrictions on communications. By default, communication may cross at most one kell boundary: it is allowed within a kell, and between a kell and its parent-kell. Direct communication on some gate  $G$  between two threads separated by more than one kell boundary is only allowed if every kell boundary crossed by the communication has this gate *opened*<sup>4</sup>. A gate  $G$  can be opened in the boundary of a kell  $\kappa$  by its parent-kell using the procedure call

```
{Open K G}
```

To allow two sibling kells  $\kappa_1$  and  $\kappa_2$ , children of kell  $\kappa$ , to communicate directly on a gate  $G$ , one has to open  $G$  for either  $\kappa_1$  or  $\kappa_2$ , for instance via this statement in kell  $\kappa$ :

```
{Open K1 G}
```

To make a parent kell *transparent* for some or all of its child-kells, i.e. to allow all child-kells to communicate directly between them, or with the parent kell environment, one can use the key-word `all` to reference all the child-kells, and all the gates in a kell. Thus, the statement

```
{Open all all}
```

opens all the gates for all the children-kells of the current kell, whereas the statement

```
{Open K all}
```

opens all the gates for child-kell  $\kappa$  of the current kell.

<sup>4</sup>The exact condition, defined formally in Appendix A, is a bit more complex than that, because it takes into account the base case where all gates are opened for communication between a thread in a child kell and a thread in a parent kell. Hence, when a gate  $G$  is opened for a child kell, all the threads in the child kell have the same communication possibilities, on gate  $G$ , than thread in the parent kell.

**Packing and unpacking.** `{Pack  $\kappa$   $v$ }` is the statement implementing passivation. It suspends the execution of the child  $\kappa$  of the current kell and marshalls it, together with the relevant portion of the store, in a *packed value* bound to the variable  $v$ . Packed values can be modified using the `Mark` operation. Specifically, `{Mark  $v_1$   $R$   $v_2$ }` returns in  $v_2$  the packed value  $v_1$  modified according to the instruction given by tuple  $R$ . If  $R = \text{gate}(G_1 G_2)$ , the gate  $G_1$  is replaced in  $v_2$  by  $G_2$ . If  $R = \text{proc}(P Q)$ , the procedure  $P$  is replaced by  $Q$ . A side-effect of `Mark` is that it prevents *marked* names to be changed during unpacking of the packed value, as described below. Thus, the statement `{Mark  $v_1$   $\text{gate}(G G)$   $v_2$ }` only marks gate  $G$  to prevent it being renamed when unpacking  $v_2$ .

The statement `{Unpack  $v$   $R$ }` can be used to unpack a packed value  $v$ . Unpacking creates an execution structure similar to the one which has been packed, with new names for its gates, kells, and procedures, with the exception of the ones which have been marked. The new gate names are returned in the list of pairs  $R$ . The first element of a pair in  $R$  is the name of a gate in the packed value. The second element of a pair in  $R$  is the corresponding new name for the gate. A `Mark` operation on a packed value can be understood as a dynamic linking operation that connects a kell about to be unpacked to its new environment.

**Failure handling.** OZ has only classical exception handling. In the context of OZ/K, we need to deal with thread-level and kell-level failures. This is made possible by the detection of thread failures, via the `Status` statement. Briefly, the statement

```
{Status  $T$   $X$ }
```

returns in  $X$  the termination status of thread  $T$ . More precisely,  $X$  gets bound to either `terminated`, if thread  $T$  has terminated successfully, or a failed value reflecting the unsuccessful termination of a thread. See Section 5 for details.

### 3.3 OZ values and syntactic conveniences

**OZ values.** We occasionally employ in our examples procedures that do not belong to the OZ/K kernel language, but that can be defined in terms of the kernel language, and which belong to the OZ base environment. We refer the reader to [56, 40] for more details on the OZ base environment. In particular, we use the notion of *chunk*, which is a basic data type provided in OZ. A chunk behaves much like a record, except that its label is always a name (and not an atom), and it is not possible to obtain its list of features through the `Arity` operation.

**Syntactic conveniences.** We use syntactic conveniences to abbreviate OZ/K programs. Thus, variable introduction

```
local  $X_1$  ...  $X_n$  in  $S$  end
```

can be abbreviated as

```
 $X_1$  ...  $X_n$  in  $S$ 
```

Also, variables can be both declared and initialized at variable introduction. Thus,

```
local  $X$  in
   $X = 10$  { $P$   $X$ }
end
```

can be abbreviated as

```
 $X = 10$  in { $P$   $X$ }
```

Likewise, a procedure declaration of the following form, where  $S$  is some statement,

```
Pr in
  proc {Pr ...} ... end
  S
```

can be abbreviated as

```
proc {Pr ...} ... end in S
```

or

```
Pr = proc {$ ...} ... end in S
```

Nested case statements can be abbreviated using `[]` to discriminate between different cases in a case statement: for instance,

```
case X of
  a(X1 X2) then {P1 X1 X2}
[] b(Y1 Y2) then {P2 Y1 Y2}
else skip
end
```

abbreviates

```
case X of a(X1 X2) then {P1 X1 X2}
else
  case X of b(Y1 Y2) then {P2 Y1 Y2}
  else skip
  end
end
```

Tuples are record with integer features. They can be written with their features left implicit. Thus `X = r(X1 X2)` is the same as `X = r(1:X1 2:X2)`. Nested values can be written directly, without introducing variables to hold intermediate values. Thus, `X = rec(a:11 b:r(1 2))` abbreviates:

```
local X1 X2 X3 X4 in
  X1 = 11
  X2 = 1
  X3 = 2
  X4 = r(X1 X2)
  X = rec(aX1 b:X4)
end
```

We can use the wildcard “`_`” in places where a variable is needed but not subsequently used. Thus `thread{ _ } S end` abbreviates:

```
local X in thread{X} S end
```

We also abbreviate: `thread{ _ } S end` to the simpler: `thread S end`. Likewise, we abbreviate: `kill{ _ } S end` to: `kill S end`.

We often make use of a list version of the `Open` and `Close` primitives, writing for instance:

```
{Open K1 [G1 G2 G3]}
```

for the statements:

```
{Open K1 G1} {Open K1 G2} {Open K1 G3}
```

Finally we make use of the nested marker `$` to simplify the writing of expressions, i.e. statements that return a value. For instance, if `P` is a procedure of  $n + 1$  arguments, that returns its result on the last argument, we can write: `X = {P A1 ... An $}` for: `{P A1 ... An X}`, and:

```
{P {P A1 ... An $} B2 ... Bn X}
```

for:

```
Y in {P A1 ... An Y} {P Y B2 ... Bn X}
```

## 4 Open programming in OZ/K

In this section, we present several simple examples that illustrate how OZ/K supports various open programming features. In the process, we discuss the motivation for the features presented.

### 4.1 Components

The `kell` construct provides a form of component that is close to the software architecture [50, 103] notion: encapsulation behind well defined interfaces (gates), separation between interface and implementation, first-class notion of *connector* for supporting interaction between components. Apart from usual software engineering considerations (e.g. software quality, ease of maintenance and evolution), an explicit software architecture is interesting to combat architecture erosion, to facilitate system configuration and assembly, and to automate system management functions, as demonstrated e.g. in architecture-based management approaches [24, 35].

To illustrate support for component-based concepts, we present below three examples: the first one illustrates changes in component implementation exploiting only standard OZ constructs; the second one shows how one can recover the notion of owned component interface using gates; the third one illustrates the use of kells as both components and connectors, and `kell`-based dynamic reconfiguration.

As a first example, consider the `kell` `Server`, created by the `kell` statement below.

```
kell{Server}
  ServerState = {NewCell unit $}
  Serve = proc{$ InGate OutGate Handler State}
    Message in
      {Receive Ingate Message}
      case Message of
        replace(NewServe) then {NewServe InGate OutGate ServerState}
        [] update(NewHandler) then {Serve InGate OutGate NewHandler ServerState}
        [] msg(Op Args Continuation) then
          Response in
            {Handler Op Args Response}
            {Send OutGate resp(Continuation Response)}
            {Serve InGate Outgate Handler ServerState}
          else skip end
        end
      Handle = proc{$ X Y Z} ... end
    in
      {Serve In Out Handle ServerState}
  end
```

This `kell` corresponds to a component with two interfaces, gates `In` and `Out`, and an initial implementation given by the statement `{Serve In Out Handle ServerState}`. The cell `ServerState` holds the internal state of the `Server` component. The implementation of `kell Server` does a simple job: upon receipt on gate `In` of a message of the form `msg(Op As C)` (where `Op` denotes the name of the operation to perform, `As` is a list of arguments for the operation, and `C` is a continuation), the operation name and the arguments are passed to an internal procedure (initially, `Handle`) for evaluation; when the call to the procedure returns, the result `R` is send together with the continuation `C` as a response message on gate `Out`. In addition, the implementation (given by procedure `Serve`) can be changed partially, upon receipt of an `update` message, which changes only the internal `Handle` procedure, or completely, upon receipt of the `replace` message. The latter illustrates the separation that is achieved between interfaces (gates) and implementation (a call to a procedure taking gates as arguments).

Apart from the `kell` construct, the above example uses only standard OZ constructs. It illustrates what one may call *planned reconfiguration*. Reconfiguration in `Server` can take place, as a consequence of the receipt of `update` or `replace` messages, in between the handling of request messages of the form `msg(Op As C)`. The

reconfiguration triggered by the `update` and `replace` messages is *planned* (or *subjective*) in the sense that the code of the `Server` component itself contains instructions for changing its own internal configuration. In OZ/K, the `kell` construct also supports *unplanned* (or *objective*) reconfigurations, by means of passivation. We illustrate this in the second component example below.

Before we come to this example, let us remark that the analogy with the usual notion of component is not perfect, for a component typically *owns* its ports or interfaces, which cannot be shared with other components. This is not the case with kells and gates, however it is possible to enforce a form of “gate ownership”, by turning a gate into a unidirectional communication port and exporting only to the environment of a kell one “side” of a gate. We can do this using chunks as capabilities, as shown below.

```

proc{NewHalfGate Dir GI GO}
  G Anchor in
    {NewGate G} {NewName Anchor}
    proc{NSend X} {Send G X} end
    proc{NReceive X} {Receive G X} end
    proc{NOpen K} {Open K G} end
    case Dir of
      send then Z in {NewChunk r(send:NSend open:NOpen) GO}
                       {NewChunk r(receive:NReceive Anchor:Z) GI}
      [] recv then Z in {NewChunk r(receive:NReceive open:NOpen) GI}
                       {NewChunk r(send:NSend Anchor:Z) GO}
    else skip end
end

```

In the above code snippet, we define a new procedure `NewHalfGate` which creates two “half gates”, one for receiving and one for emitting. The additional argument `Dir` to `NewHalfGate` specifies which capability is to be exported: if it is `send`, then the `send` capability is exported (notice how the ability to open the gate is attached to the `send` capability in this case, meaning that communication across kell boundaries can only be done by passing *this* capability). Note the extra feature `Anchor` that is added to the non-exportable half gate. The field associated with `Anchor` remains unbound, which prevents the corresponding chunk from being communicated outside the current kell, because of the strictness requirement on gate communication. This suffices to ensure that only the proper half gate can be known outside of the current kell, and that the other half remains only known inside the current kell. This ensures a form of ownership of the gate since only the originating kell can either send or receive on the private half gate.

Our second component example illustrates that component configurations can be organized hierarchically, and can be changed in an *objective* fashion by packing kells. Consider the following code:

```

proc{Link I O} M in {Receive I M}{Send O M}{Link I O} end

kell{Comp}
  I1 O1 I2 O2 I3 O3 Comp1 Comp2 Comp3 Con1 Con2 Con3 in
    {NewGate [I1 O1 I2 O2 I3 O3]}

    kell{Comp1} {Beh1 I1 O1} end
    kell{Comp2} {Beh2 I2 O2} end
    kell{Comp3} {Beh3 I3 O3} end
    kell{Con1} thread {Link In I1} end {Link O1 I3} end
    kell{Con2} {Link O3 Out} end
    {Open Con1 [In I1 O1 I3]} {Open Con2 [O3 Out]}

    {Receive G M}
    case M of switch
    then {Pack Con1 _}
         kell{Con3}

```

```

        thread {Link In I2} end
        {Link O2 I3}
    end
    open Con3 [In I2 O2 I3]
else skip
end
end
end

```

In the above example, `kell Comp` has three subcomponents `Comp1`, `Comp2` and `Comp3`, whose behavior is defined by three procedures, `Beh1`, `Beh2`, and `Beh3`, defined elsewhere. Initially, `Comp` is configured as a pipeline of two subcomponents, `Comp1` and `Comp3`, with the gate `In` linked to gate `I1`, gate `O1` linked to gate `I3`, and gate `O3` linked to gate `Out`. In this initial configuration, `Comp2` is not in use since its gates are not linked. Note that procedure `Link` acts as a channel between an input gate and an output gate, and that kells `Con1`, `Con2`, and `Con3` are used as *connectors*, that bind several gates at once (for instance, gates `In` and `I1`, as well as `O1` and `I3`, in the case of `Con1`). Upon receipt of the `switch` event on the `G` gate, defined elsewhere, the initial configuration is changed to a pipeline of `Comp2` and `Comp3`. The reconfiguration is effected by removing the connector `Cn1`, via packing, and by replacing it with a new connector `kell`, `Cn3`. Notice that the reconfiguration code above does not pay any consideration to the exact state of the components: in particular, the switch may lose messages being processed by `Comp1` and by `Con1` at the moment of the switch. Note also that, in this instance, one could have programmed the removal of `Con1` directly in `OZ`, e.g. by having its behavior dependent on checking whether a given variable, indicating termination, is bound or not. However, the use of packing here provides an example of unplanned reconfiguration, where the code of connector components is independent of external reconfiguration actions.

## 4.2 Distribution

As explained in Section 2.2, `OZ/K` has no built-in support for remote communications. However, because of their inherent separation, kells in can be used to *model* different sites, communicating using different communication semantics. For instance, here is a simple configuration, with two sites `Site1` and `Site2`, running programs `P1` and `P2` respectively. The `kell Net` acts like an interconnecting asynchronous network, relaying messages from one site to another: we suppose that `S1` (resp. `S2`) listen on the gate `In1` (resp. `In2`) and emit on `Out1` (resp. `Out2`).

```

kell{Site1} {P1} end
kell{Site2} {P2} end
kell{Net}
  Relay in
    proc{Relay G1 G2}
      M in
        {Receive G1 M} thread {Send G2 M} end {Relay G1 G2}
    end
    thread {Relay Out1 In2} end
    thread {Relay Out2 In1} end
  end
{Open Net all}

```

The `Net` component simply relays messages that are sent on output gates `Out1` and `Out2` to their destination sites, designated by their addresses, i.e. input gates `In1` and `In2`. The `Relay` procedure simply forwards asynchronously (due to the triggering of a separate thread for forwarding messages to their destination) messages between two gates. The `Net` component is allowed to communicate on all gates with its siblings, namely sites `Site1` and `Site2`. Note also that `Site1` and `Site2` are not allowed to communicate directly with each other since there are no gates explicitly opened for them (as a result, threads in `Site1` and `Site2` can only communicate with threads in `Net`).

This (evidently simplistic) example illustrates how the separation between different loci of computation can be used to model a networked environment. Note that we encapsulated the network in a separate `kell`: this would allow us, for instance, to model failures of the `Net` component independent from failures of sites. A programmer can thus be provided with a semantics for distributed computation in terms of the `OZ/K` computation model. Importantly, this semantics can be adapted to different network environments, and arbitrary details of the supporting infrastructure revealed to the programmer, without having to change the language semantics. One can thus provide different abstractions to distributed programmers, depending on their needs, the network environment considered, and the level of distribution transparency required, as in [21].

### 4.3 Modules and dynamic linking

**Modules.** The notion of `kell` unifies notions of software modules and components, and packing provides both a generalization and a formal interpretation of the pickling construct provided by the `MOZART` environment for the `OZ` language. Consider for instance the following code, where `G` is a gate:

```
kell{Mod}
  P1 P2 M T in
    proc{P1 A} ... end
    proc{P2 A} ... end
    proc{T X} {Send G X}{T X} end
  M = module(op1:P1 op2:P2)
    {T M}
end
```

This code fragment creates a new `kell Mod` which simply defines two unary procedures, `P1` and `P2`, and gathers them in a record `M`, which is repeatedly sent on gate `G`. In effect, `M` corresponds to a software module that consists of just two procedures, accessed through the names `op1` and `op2`. To use the module is simple; just retrieve the module proper on gate `G`, and use the module's procedures through their advertised names, `op1` and `op2`:

```
{Receive G Y}
{M.op1 X}
```

Importantly, `kell Mod` can be packed and sent to a different site (another `kell`), so that the module can be made available there. For instance, assuming `Out` is a gate on a channel to a different site (as illustrated in Section 4.2), then:

```
{Pack Mod Z}
{Send Out Z}
```

illustrates how to marshall the `kell Mod` using the packing operation, and how to send the resulting packed value for use of the module at a different site.

**Strong mobility and dynamic linking.** Assume a distributed environment similar to the one in Section 4.2. Assume further that each site upholds the convention that the atom `service` denotes a local module, consisting of two operations `op1` and `op2` (like the module in the previous example), which have different implementations at each site. How can we ensure that code programmed to use the `service` module at one site can be moved safely to a different site and use the local `service` module implementation? We cannot simply use the previous `Mod` construction: the variable `M` references the module available at gate `G`, which does not refer to the correct implementation at other sites. One solution is to ensure each copy of the module dynamically retrieves the local implementation upon each call, so as to take into account possible moves from clients of the module. The following code implements this:

```
kell{DynMod}
  T P1 P2 DynM Ploc1 Ploc2 Loc1 Loc2 in
    proc{P1 A} ... end
```

```

    proc{P2 A} ... end
    {NewName Loc1}{NewName Loc2}
    proc{T X} {Send G X}{T X} end
    proc{Ploc1 M} Z in {Receive G Z}{Z.Loc1 M} end
    proc{Ploc2 M} Z in {Receive G Z}{Z.Loc2 M} end
    DynM = {NewChunk m(op1:Ploc1 Loc1:P1 op2:Qloc2 Loc2:P2) $}
    {T DynM}
end

```

As before, the procedures  $P1$  and  $P2$  constitute the local implementation of the module's functionality, and the procedure  $T$  is available to the module  $M$  on gate  $G$ . Using the module  $DynMod$  remains similar to the previous example: just access the procedure through the module's features  $op1$  and  $op2$ . The features  $Loc1$  and  $Loc2$ , that store the local implementation of the procedures  $P1$  and  $P2$ , are not directly used by the client of the module. Access to the private features  $Loc1$  and  $Loc2$  is protected by gathering all the features of module  $DynM$  in a chunk.

Assume an agent, modelled as a kell, that moves from site to site, through a series of packing/sending/receiving/unpacking moves, and that requires access at each site to the `service` module. Now, as the agent moves from the site  $S1$  to the site  $S2$ , the gate where the module is available changes, raising the necessity to modify the reference of the gate in the agent's code. This modification of the agent's code is done using the procedure `Mark`, as presented in the next example (we suppose that the module is available at  $G1$  – resp.  $G2$  – at site  $S1$  – resp.  $S2$ ):

```

%% Site 1
{Pack Agent Z}
{Send Out1 msg(service:G1 pack:Z)}

%% Site 2
Message K Agent in
{Receive In2 Message}
case Message of msg(service:OldGate pack:PackedAgent) then
    kell{Agent}
    {Mark PackedAgent gate(OldGate G2) K}
    {Unpack K _}
end
else skip end

```

The dynamic linking technique introduced above incurs an overhead at each call, since it requires retrieving the local copy of the module before the actual call. We can provide an optimized version of dynamic linking by changing directly procedures in packed values. Assume a module defined like `Mod` above, with local copies of the same form at different sites. We can optimize the transfer of a mobile agent and the execution of dynamically linked procedures, by proceeding as follows: before sending the agent (in packed form), replace the procedures from module `Mod` by place-holder ones; upon receiving the agent in packed form, replace the place-holder procedures by those of the local copy of `Mod`. Sending of the mobile agent `Agent` would look like this, assuming `Agent` designates a kell,  $G$  denotes the gate at which the module `Mod` is available, `service` is the well-known name under which `Mod` is known at the different sites, and `Out` denotes a gate for sending to the chosen remote site:

```

Z1 Z2 M PackedAgent P1 P2 PH1 PH2 in
    proc{PH1 A} skip end
    proc{PH2 A} skip end
    {Pack Agent Z1}
    {Receive G M}
    P1 = M.op1 P2 = M.op2
    {Mark Z1 prc(P1 PH1) Z2}{Mark Z2 prc(P2 PH2) PackedAgent}
    {Send Out msg(s:service prc:[PH1 PH2] agent:PackedAgent)}

```

Receiving and linking the mobile agent at the remote site, would look like this, assuming that `In` denotes a gate for receiving from the original site, and that  $G$  denotes the gate at which `Mod` is known at the receiving site:

```

Message M Z1 Z2 Agent P1 P2 K in
  {Receive In Message}
  case Message of msg(s:service prc:[PH1 PH2] agent:PackedAgent)
  then {Receive G M}
    P1 = M.op1 P2 = M.op2
    kell{Agent}
      {Mark PackedAgent prc(PH1 P1) Z1}{Mark Z1 prc(PH2 P2) K}
      {Unpack K _}
    end
  else skip end

```

In effect, we directly replace in the agent code the place-holder procedures `PH1` and `PH2` by the local procedures `P1` and `P2`. This solution for dynamically linking a mobile agent to local copies of a well-known module, has less overhead than the previous one, since there is no need to first retrieve the local module copy prior to invoking an operation of the module. Note that the above techniques for dynamic linking can be used in conjunction with a name server at each site. Provided that all sites agree of a single atom such as `service` to refer to this name server, then this is enough to bootstrap dynamic linking (and dynamic binding) of services referenced across sites using well-known names (e.g. atoms or strings).

#### 4.4 Isolation

The `kell` construct provides the ability to build very configurable sandboxes. Consider the case of a plug-in of dubious origin. It is possible to isolate it in different ways. A first example is provided by the following code, which is a straightforward application of dynamic linking. In this case, the sandbox `Sandbox` allows communication of the plug-in only on the gate `G`, that correspond to the communications advertised as required by the plug-in, under the well-known name `service`. Once received, the plug-in is placed inside the new `kell` `K`, inside the sandbox. It is then marked with the local gate `G`, and unpacked. The double inclusion is necessary to avoid any communication of the plug-in with the environment of the sandbox, apart from communications on gate `G`.

```

Sandbox in
  {Receive In Message}
  case Message of msg(service:OldGate plugin:PlugIn) then
    kell{Sandbox}
      K P in
        kell{K} {Mark PlugIn gate(OldGate G) P}{Unpack P _} end
      {Open K G}
    end
  else skip end

```

The behavior of a sandbox can be more complex. For instance, we may allow the plug-in to request the opening of some gate for communication. The sandbox can then check the security of such an opening, using the procedure `Check`, and allow it or not. The control policy module can take the form of a procedure `Control` listening on a given gate identified by a well-known name such as `control`. The resulting sandbox can take the following form:

```

Check = proc{$ K X Y B} ... end
Control = proc{$ SandBoxedKell CtrlGate}
  {Receive CtrlGate Message}
  case Message of r(service:S gate:G returnGate:R)
  then B in
    {Check SandBoxedKell S G B}
    if B then {Open SandBoxedKell G} {Send R ok} else {Send R nok} end
  else skip end
  {Control SandBoxedKell CtrlGate}
end

```

```

in
  {Receive In Msg}
  case Msg of msg(control:G plugin:P) then
    kell{Sandbox}
      Ctrl SndBoxK P1 in
        {NewGate Ctrl}
        kell{SndBoxK} {Mark P gate(G Ctrl) P1}{Unpack P1 _} end
        {Control SndBoxK Ctrl}
      end
    else skip end

```

The encapsulation realized by the `kell` construct allows in particular to build wrappers as in the Boxed- $\pi$  calculus [102]. For instance, we can build a simple *filtering wrapper* for some untrusted plugin, which requires the use of a service, where the required service is made available locally (after filtering) on gate `SV`.

```

Filter Msg Sandbox in
  proc{Filter G1 G2} ... end
  {Receive In Msg}
  case Msg of msg(service:PG plugin:P) then
    kell{Sandbox}
      K P1 G in
        {NewGate G}
        kell{K} {Mark P gate(PG G) P1} {Unpack P1 _} end
        thread {Filter G SV} end
      end
    else skip end

```

In this example, the procedure `Filter` acts as a partial relay between the gates `G` and `SV`, transmitting only *valid* messages and erasing the others.

## 4.5 Handling failures

Failure handling in OZ/K bears a strong similarity with failure handling in Erlang [9], and with a recent proposal for enhanced failure handling in OZ [39]. Units of failure in OZ/K are threads and kells. Handling a failure in a thread or a kell requires setting up an independent thread that can monitor state changes in the supervised thread or kell. Setting up a monitoring thread can be done as in the following program:

```

proc {NewMonThread Body Gate}
  M in
    thread{Th} {Body} end
    thread{M}
      S = {Status Th $} in
        case S of failed(Z) then {Send Gate thFail(Th Z)} else skip end
      end
    end

```

The above program creates two threads, the monitoring thread `M`, and the monitored thread `Th`. The behavior of `M` is simple: it waits for `Th` to fail, and then notifies this failure on gate `G`. The program makes use of the operation `Status`, that returns the *execution status* of a thread. A thread execution status can essentially be in two states: active or failed. It is manifested by a 'read-only) variable that is either unbound, signifying that the thread is active, or bound to a failed value of the form `failed(x)`, signifying that the thread has failed with failure cause `x`.

It is also possible to force a kell to abort upon the occurrence of some failure in one of its threads, thereby obtaining a similar effect to process linking in Erlang, which causes a group of Erlang processes to fail together if one of the processes in the group fails. In our case, we can *link* threads by placing them in a kell and setting up an appropriate monitoring structure. This is illustrated in the following program, where two threads are linked in a kell, which is aborted as soon as one the two threads fails. The code snippet below also illustrates how kells themselves can be monitored for failure (in this case, a failure message is sent on the monitoring gate `MG`).

```

G K in
  {NewGate G}
  kell{K} {NewMonThread Beh1 G} {NewMonThread Beh2 G} end
  {Receive G M}
  case M of thFail(T Z) then {Pack K _} {Send MG kFail(K Z)}
  else skip end

```

## 5 OZ/K operational semantics

The operational semantics of the OZ/K kernel language is given in terms of a reduction relation  $\rightarrow \subseteq (\text{Store} \times \text{Task})^2$ , which is a binary relation on execution structures. We call *execution structure* an element of  $\text{Store} \times \text{Task}$ , i.e. a pair consisting of a store and a task. We assume given the following infinite countable and mutually disjoint sets:  $\text{Ident}$ , the set of variable identifiers,  $\text{Var}$  the set of logical variables,  $\text{Name}$  the set of names,  $\text{Atom}$  the set of atoms. We denote  $\text{Int}$  the set of integers, which we assume also disjoint from  $\text{Ident} \cup \text{Var} \cup \text{Name} \cup \text{Atom}$ .  $\text{Name}$  contains the following distinguished elements: **true**, **false**, **unit**,  $\top$ . The latter name denotes the name of the root of the kell tree (or *top-level kell*).

**Statements.** The set of statements,  $\text{Statement}$ , corresponds to the set of terms  $S$  given by the grammar productions in Table 1 and Table 3. The set of *extended statements*,  $\text{Statement}^\dagger$ , consists in the set of statements augmented with the set of terms  $S$  where logical variables are substituted to some or all variable identifiers in the term  $S$ , i.e.

$$\text{Statement}^\dagger = \text{Statement} \cup \{S\theta \mid S \in \text{Statement}, \theta : \text{Ident} \rightarrow \text{Var}\}$$

The effect of a substitution  $\theta$  on a statement  $S$  is defined in Section A.

**Tasks.** The set of tasks,  $\text{Task}$ , consists of elements  $\mathcal{T}$  given by the following grammar (where  $\eta$  denotes a name, and  $S$  denotes an extended statement):

$$\begin{array}{ll} \mathcal{T} ::= \eta : T \mid \mathcal{T} \mathcal{T} & \text{tasks} \\ T ::= \langle \rangle \mid \langle S \mathcal{T} \rangle & \text{thread stacks} \end{array}$$

Intuitively, a task  $\mathcal{T}$  is a multiset (parallel composition) of named threads  $\eta : T$ . As a notational convenience, when making explicit the structure of a named thread, we often elide the ‘:’, thus “ $\eta : \langle S \mathcal{T} \rangle$ ” is often noted “ $\eta \langle S \mathcal{T} \rangle$ ”.

**Stores.** The set of stores,  $\text{Store}$ , consists of elements  $\sigma$  given by the grammar in Figure 1, where  $x, y$  and their decorated variants range over variables;  $l$ , and its decorated variants range over literals (atoms and names);  $f$  and its decorated variants range over integers and literals;  $\xi, \eta, \zeta$  and their decorated variants range over names.

A store consists in a conjunction (noted  $\wedge$ ) of primitive assertions. Primitive assertions comprise:

- Variable in store assertions of the form  $x$ , which indicates that variable  $x$  is in the store domain, which we denote by:  $x \in \text{dom}(\sigma)$ .
- Variable bindings, of the form  $x = V$ , where  $x$  is a variable and  $V$  is some value (integer, atom, name, record, failed value, or packed value), or  $x = y$ , where  $x$  and  $y$  are both variables. Notice that the assertion  $x = \text{pack}(\zeta, \mathcal{T}, \sigma, \mu)$  corresponds to a binding of a variable to a packed value, which happens only as a side-effect of passivation.
- Name bindings, of the form  $\xi : T$ , where  $T$  is some semantical value such as a procedure or a gate. Notice that a semantical value  $T$  embeds explicit type information about the nature of elements which are referred to by a name.

$\sigma ::= x$	<i>variable in store</i>
$x = u$	<i>binding to base value</i>
$x = l(f_1 : x_1, \dots, f_n : x_n)$	<i>binding to record</i>
$x = y$	<i>equality between variables</i>
$x = \text{failed}(y)$	<i>failed value</i>
$x = \text{pack}(\zeta, \mathcal{T}, \sigma, \mu)$	<i>variable bound to packed value</i>
$\xi : \text{proc}\{\$ X_1 \dots X_n\} S \text{ end}$	<i>procedure value</i>
$\xi : \text{thread}(x)$	<i>thread pointer</i>
$\xi : \text{cell}(x)$	<i>cell value</i>
$\xi : \text{kell}(\pi, x)$	<i>kell pointer</i>
$\xi : \text{gate}$	<i>gate</i>
$\text{need}(x)$	<i>needed variable</i>
$\text{read}(x, y)$	<i>read-only variable</i>
$\text{read}(x)$	
$\text{in}(\xi, \zeta)$	<i>kell in kell</i>
$\text{inth}(\xi, \zeta)$	<i>thread in kell</i>
$\text{subg}(\xi, \zeta)$	<i>sub gate</i>
$\sigma \wedge \sigma$	<i>store conjunction</i>
$\mu ::= \emptyset$   $\{\xi_1, \dots, \xi_n\}$	<i>mark set</i>
$\pi ::= \emptyset$	<i>empty grant set</i>
$\{\xi \cdot \gamma\}$	<i>gates <math>\gamma</math> for subkell <math>\xi</math></i>
$\xi \cdot \mathbf{G}$	<i>all gates for subkell <math>\xi</math></i>
$\mathbf{K} \cdot \gamma$	<i>gates <math>\gamma</math> for all subkells</i>
$\mathbf{K} \cdot \mathbf{G}$	<i>all gates for all subkells</i>
$\pi \cup \pi$	<i>grant set union</i>
$\pi \setminus \pi$	<i>grant set difference</i>
$\gamma ::= \xi$   $\xi^r$   $\xi^*$	<i>gate, or gate and subordinates</i>

Figure 1: Store grammar

- Additional assertions; of the form  $\text{pred}(\dots)$ , where  $\text{pred}$  is some predicate qualifying or relating names, or variables.

The packed value, thread, and kell constructs warrant some explanation. A packed value  $v = \text{pack}(\kappa, \mathcal{T}, \sigma, \mu)$  comprises four elements: a suspended task  $\mathcal{T}$ , and its associated store  $\sigma$ ; the name  $\kappa$  of the kell that has been packed; a set of names  $\mu$  that have been *marked* to not be affected during unpacking. The suspended task and the store are captured by packing an executing kell. The set of names  $\mu$  can include the name of the packed kell, the name of procedures in the packed kell, and the name of gates in the packed kell.

A thread binding  $\tau : \text{thread}(x)$  refers to a thread named  $\tau$ , whose execution status is given by the (read-only) variable  $x$ . While the thread is running, variable  $x$  remains unbound. If the thread terminates normally, then  $x$  becomes bound to the value `terminated`. If the thread fails, because of an uncaught exception, then  $x$  becomes bound to a failed value of the form `failed(y)`, where  $y$  is the exception that caused the thread to fail. The status of a thread can be obtained using the `Status` operation.

A kell binding takes the form  $\kappa : \text{kell}(\pi, x)$ , where  $\kappa$  is the name of the kell,  $\pi$  is called the *grant set* of the kell, and where the variable  $x$  contains the execution status of the kell. Variable  $x$  remains unbound while the kell is executing. It becomes bound to the value `packed` when the kell is packed. The status of a kell is not directly accessible, but it can be obtained indirectly when packing a kell, as the kell monitoring example in Section 4 illustrates. The packed status of a kell is checked when replacing a kell (only a packed kell can be replaced). The grant set corresponds to a specification of the gates that have been opened for communication to and from subkells of  $\xi$ . If  $\kappa \cdot \gamma \in \pi$ , then gate  $\gamma$  is opened to subkell  $\kappa$  for communication. If  $K \cdot \gamma \in \pi$ , then the gate  $\gamma$  is opened to all subkells. If  $\kappa \cdot G$ , then all gates are opened to subkell  $\kappa$  for communication. If  $\kappa \cdot \gamma^x \in \pi$ , then gate  $\gamma$  and all its immediate subordinate gates are opened to subkell  $\kappa$  for communication. If  $\kappa \cdot \gamma^* \in \pi$ , then gate  $\gamma$  and all its (recursively) subordinate gates are opened to subkell  $\kappa$  for communication. If  $K \cdot G \in \pi$ , then all gates are opened to all subkells.

The predicate `need` is used for lazy evaluation. Specifically, it is used for the definition of the `waitNeeded` operation: the statement `{waitNeeded x}` blocks until the variable `x` references is needed elsewhere in a computation. The predicate `read` is used for read-only variables. `read(x)` just indicates that the variable  $x$  is read only, while `read(x, y)` indicates that the variable  $x$  is read only and that its value, when it is determined, will be that of variable  $y$ . The predicate `in`( $\xi, \zeta$ ) indicates that the kell  $\zeta$  is located inside kell  $\xi$ . The predicate `inth`( $\xi, \zeta$ ) indicates that the thread  $\zeta$  is located inside kell  $\xi$ . The predicate `subg`( $\xi, \zeta$ ) indicates that the gate  $\zeta$  is a subordinate of the gate  $\xi$ .

**Reduction relation.** The reduction relation  $\rightarrow$  is defined as the smallest subset of  $(\text{Store} \times \text{Task})^2$  that satisfies the set of inference rules given in Section 5.1 below. To facilitate the comparison with the original OZ operational semantics, and to stay close to the definition of an abstract machine for OZ/K, we use the same approach to operational semantics than the one defined in chapter 13 of [111]. In particular, we use the same notational conventions, noting  $\langle \sigma, \mathcal{T} \rangle \rightarrow \langle \sigma', \mathcal{T}' \rangle$  as  $\frac{\mathcal{T} \parallel \mathcal{T}'}{\sigma \parallel \sigma'}$ . The reduction rules take the form of inference rules of the form

$$\frac{\mathcal{T} \parallel \mathcal{T}'}{\sigma \parallel \sigma'} \quad \text{if } C$$

where  $C$  is some condition on  $\mathcal{T}$ ,  $\sigma$ ,  $\mathcal{T}'$  and  $\sigma'$ . We use a number of abbreviations to simplify the writing of reduction inference rules. The table below gathers the different abbreviations. By definition, names and tasks that appear on the right column, but that do not appear on the left column, are different from the latter, and mutually distinct, but otherwise arbitrary. Intuitively, a decorated statement such as  $S \mid_{\kappa}$  refers to a statement occurring within a thread of the kell named  $\kappa$ .

Rule	Abbreviates
$\frac{S \parallel S'}{\sigma \parallel \sigma'} \text{ if } C$	$\frac{\tau\langle S \ T \rangle \parallel \tau\langle S' \ T \rangle}{\sigma \parallel \sigma'} \text{ if } C$
$\frac{T \parallel T'}{\sigma \models \phi \parallel \sigma'} \text{ if } C$	$\frac{T \parallel T'}{\sigma \parallel \sigma'} \text{ if } C \wedge \sigma \models \phi$
$\frac{S \mid_{\kappa} \parallel S'}{\sigma \parallel \sigma'} \text{ if } C$	$\frac{\tau\langle S \ T \rangle \parallel \tau\langle S' \ T \rangle}{\sigma \models \text{inth}(\kappa, \tau) \parallel \sigma'} \text{ if } C$
$\frac{S_1 \mid_{\kappa_1} \ S_2 \mid_{\kappa_2} \parallel S'_1 \ S'_2}{\sigma \parallel \sigma'} \text{ if } C$	$\frac{\tau_1\langle S_1 \ T_1 \rangle \ \tau_2\langle S_2 \ T_2 \rangle \parallel \tau_1\langle S'_1 \ T_1 \rangle \ \tau_2\langle S'_2 \ T_2 \rangle}{\sigma \models \text{inth}(\kappa_1, \tau_1) \wedge \text{inth}(\kappa_2, \tau_2) \parallel \sigma'} \text{ if } C$
$\frac{S \mid_{\kappa} \ T \parallel S' \ T'}{\sigma \parallel \sigma'} \text{ if } C$	$\frac{\tau\langle S \ T \rangle \ T \parallel \tau\langle S' \ T \rangle \ T'}{\sigma \models \text{inth}(\kappa, \tau) \parallel \sigma'} \text{ if } C$

Table 4: Abbreviations for reduction rules

## 5.1 Reduction rules

We give in this section the inference rules that define the reduction relation. Some of these rules make use of various auxiliary functions and relations. In this section, we only present them informally. They are defined formally in Appendix A. To simplify the presentation, we do not present straightforward failure rules, which specify that a given operation fails in case its arguments are ill-typed. Failure rules are given in Appendix B. We also do not present garbage collection or obvious optimization rules which can be applied during an OZ/K computation, for instance when packing a kell and its associated store.

In the following rules, unless explicitly stated otherwise:  $\sigma$  and its decorated variants denote stores;  $\phi, \psi$  and their decorated variants denote assertions;  $\xi, \eta, \zeta$  and their decorated variants denote names;  $\kappa$  and its decorated variants denote kell names;  $\tau$  and its decorated variants denote thread names;  $\gamma$  and its decorated variants denote gate names;  $x, y, z, w, r, s$  and their decorated variants denote logical variables;  $u, v$  and their decorated variants denote values (i.e. integers, atoms, names, failed values, records);  $S$  and its decorated variants denote extended statements;  $T$  and its decorated variants denote thread stacks;  $\mathcal{U}, \mathcal{T}$  and their decorated variants denote tasks.

**Auxiliary functions and relations.** The reduction relation depends on a number of functions and relations. The first relation is an equivalence relation, noted  $\equiv$ , between tasks and between stores. Intuitively, the equivalence relation between tasks asserts that the parallel operator between tasks is commutative, and associative, and that thread stacks that differ only from an alpha-renaming of variable identifiers in the statement they contain, are equivalent. The equivalence relation between stores asserts that the conjunction of stores is commutative and associative, and that two stores are equivalent if they entail the same assertions. The entailment relation between stores and assertions, noted  $\models$ , characterizes the logical assertions that can be derived from a store. The function  $\text{dom}$  takes a store  $\sigma$  as parameter and returns the set of all the names and variables used in  $\sigma$ . The predicate  $\text{strict}_{\sigma}(v)$  is true if  $v$  is a *strict* value in the store  $\sigma$ . We extend this predicate on variables  $x$  and statements  $S$ . The predicate  $\text{strict}_{\sigma}(S, V)$  is true if all the variables in extended statement  $S$ , except those in  $V$ , are strict. The

assertion  $\text{access}_\sigma(\gamma, \kappa, \kappa')$  means that gate  $\gamma$  is accessible for communication between the kells  $\kappa$  and  $\kappa'$ . For this to be true,  $\gamma$  must have been opened for communication for all the kells on the path that connects  $\kappa$  and  $\kappa'$  in the kell tree<sup>5</sup>, unless they are separated by at most one kell boundary. The function  $\text{grant}_\sigma$  associates to the pair of variables  $(k, g)$  a set: the singleton pair corresponding to their names if  $k$  denotes a kell and  $g$  denotes a gate, and  $\emptyset$  otherwise. The last auxiliary functions are  $\text{subk}_\sigma$  and  $\text{subth}_\sigma$ :  $\text{subth}_\sigma(\kappa)$  returns the set of names of all the threads contained by the kell  $\kappa$  and all its descendant kells,  $\text{subk}_\sigma(\kappa)$  returns the set of names of all the descendant kells of kell  $\kappa$ .

### Structural rules

The contextual rules define reductions under execution contexts, – namely parallel task contexts –, and for equivalent execution structures (i.e. pairs  $\langle \text{store}, \text{task} \rangle$ ). Rules PAR and EQUIV are already present in OZ semantics.

$$[\text{PAR}] \frac{T \mathcal{U} \parallel T' \mathcal{U}}{\sigma \parallel \sigma'} \text{ if } \frac{T}{\sigma} \parallel \frac{T'}{\sigma'}$$

$$[\text{EQUIV}] \frac{\mathcal{V} \parallel \mathcal{V}'}{\gamma \parallel \gamma'} \text{ if } \frac{\mathcal{U} \parallel \mathcal{U}'}{\sigma \parallel \sigma'} \text{ and } \mathcal{U} \equiv \mathcal{V}, \mathcal{U}' \equiv \mathcal{V}', \sigma \equiv \gamma, \sigma' \equiv \gamma'.$$

### Sequential execution

$$[\text{SKIP}] \frac{\tau \langle \text{skip } T \rangle \parallel \tau : T}{\sigma \parallel \sigma}$$

$$[\text{SEQTH}] \frac{\tau \langle (S_1 S_2) T \rangle \parallel \tau \langle S_1 \langle S_2 T \rangle \rangle}{\sigma \parallel \sigma}$$

$$[\text{NIL}] \frac{\tau \langle \rangle}{\sigma \models \tau : \text{thread}(x) \wedge x = \perp} \parallel \frac{}{\sigma \wedge x = \text{terminated}}$$

The rules for sequential execution are identical to those in OZ, modulo the introduction of named threads, and the garbage collection rule NIL, that replaces the equivalent rule NIL in the OZ semantics given in [111]. Notice that only the thread stack is collected: the termination status  $x$  of thread  $\tau$  can be still be accessed.

### Thread creation

$$[\text{NEWTHT}] \frac{\tau \langle \text{thread}\{x\} S \text{ end } T \rangle \parallel \tau : T \quad \tau' \langle S \rangle}{\sigma \models x = \perp \wedge \text{inth}(\kappa, \tau) \parallel \sigma \wedge \sigma'} \quad \tau', w \notin \text{dom}(\sigma)$$

where

$$\sigma' \equiv x = \tau' \wedge \tau' : \text{thread}(w) \wedge w \wedge \text{read}(w) \wedge \text{inth}(\kappa, \tau')$$

### Variable introduction

$$[\text{VAR}] \frac{\text{local } X_1 \dots X_n \text{ in } S \text{ end}}{\sigma} \parallel \frac{S\{X_1 \rightarrow x_1, \dots, X_n \rightarrow x_n\}}{\sigma \wedge x_1 \wedge \dots \wedge x_n} \quad x_i \notin \text{dom}(\sigma)$$

<sup>5</sup>The exact rule is a bit more subtle, but see Appendix A for a formal definition.

### Read-only variables

$$[\text{READ}] \frac{x = !!y \quad \parallel \quad \text{skip}}{\sigma \models x = \perp \quad \parallel \quad \sigma \wedge z \wedge x = z \wedge \text{read}(z, y)} \quad z \notin \text{dom}(\sigma)$$

$$[\text{READU}] \frac{\parallel}{\sigma \wedge \text{read}(z, y) \models y \neq \perp \quad \parallel \quad \sigma \wedge z = y}$$

### Binding

We adopt a different approach than MOZART for variable bindings: we consider only basic bindings, i.e. bindings where, in a statement  $x = y$ , only one of  $x$  or  $y$ , previously unbound, gets bound. This behavior is captured by the rules below, where  $v$  is a value (i.e. either a base value, integer or literal, a record, a failed value, or a packed value — and hence  $v \neq \perp$ ). We note  $\text{rread}(x)$  the predicate  $\text{read}(x) \vee \exists y \text{read}(x, y)$ .

$$[\text{BINDV}] \frac{x = v \quad \parallel \quad \text{skip}}{\sigma \models x = \perp \wedge \neg \text{rread}(x) \quad \parallel \quad \sigma \wedge x = v}$$

The following rule, which defines the semantics of the variable equality statement, is actually a rule schema, with correlated  $\phi$  and  $\sigma(\phi)$ , given by the table below the rule:

$$[\text{BINDXY}] \frac{x = y \quad \parallel \quad \text{skip}}{\sigma \models \phi \quad \parallel \quad \sigma \wedge \sigma(\phi)}$$

where

$\phi$	$\sigma(\phi)$
$x = v \wedge y = \perp \wedge \neg \text{rread}(y)$	$x = y$
$y = v \wedge x = \perp \wedge \neg \text{rread}(x)$	$x = y$
$x = \perp \wedge \neg \text{rread}(x) \wedge y = \perp \wedge \neg \text{rread}(y)$	$x = y$
$x = \perp \wedge y = \perp \wedge \text{rread}(x) \wedge \neg \text{rread}(y)$	$x = y \wedge \text{read}(y)$
$x = \perp \wedge y = \perp \wedge \neg \text{rread}(x) \wedge \text{rread}(y)$	$x = y \wedge \text{read}(x)$

$$[\text{BINDR}] \frac{x = y.z \quad \parallel \quad \text{skip}}{\sigma \models x = \perp \wedge \neg \text{rread}(x) \wedge y = l(f_1 : w_1 \dots f_n : w_n)_m \wedge z = f_i \quad \parallel \quad \sigma \wedge x = w_i}$$

### Unification

The `Unify` operation is defined by the rules `UNI` and `UNIF`. It essentially implements the *naive tell semantics* discussed in chapter 13 of [111].

$$[\text{UNI}] \frac{\{\text{Unify } x \ y\} \quad \parallel \quad \text{skip}}{\sigma \quad \parallel \quad \sigma'} \quad \text{if } \sigma' = \text{Unify}(x, y, \sigma) \not\equiv \perp$$

$$[\text{UNIF}] \frac{\{\text{Unify } x \ y\} \quad \parallel \quad \text{raise error}(\text{uni}(x \ y)) \text{ end}}{\sigma \quad \parallel \quad \sigma} \quad \text{if } \text{Unify}(x, y, \sigma) \equiv \perp$$

### Equality between values

Two operations are possible on all values. They correspond to equality and inequality tests. Note that as syntactic convenience we write  $x == Y$  for  $\{\text{Equal } X \ Y \ \$\}$  (i.e. the value of  $x == Y$  is the boolean returned by operation `Equal`), and  $x \neq Y$  for  $\{\text{NotEqual } X \ Y \ \$\}$ , where the function `NotEqual` can be defined as

```

proc {NotEqual X Y R}
  if {Equal X Y $} then R = false else R = true end
end

```

Note that the operation `Equal` suspends if the checks  $x \equiv_\sigma y$  and  $x \bowtie_\sigma y$  cannot take place.

$$\begin{array}{l}
[\text{EQTRUE}] \frac{\{\text{Equal } x \ y \ r\} \parallel \text{skip}}{\sigma \models r = \perp} \parallel \frac{\text{skip}}{\sigma \wedge r = \text{true}} \text{ if } x \equiv_\sigma y \\
[\text{EQFALSE}] \frac{\{\text{Equal } x \ y \ r\} \parallel \text{skip}}{\sigma \models r = \perp} \parallel \frac{\text{skip}}{\sigma \wedge r = \text{false}} \text{ if } x \bowtie_\sigma y
\end{array}$$

### Status

The operation `Status` returns the status of a thread. This is captured by the following rule. Note that it is only possible to check the status of a thread that resides in the current kell: this is to ensure separation between kells.

$$[\text{STATUS}] \frac{\{\text{Status } x \ y\} |_\kappa}{\sigma \models y = \perp \wedge x = \tau \wedge \tau : \text{thread}(w) \wedge \text{in}(\kappa, \tau)} \parallel \frac{\text{skip}}{\sigma \wedge y = w}$$

### If statement

The if statement is identical to the original OZ if statement.

$$\begin{array}{l}
[\text{IFTRUE}] \frac{\text{if } x \text{ then } S_1 \text{ else } S_2 \text{ end} \parallel S_1}{\sigma \models x = \text{true}} \parallel \sigma \\
[\text{IFFALSE}] \frac{\text{if } x \text{ then } S_1 \text{ else } S_2 \text{ end} \parallel S_2}{\sigma \models x = \text{false}} \parallel \sigma
\end{array}$$

### Case statement

The case statement is identical to the original OZ case statement.

$$\begin{array}{l}
[\text{CASE}] \frac{\text{case } x \text{ of } J \text{ then } S_1 \text{ else } S_2 \parallel S_1 \theta}{\sigma} \parallel \frac{S_1 \theta}{\sigma} \text{ if } \text{match}_\sigma(x, J) = \theta \\
[\text{CASEU}] \frac{\text{case } x \text{ of } J \text{ then } S_1 \text{ else } S_2 \parallel S_2}{\sigma} \parallel \frac{S_2}{\sigma} \text{ if } \text{match}_\sigma(x, J) = \perp
\end{array}$$

### Names

$$[\text{NEWNAME}] \frac{\{\text{NewName } x\}}{\sigma \models x = \perp} \parallel \frac{\text{skip}}{\sigma \wedge x = \eta} \ \eta \notin \text{dom}(\sigma)$$

### Procedure abstraction

Rules governing the introduction of procedures (PNEW), and procedures calls (PCALL) are similar to the ones in OZ. However, compared to OZ, we allow a dynamic update of procedure values, through the rule PREP.

The introduction of a new procedure is governed by the following rule.

$$[\text{PNEW}] \frac{\text{proc}\{x X_1 \dots X_n\} S \text{ end} \parallel \sigma \models x = \perp}{\sigma \wedge x = \xi \wedge \xi : \text{proc}\{\$ X_1 \dots X_n\} S \text{ end}} \parallel \frac{\text{skip}}{\xi \notin \text{dom}(\sigma)}$$

Calling a procedure is governed by the following rule.

$$[\text{PCALL}] \frac{\{x x_1 \dots x_n\} \parallel \sigma \models x = \xi \wedge \xi : \text{proc}\{\$ X_1 \dots X_n\} S \text{ end}}{S\{X_1 \rightarrow x_1, \dots, X_n \rightarrow x_n\} \parallel \sigma}$$

Replacing a procedure is governed by the following rule. The rule expects an already existing procedure under the name  $\xi$ , and just replaces the closure associated with the name  $\xi$ . The replacement procedure must have the same number of arguments than the replaced one.

$$[\text{PREP}] \frac{\text{proc}\{x X_1 \dots X_n\} S \text{ end} \parallel \sigma \wedge \xi : Q \models x = \xi}{\sigma \wedge \xi : P} \text{ if } C$$

where

$$Q = \text{proc}\{\$ X_1 \dots X_n\} S' \text{ end} \quad P = \text{proc}\{\$ X_1 \dots X_n\} S \text{ end} \quad C \equiv \text{strict}_\sigma(S, \emptyset)$$

### Checking determinacy

$$[\text{DETRTRUE}] \frac{\{\text{IsDet } x y\} \parallel \sigma \models x \neq \perp \wedge y = \perp}{\sigma \wedge y = \text{true}} \parallel \frac{\text{skip}}{\sigma \wedge y = \text{true}}$$

$$[\text{DETRFALSE}] \frac{\{\text{IsDet } x y\} \parallel \sigma \models x = \perp \wedge y = \perp}{\sigma \wedge y = \text{false}} \parallel \frac{\text{skip}}{\sigma \wedge y = \text{false}}$$

### Cells

$$[\text{NCELL}] \frac{\{\text{NewCell } x y\} \parallel \sigma \models y = \perp}{\sigma \wedge \xi : \text{cell}(x) \wedge y = \xi} \parallel \frac{\text{skip}}{\xi \notin \text{dom}(\sigma)}$$

$$[\text{ECELL}] \frac{\{\text{Exchange } x y z\} \parallel \sigma \wedge x = \xi \wedge \xi : \text{cell}(t) \models y = \perp}{\sigma \wedge x = \xi \wedge \xi : \text{cell}(z) \wedge y = t} \parallel \frac{\text{skip}}{\sigma \wedge x = \xi \wedge \xi : \text{cell}(z) \wedge y = t}$$

### Exception handling

$$[\text{TRYU}] \frac{\text{try } S_1 \text{ catch } X \text{ then } S_2 \text{ end} \parallel \sigma}{S_1(\text{catch } X \text{ then } S_2 \text{ end}) \parallel \sigma}$$

$$[\text{TRYC}] \frac{\text{catch } X \text{ then } S_2 \text{ end} \parallel \sigma}{\text{skip} \parallel \sigma}$$

$$[\text{RAISEW}] \frac{\tau\langle \text{raise } x \text{ end } \langle S T \rangle \rangle \parallel \sigma}{\tau\langle \text{raise } x \text{ end } T \rangle \parallel \sigma} S \neq \text{catch} \dots \text{end}$$

$$\begin{array}{c}
\text{[RAISE]} \frac{\tau \langle \mathbf{raise} \ x \ \mathbf{end} \ \langle S \ T \rangle \rangle \parallel \tau \langle S_2 \{ X \rightarrow x \} \ T \rangle}{\sigma} \quad S \equiv \mathbf{catch} \ X \ \mathbf{then} \ S_2 \ \mathbf{end} \\
\text{[RAISES]} \frac{\tau \langle \mathbf{raise} \ x \ \mathbf{end} \ \langle \rangle \rangle}{\sigma \models \tau : \mathbf{thread}(w) \wedge w = \perp} \parallel \frac{}{\sigma \wedge w = \mathbf{failed}(x)}
\end{array}$$

When a thread statement sequence has finished executing in a failed state, raised exceptions can be handled through the `Status` operation. Note that the `RAISES` rule is a form of garbage collection for abnormally terminated threads that complements the `NIL` garbage collection rule for normally terminated threads.

### By-need synchronization

The rule for by-need synchronization is given below. It depends on the relation `need`, which is defined below.

$$\text{[WAITN]} \frac{\{ \mathbf{waitNeeded} \ x \} \parallel \mathbf{skip}}{\sigma \models \mathbf{need}(x)} \parallel \frac{}{\sigma}$$

The predicate `need(x)` is added to the store according to the following rules:

$$\text{[NEED]} \frac{S}{\sigma \not\models \mathbf{need}(x)} \parallel \frac{S}{\sigma \wedge \mathbf{need}(x)} \quad \text{if } \mathbf{need}_\sigma(S, x)$$

$$\text{[NEEDD]} \frac{}{\sigma \models x \neq \perp} \parallel \frac{}{\sigma \wedge \mathbf{need}(x)} \quad \text{if } \sigma \not\models \mathbf{need}(x)$$

The assertion `needσ(S, x)` is true if and only if the following conditions hold:

1.  $\sigma \models x = \perp$ .
2. No reduction is possible for  $S$  with store  $\sigma$ .
3. There exists a set  $\beta$  of variable bindings such that  $\sigma' \wedge \beta$  is consistent and a reduction is possible for  $S$  with store  $\sigma' \wedge \beta$ .
4. For all  $\beta$  satisfying the above condition,  $\sigma' \wedge \beta \models x \neq \perp$ .

### Failed values

The rule for the creation of failed values is given below.

$$\text{[FAILC]} \frac{\{ \mathbf{FailedValue} \ x \ y \} \parallel \mathbf{skip}}{\sigma \models y = \perp} \parallel \frac{}{\sigma \wedge y = z \wedge z = \mathbf{failed}(x)} \quad z \notin \text{dom}(\sigma)$$

The second rule for failed values ensures that needing a failed value raises an exception.

$$\text{[FAILW]} \frac{S}{\sigma \models y = \mathbf{failed}(x)} \parallel \frac{\mathbf{raise} \ x \ \mathbf{end}}{\sigma} \quad \text{if } \mathbf{need}_\sigma(S, y)$$

### Strictness check

The rules for checking whether a value is strict or not are given below.

$$\text{[STRICTTRUE]} \frac{\{ \mathbf{IsStrict} \ x \ y \} \parallel \mathbf{skip}}{\sigma \models y = \perp} \parallel \frac{}{\sigma \wedge y = \mathbf{true}} \quad \text{if } \mathbf{strict}_\sigma(x)$$

$$\text{[STRICTFALSE]} \frac{\{ \mathbf{IsStrict} \ x \ y \} \parallel \mathbf{skip}}{\sigma \models y = \perp} \parallel \frac{}{\sigma \wedge y = \mathbf{false}} \quad \text{if } \neg \mathbf{strict}_\sigma(x)$$

### Gate abstraction

The rules for the creation of new gates are given below. The second rule creates a gate that is subordinate to an existing one.

$$\begin{aligned} \text{[NEWG]} \quad & \frac{\{\text{NewGate } x\}}{\sigma \models x = \perp} \parallel \frac{\text{skip}}{\sigma \wedge x = \gamma \wedge \gamma : \text{gate}} \quad \gamma \notin \text{dom}(\sigma) \\ \text{[NEWGS]} \quad & \frac{\{\text{NewGate } x\#z\}}{\sigma \models z = \perp \wedge x = \gamma \wedge \gamma : \text{gate}} \parallel \frac{\text{skip}}{\sigma \wedge z = \gamma' \wedge \gamma' : \text{gate} \wedge \text{subg}(\gamma, \gamma')} \quad \gamma' \notin \text{dom}(\sigma) \end{aligned}$$

The rule COM governs communication through gates.

$$\text{[COM]} \quad \frac{\{\text{Send } g \ x\} |_{\kappa} \quad \{\text{Receive } h \ y\} |_{\kappa'}}{\sigma \models y = \perp \wedge \phi} \parallel \frac{\text{skip} \quad \text{skip}}{\sigma \wedge y = x} \quad \text{if } \text{strict}_{\sigma}(x) \wedge \text{access}_{\sigma}(\gamma, \kappa, \kappa')$$

where

$$\phi \equiv g = \gamma \wedge h = \gamma \wedge \gamma : \text{gate}$$

### Opening and closing

The ability for a kell to communicate with its environment is governed by the `Open` and `Close` operations. Operation `Open` opens a gate for communication for a subkell of the current kell, whereas `Close` closes this gate for communication. There are thus two prerequisites for a successful communication: (i) knowing a gate name, and (ii) having an access path established (through previous `Open` operations) to cross the required kell boundaries. Note that both arguments to primitives `Open` and `Close` can take the value `all`. If the first argument is `all`, this means that the gate specified in the second argument is opened or closed to all children of the current kell. If the second argument is `all`, this means that all the gates are opened, or closed, to the subkell specified in the first argument.

The rules that define the semantics of operations `Open` and `Close` are given below.

$$\begin{aligned} \text{[OPEN]} \quad & \frac{\{\text{Open } k \ g\} |_{\kappa}}{\sigma \wedge \kappa : \text{kell}(\pi, w)} \parallel \frac{\text{skip}}{\sigma \wedge \kappa : \text{kell}(\pi \cup \text{grant}(\sigma, k, g), w)} \quad \text{if } \text{grant}(\sigma, k, g) \neq \emptyset \\ \text{[CLOSE]} \quad & \frac{\{\text{Close } k \ g\} |_{\kappa}}{\sigma \wedge \kappa : \text{kell}(\pi, w)} \parallel \frac{\text{skip}}{\sigma \wedge \kappa : \text{kell}(\pi \setminus \text{grant}(\sigma, k, g), w)} \quad \text{if } \text{grant}(\sigma, k, g) \neq \emptyset \end{aligned}$$

### Kell abstraction

The rules pertaining to the kell abstraction deal with the creation and the replacement of kells. The rule for kell creation is similar to the rule for thread creation. It creates a new kell as well as a new thread which begins executing the body of kell statement:

$$\text{[NEWKELL]} \quad \frac{\text{kell}\{y\} \text{S end } |_{\kappa}}{\sigma \models y = \perp} \parallel \frac{\text{skip} \quad \tau' \langle S \ \rangle}{\sigma \wedge \sigma'} \quad \text{if } C$$

where

$$C \equiv \kappa', \tau', w, r \notin \text{dom}(\sigma) \wedge \text{strict}_{\sigma}(S, \{y\})$$

$$\sigma' \equiv y = \kappa' \wedge \kappa' : \text{kell}(\emptyset, w) \wedge w \wedge \text{read}(w) \wedge \tau' : \text{thread}(r) \wedge r \wedge \text{read}(r) \wedge \text{inth}(\kappa', \tau') \wedge \text{in}(\kappa, \kappa')$$

The rule for kell replacement is similar to the rule of procedure replacement. It allows the replacement of a silent (i.e. non running) kell by a new one while preserving the original kell name. A side effect of this replacement is to change the status of the replaced kell to active (run) again.

$$[\text{KREP}] \frac{\text{kell}\{y\} S \text{ end} \mid_{\kappa} \parallel \text{skip} \quad \tau'\langle S \ \rangle}{\sigma \wedge \kappa' : \text{kell}(\pi, w) \models \phi} \text{ if } C$$

where

$$\begin{aligned} C &\equiv \tau', r, s \notin \text{dom}(\sigma) \wedge \text{strict}_{\sigma}(S, \emptyset) \\ \phi &\equiv y = \kappa' \wedge w = \text{packed} \wedge \text{in}(\kappa, \kappa') \\ \sigma' &\equiv \kappa' : \text{kell}(\pi, s) \wedge s \wedge \text{read}(s) \wedge \tau' : \text{thread}(r) \wedge r \wedge \text{read}(r) \wedge \text{inth}(\kappa', \tau') \end{aligned}$$

### Packed values

Packed values can be modified by means of the `Mark` operation. The `Mark` operation takes as input a change instruction, in the form of a pair of names. The first name of the input pair specifies the name of the gate or procedure to replace in the packed value. The second name of the input pair specifies the name of the replacement gate or procedure. A side effect of the operation is that gates or procedures that have thus *marked* do not get renamed upon unpacking. The first rule concerns the replacement of gates.

$$[\text{MARKG}] \frac{\{\text{Mark } z \text{ gate}(x \ y) \ p\}}{\sigma \models \phi \wedge p = \perp} \parallel \frac{\text{skip}}{\sigma \wedge p = \text{pack}(\omega, \mathcal{T}\theta, \sigma'\theta, \mu \cup \{\gamma\})} \text{ if } C$$

where

$$\begin{aligned} C &\equiv \sigma' \models \gamma' : \text{gate} \wedge \theta = \{\gamma' \rightarrow \gamma\} \\ \phi &\equiv z = \text{pack}(\omega, \mathcal{T}, \sigma', \mu) \wedge x = \gamma' \wedge \gamma' : \text{gate} \wedge y = \gamma \wedge \gamma : \text{gate} \end{aligned}$$

The next rule deals with the replacement of a procedure inside a packed value. The operation is similar to the replacement of gates, and has an effect similar to the rule `PREP` that governs the replacement of procedures, except this replacement takes place inside a packed value.

$$[\text{MARKP}] \frac{\{\text{Mark } z \text{ proc}(x \ y) \ p\}}{\sigma \models \phi \wedge p = \perp} \parallel \frac{\text{skip}}{\sigma \wedge p = \text{pack}(\omega, \mathcal{T}\theta, \sigma_2, \mu \cup \{\xi\})} \text{ if } C$$

where

$$\begin{aligned} \phi &\equiv z = \text{pack}(\omega, \mathcal{T}, \sigma_1, \mu) \wedge x = \xi' \wedge \xi' : \text{proc}\{\$ X_1 \dots X_n\} S' \text{ end} \wedge y = \xi \wedge \xi : \text{proc}\{\$ X_1 \dots X_n\} S \text{ end} \\ C &\equiv \theta = \{\xi' \rightarrow \xi\} \wedge \text{strict}_{\sigma}(S, \emptyset) \wedge \sigma_2 \equiv \sigma_1 \theta \wedge \xi : \text{proc}\{\$ X_1 \dots X_n\} S \text{ end} \end{aligned}$$

### Packing

The rule for packing is given below. Notice that packing implies passivating the target kell, together with all of its subkells. Packing produces a *packed value*, which encapsulates the part of the current execution structure corresponding to the target kell. The set of marks of the resulting packed value is initially empty.

$$[\text{PACK}] \frac{\{\text{Pack } x \ y\} \mid_{\kappa} \quad \tau_1 : T_1 \dots \tau_n : T_n \parallel \text{skip} \quad \emptyset}{\sigma \models \bigwedge_{i=1}^m \phi_i \wedge y = \perp \wedge \phi} \parallel \frac{\text{skip} \quad \emptyset}{\sigma \wedge \sigma'} \text{ if } C$$

where

$$\begin{aligned} C &\equiv \text{subth}_{\sigma}(\kappa_0, \{\tau_1, \dots, \tau_n\}) \wedge \text{subk}_{\sigma}(\kappa_0, \{\kappa_1, \dots, \kappa_m\}) \\ \phi &\equiv x = \kappa_0 \wedge \kappa_0 : \text{kell}(\pi, z) \wedge z = \perp \wedge \text{in}(\kappa, \kappa_0) \quad \phi_i \equiv \kappa_i : \text{kell}(\pi_i, w_i) \wedge w_i = \perp \\ \sigma' &\equiv \bigwedge_{i=1}^m w_i = \text{packed} \wedge y = \text{pack}(\kappa_0, \mathcal{T}, \sigma, \emptyset) \wedge z = \text{packed} \quad \mathcal{T} \equiv \tau_1 : T_1 \dots \tau_n : T_n \end{aligned}$$

The rule for unpacking is given below. Unpacking creates an execution structure which is similar to the packed one, except all the variables and all the non-marked names in the packed structure are renamed to avoid any potential conflict between the current store,  $\sigma$ , and the unpacked one,  $\sigma'$ . In addition, unpacking returns a list of pairs, called the *name list*. The first elements  $\xi_i$  pairs in the name list are all the gate names that appear in the packed value. The second element  $\xi_i \theta$  of a pair in the name list is the new name which has been substituted to the first element of the pair during unpacking.

$$[\text{UNPACK}] \frac{\{\text{Unpack } y \ x\} |_{\kappa} \quad \emptyset}{\sigma \models \kappa : \mathbf{kell}(\pi, z) \wedge x = \perp \wedge y = \mathbf{pack}(\kappa_0, \mathcal{T}, \sigma', \mu)} \parallel \frac{\mathbf{skip} \quad \mathcal{T}\theta\theta'}{\sigma \wedge \sigma'''} \text{ if } C_1 \wedge C_2$$

where

$$\begin{aligned} C_1 &\equiv (\text{dom}(\theta) = \text{dom}(\sigma') \setminus \mu) \wedge (\sigma \wedge \sigma''' \not\equiv \perp) \wedge \forall l \in \text{ran}(\theta), l \notin \text{dom}(\sigma, \sigma') \\ C_2 &\equiv (\sigma' \equiv \sigma'' \wedge \kappa_0 : \mathbf{kell}(\pi', z')) \wedge \theta' = \{\kappa_0 \rightarrow \kappa\} \wedge \{\xi_1, \dots, \xi_n\} = \mathbf{gn}(\mathcal{T}, \sigma') \\ \sigma''' &\equiv x = [(\xi_1 \ \xi_1 \theta) \dots (\xi_n \ \xi_n \theta)] \wedge \sigma'' \theta \theta' \wedge \bigwedge_{\kappa' \in \text{tkn}_{\sigma'}(\mathcal{T})} \mathbf{in}(\kappa, \kappa' \theta) \end{aligned}$$

## 5.2 OZ/K properties

This section gathers elementary properties of the OZ/K operational semantics. We let  $\rightarrow^*$  denote the reflexive and transitive closure of the reduction relation  $\rightarrow$ . We say that an execution structure  $(\sigma, \mathcal{T})$  results from the execution of an OZ/K statement, if there exists a OZ/K statement  $S$  such that  $(\sigma_0, \tau \langle S \rangle) \rightarrow^* (\sigma, \mathcal{T})$ , where  $\sigma_0 \equiv \tau : \mathbf{thread}(w) \wedge w \wedge \mathbf{read}(w) \wedge \mathbf{inth}(\top, \tau)$ .

We say that a task  $\mathcal{T}$  belongs to a kell  $\kappa$  if for all names  $\tau$  of threads in  $\mathcal{T}$ , we either have  $\mathbf{inth}(\kappa, \tau)$  or  $\mathbf{inth}(\kappa', \tau)$ , where  $\kappa'$  is a descendant kell of the kell  $\kappa$ . The following proposition establishes the separation property for OZ/K computation. It asserts that two distinct kells in an execution structure cannot hold references to the same unbound variable (either directly, or indirectly, through cells, procedures, etc).

**Proposition 1** *Assume  $(\sigma, \mathcal{T})$ , with  $\mathcal{T} \equiv \mathcal{T}_1 \ \mathcal{T}_2 \ \mathcal{T}'$ , is an execution structure that result from the execution of an OZ/K statement, where  $\mathcal{T}_1$  belongs to kell  $\kappa_1$ ,  $\mathcal{T}_2$  belongs to kell  $\kappa_2$ , and  $\kappa_1 \neq \kappa_2$ . If  $\sigma \models x = \perp$ , and  $x \in \mathbf{v}(\mathcal{T}_1, \sigma)$ , then  $x \notin \mathbf{v}(\mathcal{T}_2, \sigma)$ .*

**Proof:** See Appendix C. □

The following proposition asserts a form of *perfect firewall* property for OZ/K, namely, that there exists an execution structure where a task can be completely isolated from the rest of the other tasks in the execution structure. Let  $(\mathcal{T}, \sigma)$  be an execution structure. We say that  $\kappa$  *appears at the top level* in  $(\mathcal{T}, \sigma)$ , if  $\sigma = \sigma' \wedge \mathbf{in}(\top, \kappa)$ , for some  $\sigma'$ . We also say that  $\kappa$  is *not referenced in  $\mathcal{T}$*  if there exists no variable  $x$  such that  $x \in \mathbf{v}(\mathcal{T}, \sigma)$  and  $\sigma = \sigma' \wedge x = \kappa$ , for some  $\sigma'$ .

**Proposition 2** *Let  $(\mathcal{T} \ \mathcal{T}_{\kappa}, \sigma)$  be an execution structure that results from the execution of a OZ/K statement, where  $\kappa$  appears at the top level,  $\mathcal{T}_{\kappa}$  is the set of all threads that belong to  $\kappa$ ,  $\kappa$  is not referenced in  $\mathcal{T}$ , there is no thread  $\tau$  such that  $\sigma \models \mathbf{inth}(\kappa, \tau)$ , and  $\sigma = \sigma_0 \wedge \kappa : \mathbf{kell}(\emptyset, w)$ , for some  $\sigma_0, w$ . The reductions possible from  $\langle \sigma, \mathcal{T} \ \mathcal{T}_{\kappa} \rangle$  can only be of one of the following two forms:*

$$\frac{\mathcal{T} \ \mathcal{T}_{\kappa} \parallel \mathcal{T}' \ \mathcal{T}_{\kappa}}{\sigma \parallel \sigma'} \text{ or } \frac{\mathcal{T} \ \mathcal{T}_{\kappa} \parallel \mathcal{T} \ \mathcal{T}'_{\kappa}}{\sigma \parallel \sigma'}$$

where  $\mathcal{T}'_{\kappa}$  is the set of threads that belong to  $\kappa$  in execution structure  $(\mathcal{T} \ \mathcal{T}'_{\kappa}, \sigma')$ , and  $\sigma'$  is such that there is no  $\tau$  such that  $\sigma' \models \mathbf{inth}(\kappa, \tau)$ , and  $\sigma' = \sigma'_0 \wedge \kappa : \mathbf{kell}(\emptyset, w)$ , for some  $\sigma'_0$ .

**Proof:** See Appendix C. □

Informally, if we denote by  $\kappa[\mathcal{T}]$  a task  $\mathcal{T}$  whose threads belong to  $\kappa$ , the proposition asserts that a kell structure of the form  $\kappa[\kappa_1[\mathcal{T}_1] \dots \kappa_n[\mathcal{T}_n]]$  at the top level, where  $\kappa$  is not referenced outside of  $\kappa[\dots]$  (and thus cannot be packed), constitutes a perfect firewall for the tasks  $\mathcal{T}_1, \dots, \mathcal{T}_n$ . This can be understood intuitively since there is no thread in kell  $\kappa$  (condition *there is no thread named  $\tau$  such that  $\sigma \models \mathbf{inth}(\kappa, \tau)$ ) that can act as a relay of communication between threads in  $\mathcal{T}_1, \dots, \mathcal{T}_n$  and the outer environment, and since there is no gate opened in  $\kappa$  for such communication (condition  $\sigma = \sigma_0 \wedge \kappa : \mathbf{kell}(\emptyset, w)$ ).*

## 6 Discussion

### 6.1 Component granularity in Oz/K

As currently designed, Oz/K supports different forms of components. Kells, of course, constitutes primitive components. Notions of components which can be built in standard Oz, such as e.g. port objects, active objects, and modules, and their variants (such as e.g. port objects sharing one thread) are also available to Oz/K programmers. This variety of component forms allows programmers to build component-based programs at different granularities. For instance, having multiple port objects sharing the same thread can reduce their cost to that of a standard object, whereas a kell can be as small as a single thread or an active object<sup>6</sup>. However, these different components do not share the same properties, e.g. with respect to passivation and objective reconfiguration, or isolation. It might be beneficial to study how to unify further the different notions involved. For instance, the `kell` and `thread` constructs share many characteristics, and one could think of applying the passivation operation to a single thread. The current Oz/K design distinguishes threads and kells because of their different communication capabilities. However, this distinction could be lifted if one allowed other forms of communication between kells, and a different passivation semantics.

### 6.2 Encapsulation and sharing in Oz/K

The kell construct in Oz/K enforces a strong encapsulation, exemplified by the firewall property (Proposition 2 in Section 5.2). This strong encapsulation prevents communications between subkells or between a subkell and its parent kell's environment, to bypass the parent kell, which can thus act as a sandbox. Sandboxing can be done without any knowledge of the behavior of an encapsulated kell, as exemplified by the sandbox examples in Section 4, and thus provides a simple way to enforce given protocols for establishing communication with an environment outside a sandbox. The encapsulation realized by the kell construct allows in particular to build wrappers as in the Boxed- $\pi$  calculus [102].

The strong encapsulation provided by the kell construct can become a hindrance, however, when building software architectures with component sharing [61]. For instance, A logger might be used to provide a logging service to different components in a software structure, whose locations, in the component hierarchy, can be arbitrary. A component such as the logger, can be understood as being *shared* among all the composite components that encapsulate its client components. In Oz/K, component structures with sharing can be approximated using gate opening. For instance, a logger configuration, with two components C1 and C2 that use the logging service, placed inside composite components CA1 and CB1, and CA2, respectively, can be defined as follows (with LG the gate at which the logging service is made available by the logger `Logger`):

```

kell{CA1}
  kell{CB1}
    kell{C1} ... end
    {Open C1 LG}
    ...
  end
  {Open CB1 LG}
end

kell{CA2}
  kell{C2} ... end
  {Open C2 LG}
  ...
end

kell{Logger} ... end
{Open all LG}

```

<sup>6</sup>Note that threads in Oz are extremely lightweight, which authorizes in Oz the liberal use of port objects as units of modularity. The cost of a kell at execution is no higher than that of an Oz port object.

In the above program sketch, sharing the `Logger` component amounts to establishing direct communication channels with it, by opening the `LG` gate for communication at all the required levels of the component structure. In the case of a logging service, the communication between a logger client and the logger is typically unidirectional, with the client just requesting that some information be logged in a single message. If a service such as a database management service is shared, then one would expect to have interactions between a client and the database take the form of requests with responses. We can accommodate simply this kind of interactions using subordinate gates. For instance, with a component structure similar to the one above, we would set:

```

kell{CA1}
  kell{CB1}
    kell{C1} ... end
    {Open C1 LG#all}
    ...
  end
  {Open CB1 LG#all}
end

kell{CA2}
  kell{C2} ... end
  {Open C2 LG#all}
  ...
end

kell{Database} ... end
{Open all LG#all}

```

Instructions of the form `{Open C LG#all}` open the gate `LG` for direct communication but also all of its subordinate gates, thus allowing, for instance, a subordinate gate to be used as a continuation in a request / response interaction style. If necessary, one can protect the different gates from tampering by the component involved by building e.g. the equivalent of `FRACTAL` interfaces, as shown in Section 6.7, which encapsulate gates and a particular interaction protocol. Unfortunately, we do not know how to avoid decorating the whole component structure with `Open` instructions in order to model component structures with sharing. It seems there is a basic tension between the need for strict encapsulation, as required e.g. for writing wrappers for untrusted components, and the definition of “natural” component architectures with sharing. One could of course imagine adopting a different stance for `OZ/K`, which would consist in turning by default all kells into transparent ones (i.e. ones which would allow direct communication on all gates), and in adding a new operation allowing to retrieve and monitor the set of gates used for communication by `kell`. Creating transparent kells is easy in `OZ/K`. The following procedure creates such kells:

```

proc{NewKell P K}
  kell{K} {P} {Open all all} end
  {Open K all}
end

```

The body of the `kell` is input to procedure `NewKell` in the form of a nullary procedure, `P`. The first `Open` statement allows all the subkells of the transparent `kell` `K` to have the same communication rights as threads in `K`. The second `Open` statement allows the content of `K` to have the same communication rights as threads in the current `kell`.

However we do not have the possibility to dynamically monitor the gates of a `kell` and preventing communication using only transparent kells. The problem with the alternate approach would thus be to devise an appropriate primitive for this gate monitoring and selective gate communication prevention.

A different approach to the issues of encapsulation and sharing would be to rely on a static type system, to ensure proper encapsulation in a context where sharing is the norm, as with object-based languages. The solutions devised for object-oriented languages to overcome the aliasing issues, would be relevant here. For instance, Clarke et al. [36, 37] introduce *ownership types* which attribute to each object *obj* an *owner* that controls the references to *obj*. Similar types are used in ArchJava [3] to ensure a form of component encapsulation called *communication*

*integrity*. To what extent these ideas, together with the techniques for typing and the dynamic binding of modules developed for Alice and Acute can be exploited in our setting, remains for further study.

### 6.3 On network independence

The principle of network independence actually leads us to avoid introducing in the language any abstraction for remote communication or remote execution. This may seem paradoxical in a language intended for distributed programming, but this decision actually opens the way for the introduction of many different forms of abstractions for distributed programming. Let us explain this in more detail<sup>7</sup>. The only abstraction for distribution which we introduce is OZ/K, is that of a *kell*, a form of locality as can be found in distributed process calculi such as  $D\pi$  or Mobile Ambients. The presence of kells means that we can partition OZ/K computations into separate places. Now, these places can be realized either as data structures and programs executing on a given machine, or as whole machines, together with their software environment – including e.g. a virtual machine for running OZ/K programs. In other words we can view an OZ/K statement either as an *executable program*, to be run e.g. on an OZ/K virtual machine, or as a *model*, that specifies the behavior of some system. A distributed environment with two machines  $M_1$  and  $M_2$ , an interconnection network  $N$ , can be modelled by an OZ/K statement of the form:

```
kell{N} {NetBehavior} end
kell{M1} {MachineBehavior M1 Add1 Program1} end
kell{M2} {MachineBehavior M2 Add2 Program2} end
thread {TopLevelBehavior} end
```

where `Program1` and `Program2` correspond to OZ/K statements to execute on  $M_1$  and  $M_2$ , respectively, and where `Add1` and `Add2` correspond to addresses of  $M_1$  and  $M_2$  on the network, and the overall behavior of the network is modelled by the two statements `TopLevelBehavior` and `NetBehavior` (the statement `TopLevelBehavior` typically merely provides for the opening and closing of gates for a direct communication between the network *kell*  $N$  and the machine kells  $M_1$  and  $M_2$ ). The procedure `MachineBehavior` can also be seen as having a structure of the form

```
proc{MachineBehavior M Add Program}
  VM in
    thread {VirtualMachinery VM} end
    kell{VM} {Program M Add} end
end
```

which means it consists in running a `Program` – here described as a procedure that takes as argument the name of the local machine (e.g. a gate representing its IP domain name), and its address (e.g. its IP address) – together with some additional virtual machinery.

The important point to notice is this: because we have separated the computation space into different kells, these can be realized in different ways. For instance, the *kell* `VM` above can be realized as an OZ/K virtual machine, able to execute OZ/K statements, such as `{Program M Add}`, whereas kells  $M_1$ ,  $M_2$ ,  $N$  will model the actual machines and network, that support the execution of the two copies of the `VM` virtual machine in our distributed environment. The statements `NetBehavior`, and `{MachineBehavior M1 Add1 Program1}`, should then be considered not as executable OZ/K programs, but as models of the behavior of  $N$ , and  $M_1$ , respectively. From the point of view of an OZ/K programmer, programming in a distributed setting means accessing, using the communication constructs in OZ/K (sending and receiving on gates), the services available in the realization of `MachineBehavior` and `NetBehavior`, just as if they were ordinary OZ/K programs. In other terms, to program in a distributed environment, we just require that its basic services (typically, communication services) be made available as OZ/K abstractions. From a semantical point of view, the boundary between actual programs and models of the supporting environment, is clearly marked, thanks to the separation of computation in different kells.

In a sense, this approach is comparable to a distributed computing extension to an object-based language that relies on special objects that wrap distributed services available from the supporting environment (communication

<sup>7</sup> A similar case has been made for a network independent abstract machine for the Kell calculus in [21].

libraries, machines and networks). Contrary to classical distributed extensions to object-oriented languages, such as RMI for Java or Network Objects for Modula 3 [22], however, we do not try to extend the semantics of the language local communication primitive — method invocation in the case of Java and Modula 3 — to cover the case of remote execution. Instead, all communications in OZ/K, including those with distributed services, retain their semantics, and remain strictly local. Also, note an important distinction: with our approach, distributed execution can be described within the semantics of the language, because of the introduction of localities; this is not the case with the network objects approach, where distributed execution is not captured by the language semantics. Having a primitive notion of locality in the language semantics allows to specify different forms of separation, much like having a notion of thread in the language semantics allows to formalize concurrent execution and synchronization, compared to an approach where concurrency is introduced as an external library to a sequential language, and thus is not part of the language semantics.

Overall, our approach to distribution is similar to that of Acute, which adheres to the belief that “a general-purpose distributed programming language should not have a built-in commitment to any particular means of interaction” [101, 99]. However, in contrast to Acute, we believe it is important to add a basic notion of separation in the language semantics, so as to be able to provide a model of a distributed environment in terms of the language semantics. In Acute, distribution is only manifest through the sending and receiving of marshalled modules.

Supporting different communication capabilities in a uniform fashion can be obtained via the `export/bind` design pattern, which has been used to good effect in different operating system and middleware developments [5, 45, 47, 68]. This architectural pattern can be summarized as follows:

- Communication between different sites first requires that these sites share a common *naming context*, within which communicating entities at each site can be unambiguously designated (named). The `export` operation allow an entity in a participating site to receive an unambiguous name within the common context.
- Communication between different sites then requires the existence of *binding factories*. A binding factory supports the `bind` operation, which establishes a communication path (called *binding*) between a set of named entities.

A simple example of the `export/bind` pattern is provided by the establishment of a remote operation channel between a client and a server located on two different machines. To enable clients to connect to the server requires first that the server’s local name be exported with the naming context that is provided by the chosen remote operation protocol. The `export` operation returns a name that unambiguously designates the server in the context of the chosen remote operation protocol, and that can be communicated to potential clients. Once a client has obtained the exported name of the server, it can invoke the appropriate binding factory to establish a binding with the server via the `bind` operation. Once the binding is established, the client can invoke operations on the server. This behavior can be readily captured as follows. We model as above each site and the network by a distinct *kell*. A server component is modelled as a *kell* communicating on a dedicated half-gate (with the receive capability private to the server *kell*). Each request to the server takes the form of a message, i.e. a tuple with two fields: the first field contains the request arguments, the second field contains a subgate of the server gate, which can be used as a continuation to send back to the client the response to the request. Each site runs a `BindingFactory` component (typically as part of the `VirtualMachinery` behavior in the previous code snippet), which provides both an `Export` operation and a `Bind` operation. The `Export` operation takes as input the interface of a server (its dedicated half-gate) and returns a chunk that can be used as input to the `Bind` operation to create a communication channel between sites (e.g. similar in principle to a `Relay` process as defined in Section 4.2).

## 6.4 Gate semantics and implementation

Two important design choices have been made concerning gates and gate communication. First, gates are not located, i.e. a gate does not belong to a particular *kell*. This is possible because of the network independence assumption, which makes all gates local (to the top level *kell* in an OZ/K virtual machine). An advantage of this design choice is that gates can be used indifferently by all *kells*, subject to the opening and closing specifications

that govern communication on gates. A disadvantage is that the usual notion of component interface, which implies some sort of ownership of interfaces by components, must be encoded, as demonstrated with the encoding of half-gates. Second, communication via gates is by atomic rendezvous. Again, the network independence assumption means this is reasonable since all gates are assumed to be local to an OZ/K virtual machine. A main advantage of this design choice is that there is no state associated with gates. This in turn simplifies considerably the semantics of packing and unpacking. In particular, both operations can take place at any point during an OZ/K computation. If a different communication semantics had been chosen, e.g. communication by means of bounded or unbounded buffers, then packing and unpacking would have had to deal with the state of communication buffers, as is the case e.g. in the implementation of the Kell calculus abstract machine reported in [21]. Another advantage is that it is possible to encapsulate different communication semantics in connectors, i.e. kells that act as communication channels between other kells, as illustrated in Section 4. In particular, it is possible to define connectors with explicit flow control, in contrast e.g. to ports or mailboxes in OZ, E, Erlang, or Sing#.

A disadvantage of this choice of communication semantics is that care should be exercised to obtain an efficient implementation. However one can note that communication by atomic rendezvous can be implemented efficiently, as shown e.g. by its use as the interprocess communication primitive in the Minix 3 operating system [59]. Also, it is possible to devise efficient specific implementations for rendezvous with certain forms of kells which can be assumed to always have a thread waiting for communication, e.g. when dealing with kells providing an asynchronous communication service based on buffers, or when dealing with kells that support server interfaces as in the FRACTAL model.

Overall, the current design choices represent a reasonable compromise, for they do allow different communication semantics to be defined as different forms of “connector” kells, while allowing a clear semantics for packing and unpacking. However, there are several questions pertaining to the definition of communication primitives for kells that remain open. For instance, programming control loops for self-manageable systems, as advocated by [110], could be facilitated by introducing *regulative superposition* [64], as supported in the IP formalism [49]. This in turn could require adopting some form of multicast guarded communication, with kell containment understood as superposition composition.

## 6.5 Dynamic reconfiguration in OZ/K

OZ/K provides basic support for dynamic reconfiguration through kells and packing. However, there are at least two issues that still need to be addressed with respect to dynamic reconfiguration: the granularity of possible reconfigurations, and automated support for state transfer.

The unit of dynamic reconfiguration in OZ/K, the kell, is coarse grained, for it corresponds roughly to that of an active object. On the one hand, this level of granularity provides a good isolation between components: components can fail independently, and failure handling can take place in separate monitoring components, as illustrated in Section 4. On the other hand, finer-grained component-based designs, illustrated e.g. in [111], do not get built-in support for on-line update and replacement. Supporting dynamic reconfiguration at this level, would require the ability to update executing code at the level of procedure frames, possibly exploiting ideas for dynamic software update as proposed e.g. in [60], and for updateability analysis, as presented in [106].

The second issue with dynamic reconfiguration has to do with the automation of state transfer to ensure safe component updates. The semantic issues associated with dynamic reconfiguration have been explored in some works in the past two decades [23, 55]. A key enabler is the capture of appropriate state information on the component to replace. In OZ/K, this information is made available in the form of a packed value. Providing support for automated state transfer would require having the ability to introspect the contents of a packed value, at a sufficient level of granularity. A fine-grained access to the contents of a packed value can easily be provided by a standardized record representation for tasks and stores. However, support for extracting higher-level state information from packed values record representation would still be required, typically exploiting meta-programming ideas and in particular template meta-programming, e.g. as described for Haskell in [104].

## 6.6 Failure and event handling

The failure handling facilities provided in Oz/K still remain fairly crude, and depend on predefined status information for threads and kells. Additional support is required in the Oz/K computation model for programming different forms of failure detectors, dealing e.g. with omission failures. Two alternatives seem to be worth exploring. The first one would imply introducing an explicit notion of time in Oz/K, relying on the large body of work dealing with timed transition systems, and the timed- $\pi$ -calculus in particular (for instance, [17]). The second one would be to introduce reactive programming constructs as in the ULM programming model [25], which combines synchronous and functional programming. The second alternative is particularly appealing for it provides an elegant way to deal with multiple forms of timers and interrupts, and formalizes event-driven execution with no need to introduce an explicit notion of time. A crucial element in reactive programming is the ability to react to the *absence* of events. It would be interesting to see how this relates to the ability to test for the determinacy of logical variables, and whether streams in Oz and Oz/K could be used to model flows of reactive events.

## 6.7 On component-based programming in Oz/K

The kell concept provides a basis for component-based programming, with strictly enforced component encapsulation and isolation. As an illustration of this fact, we define in this section different constructs to support programming following the FRACTAL component model. The FRACTAL model is a language-independent reflective component model, which targets the construction of highly configurable systems. It has been used for the construction of several configurable systems, including operating system kernels [47], message-oriented middleware [68], for the instrumentation and automatic deployment of application server clusters [24], for building auto-adaptive systems [44], for building systems with integrated quality of service (QoS) management [109], or building adaptive multimedia applications [66]. The main elements of the model can be summarized as follows:

- Components are run-time entities, that are encapsulated, and that exhibit interfaces (access points) for communication with their environment. Interfaces can be of two kinds: server interfaces, which can receive operation invocations (either simple requests with no response, or requests with responses); client interfaces, which can emit operation invocations.
- Components can provide different meta-level interfaces for accessing their internal structure, and for controlling their behavior. In the general case, the internal structure of a component can be understood as comprising *membrane* and *contents*. The contents of a component consists in a set of other components. The membrane of a component embodies the specific behavior of the component, including meta-level behavior, e.g. for supporting introspection and intercession.

The FRACTAL model does not prescribe a given set of meta-level interfaces for components, however several useful such interfaces have been identified [28], including:

- *Component*. This interface provides access to the different interfaces of a component.
- *Content Controller*. This interface provides access to the contents of a component, and the ability to add or remove subcomponents.
- *Binding Controller*. This interface provides the ability to bind client interfaces of the component with server interfaces of other components in its environment.
- *Attribute Controller*. This interface provides the ability to access the attributes of a component, a set of named pieces of information associated with a component.
- *Lifecycle Controller*. This interface provides the ability to access and modify some macro-states of a component, such as *active*, *waiting*, *stopped*, etc.

We present in this section an interpretation in OZ/K of the FRACTAL specifications. This interpretation is analogous to the interpretation of objects in the OZ kernel computation model. Briefly, we interpret a FRACTAL component as an OZ/K kell, whose interfaces are mapped onto gates. The sub-components of a FRACTAL component are modelled as sub-kells, whereas the membrane of a component is modelled as a record (of shared attributes) and processes, which can comprise operation handlers, and meta-level interface controllers.

**Component templates.** We first define *component templates*, i.e., by analogy with object-oriented programming, classes for components. This allows to enforce certain programming conventions when building components. A component template `Temp` is a record that contains:

- A set of server interface templates, accessible via the feature `svIfs`. A server interface template contains an interface name, and a record of operations. The features of the operation record are the names of the operation. The fields of the operation record are operations. An operation is a procedure with three arguments: a message `M`, which is always a record, whose label denotes the name of the operation; a parameter `State` the represents the current state of the component owning the interface; and a reference `S` to the interface itself.
- A set of client interface templates, accessible via the feature `slIfs`. A client interface template contains an interface name, and a set of operation names to be used as labels of messages
- A set of names, that correspond to the names of the attributes of the component, accessible via the feature `atts`. Each attribute is a stateful cell that can be accessed by the attribute name, which is either an atom or a name. The record of all attributes of a component constitute its state.
- A template for a component controller, accessible via the feature `comp`. The component controller is the most basic form of controller for a component. It provides access to the other interfaces of the component.
- A set of controller templates, accessible via the feature `ctrls`. A controller provides a meta-level interface to implement. A controller template takes the form of a procedure. A call to the procedure instantiates a new controller for the current component.
- A set of component templates, accessible via the feature `subTemps`, that constitutes the templates required to create the subcomponents of the enclosing composite.
- An initialization procedure, accessible via the feature `init`. This procedure is responsible for initializing the internal structure of the composite component, including creating subcomponents, binding them together, binding subcomponent interfaces to interfaces of the enclosing composite.

**Components.** Components are created from component templates, via the following procedure (note that we make use of standard list and record operations as provided in the MOZART environment):

```

proc{NewComponent Template Gate}
K Gate = {NewGate $} in
  kell{K}
    State IST ICT CT CCT Component Meta = c(ist:IST ict:ICT ct:CT cct:CCT) in
      {List.forAll [IST ICT CT CCT] proc{$ T}{{NewDictionaryObject T}end}
      {MakeRecord s Template.atts State}
      {Record.forAll State proc{$ A}{NewCell _ A}end}
      {Template.comp K Component State Meta Gate}
      {Record.forAll Template.ctrls proc{$ I}{I K State Meta}end}
      {Record.forAll Template.svIfs proc{$ I}{Interface.newS I Component State IST _}end}
      {Record.forAll Template.clIfs proc{$ I}{Interface.newC I Component ICT _}end}
      {Template.init K State c(IST ICT CT)}
    end
  end
end

```

Component creation proceeds as follows. First, dictionaries are created<sup>8</sup> that will hold meta data associated with the component: the table of (external) server interfaces of the component, called `IST`; the table of (external) client interfaces of the component, called `ICT`; the table of component controller interfaces, called `CCT`; the table of subcomponents, called `CT`. Then a record is created that will constitute the internal `State` of the component, in the form of a set of attributes. The component controller of the component is then created (and put in the component controller table `CCT` as a side effect). Other controllers are created, followed by server and client interfaces, and finally the initialization procedure is called (which typically can create subcomponents and configure them).

**Interfaces.** We define here the module `Interface` that allows to instantiate server and client interfaces from server and client interface templates, respectively. For server interfaces, it essentially creates a new gate which can be opened for communication with the owner component (the owner component is the current component executing the `NewServerIf` operation). For client interfaces, it essentially creates a cell that can be updated with a server interface, which corresponds to the establishment of a binding between the client interface and a server interface.

```

ServerIf TagS={NewName $} NewServerIf IsServerIf
ClientIf TagC={NewName $} NewClientIf IsClientIf
in
proc{IsServerIf I B} {HasFeature TagS I B} end
proc{NewServerIf Template Component State IfT Server}
  G = {NewGate $}
  Server = {NewChunk r(TagS:Request open:OpenS close:CloseS owner:Component) $}
  OpenS = proc{$ K} {Open K G#all} end
  CloseS = proc{$ K} {Close K G#all} end

  Request = proc{$ M}
    L = {Label M $} in
      if {HasFeature Template.ops L $}
        then case M of
          L(R unit) then {Send G M}
          [] L(R X) then Y RG in {NewGate G#RG}{Send G L(R RG)}{Receive RG Y} X = Y
          else raise invalidRequest(M) end
        end
      else raise invalidOperation(L) end
    end
  end

  Handle = proc{$}
  M L = {Label M $} in
    {Receive G M}
    case M of
      L(R unit) then
        try {Template.ops.L M State Server}
        catch _ then skip end
      [] L(R RG) then Y in
        try {Template.ops.L L(R Y) State Server}{Send RG Y}
        catch E then {Send RG error(E)} end
      else skip end
    {Handle}
  end

in
thread {Handle} end
{IfT put(Template.ifName Server)}
end

```

<sup>8</sup>Note that, to simplify the code, we postulate the existence of an operation `NewDictionaryObject` which returns not a standard MOZART dictionary but a dictionary *object*. Just like objects in MOZART, a dictionary object is a procedure which takes an operation request as argument. An operation request is a record whose label corresponds to the name of the operation.

```

\% Client interfaces

proc{IsClientIf I B} {HasFeature TagC I $} end

proc{NewClientIf Template Component IfT Client}
  Ch = {Newcell unit $}
  Client = {NewChunk r(TagC:Ch owner:Component bind:Bind unbind:Unbind invoke:Invoke) $}

  Bind = proc{$ S}
    if {IsServerIf S $} then Ch := S
      else raise notServerIf(S) end
    end
  end

  Unbind = proc{$} Ch := unit end

  Invoke = proc{$ M}
    L = {Label M $}
    S = @Ch in
      if {Member L Template.opLabels $}
        then case M of L(R X)
          then {S.TagS M}
            else raise invalidRequest(M) end
          end
        else raise invalidOperation(L) end
      end
    end
  end

in
  {IfT put(Template.ifName Client)}

end

Interface = ifMod(isServer:IsServerIf newS:NewServerIf isClientIf:IsClientIf newC:NewClientIf)

```

The procedure `NewServerIf` creates a new server interface (a chunk, with features `TagS`, `open`, `Close`, and `owner`), creates a thread dedicated to the handling (via procedure `Handle`) of operation requests on the newly created server interface, and registers the newly created server interface in the interface table `ifT` which is passed as a parameter to the procedure. An interface table is a dictionary, whose keys are the names of interfaces (represented as OZ/K atoms or names) it holds. The `Template` parameter of the procedure is a server interface template, the `Component` parameter corresponds to the owner of the newly created interface, and the `State` parameter corresponds to the record of attributes of the owner component. The `Handle` behavior is simple: it awaits a message on the gate `G` which is attached to the server interface. A message is essentially a record whose label is the name of the requested operation, and which contains two fields: the first one is a record of arguments of the operation, the second one is a continuation for the operation. If the continuation is `unit`, this indicates that the operation is a simple request with no response. Otherwise, the continuation is a gate on which the response to the operation request is returned. Note that `Handle` serves requests sequentially. If another behavior is required, such as e.g. handling requests concurrently with a pool of threads and a scheduler, then all that is required is to change the `Handle` procedure to implement the required behavior.

A server interface can be passed freely in communication to different components. The `OpenS` operation is present to allow communication on a server interface to cross component boundaries, i.e. for both operation requests and operation responses (via the statement `{Open K G#all}` in the body of procedure `OpenS`) to freely cross component boundaries. Notice that the gate which a server interface encapsulates cannot be used directly. This enforces communication on a server interface to obey the request/response protocol associated with its operation. Thus, it is not possible for a third-party to listen on the gate of a server interface and to intercept requests and responses coming sent on this gate. This construction is similar to the half-gate construct presented in section 4.

A client interface can only be known outside of its owner component via its interface name (this is because a client interface is chunk that holds a cell – a non-strict value)<sup>9</sup>. However it is possible to bind a client interface to a server interface from outside its owner component by using the binding controller (server) interface below, which will use to that effect the `Bind` operation provided by the client interface. The `Bind` operation merely updates the client interface internal cell with the server interface passed as argument. The `Invoke` operation on a client interface can be used to communicate via a client interface (note that only threads within its owner component can make use of a client interface). It takes a message as an argument, which is a record whose label corresponds to the name of an operation supported by the interface, and which contains two fields. The first one is the content of the message. The second one is the continuation of the message. If it is bound to `unit` upon invocation of the `Invoke` operation, this means the operation is a simple request with no expected response. Otherwise, it will be bound to the response to the operation request when the latter completes.

**Component controller.** We define here the component controller from the FRACTAL specification<sup>10</sup>. It allows to discover the different interfaces of a component. Note that, from an interface, it is possible to recover the `Component` controller interface, by accessing the interface's `owner` feature. The component controller provides operations with which it is possible to retrieve all the server interfaces (`GetSifs`), all the client interfaces (`GetCifs`), and all the controller interfaces (`GetCtrls`) of a component. In addition, operation (`GetHand`) retrieves the name of the cell that constitutes the component. The gate `Gate` provides a means to retrieve the component interface itself<sup>11</sup>.

```

proc{ComponentCtrl K Component State Meta Gate}
  Temp = temp(ifname:component
              ops:m(getSifs:GetSifs getCifs:GetCifs getCtrls:GetCtrls getHand:GetHand))
  GetSifs = proc{$ M}
    case M of getSifs(_ R) then {Meta.ist toRecord(ist R)} else skip end
  end

  GetCifs = proc{$ M}
    case M of getCifs(_ R) then {Meta.ict toRecord(ict R)} else skip end
  end

  GetCtrls = proc{$ M}
    case M of getCtrls(_ R) then {Meta.cct toRecord(cct R)} else skip end
  end

  GetHand = proc{$ M}
    case M of getHand(_ R) then R = K else skip end
  end

in
  {Interface.newS Temp Component State Meta.cct Component}
  thread P = proc{$}{Send Gate Component}{P} end in {P} end
end

```

Note that the creation of the component controller uses the `Interface` module and its operation for creating server interfaces, which puts in place, as a side effect, a `Handle` thread for dealing with operation requests. The component controller behavior is thus determined by the `Handle` procedure defined in the `Interface` module, and the operations associated with the `Component` server interface. This scheme is used for the other controllers defined below.

<sup>9</sup>Ensuring that an interface name is unambiguous within the context of a component is the responsibility of the procedures that update the component interface tables, i.e. procedures `NewS` and `NewC` in module `Interface`. However, for the sake of simplicity, we have not added these checks in the code of these two procedures.

<sup>10</sup>For the sake of simplicity, there are some slight differences between the controller operations we define in this section, and those in the FRACTAL specification. However the essential functionality of the FRACTAL default controllers is preserved.

<sup>11</sup>This gate can be seen also as a component identifier. To turn it into a true component identifier would require to wrap it in a half-gate, as illustrated in Section 4, so that it is only possible to receive on this gate. For the sake of simplicity, this is not shown here.

**Attribute controller.** We define here the attribute controller from the FRACTAL specification. This controller allows to access the attributes of a component, through getter and setter operations.

```

proc{AttributeCtrl K State Meta}
  Temp = temp(iframe:attribute ops:m(get:Get set:Set))
  Get = proc{$ M}
    case M of get(A R) then R = @(State.A)
    else skip
    end
  end

  Set = proc{$ M}
    case M of set((A V) unit) then (State.A) := V
    else skip
    end
  end

in
  {Interface.newS Temp Component State Meta.cct _}
end

```

**Binding controller.** We define here the binding controller from the FRACTAL specification. This controller allows to bind and unbind client interfaces of a component to server interfaces of another component.

```

proc{BindingCtrl K State Meta}
  Temp = temp(iframe:binding ops:m(bind:Bind unbind:Unbind))
  Bind = proc{$ M}
    case M of bind((L S) unit)
    then C = {Meta.ict get(L $)} in {C.bind S}
    else skip
    end
  end

  Unbind = proc{$ M}
    case M of unbind(L unit)
    then C = {Meta.ict get(L $)} in {C.unbind}
    else skip
    end
  end

in
  {Interface.newS Temp Component State Meta.cct _}
end

```

The Bind operation takes as arguments the name of the client interface to bind (as registered in the component's client interface table), and a server interface. The name of a client interface can be an atom (as is the case here with controller interfaces), or an OZ/K name.

**Content controller.** We define here the content controller from the FRACTAL specification<sup>12</sup>. This controller allows to add and remove subcomponents, to and from a component.

```

proc{ContentCtrl K State Meta}
  Temp = temp(iframe:content ops:m(add:Add remove:Remove))
  Add = proc{$ M}
    case M of add((V G) unit)
    then KK W Comp in
      kell{KK} {V.mark top(KK) W}{V.Mark gate(G:G)}{Unpack W _} end

```

<sup>12</sup>In the FRACTAL specification, the content controller supports an operation that returns the internal interfaces of a component, i.e. interfaces that are provided by interceptors for subcomponent interfaces. We have not defined this operation to keep things simple.

```

        {Receive G Comp}
        {Meta.ct put(G Comp)}
    else skip
    end
end

Remove = proc{$ M}
    case M of remove((Comp G) R)
    then K = {Comp getHand(_ $)} in
        if {Meta.ct member(G Comp) $}
        then {Meta.ct remove(G Comp)} {Pack K R}
        else R = notSubComponent(Comp G)
        end
    else skip
    end
end

in
    {Interface.newS Temp Component State Meta.ctt _}
end

```

The Add operation takes as argument a packed value  $V$  that contains the component to add, and a gate  $G$ , which corresponds to the gate on which to retrieve the component controller interface of the added component. The Add operation just unpacks the packed component in a new kell. The gate which  $G$  which identifies the component is preserved, and the component controller interface of the new sub-component is added to the sub-component table of the current component. The Remove operation takes as argument a component controller interface and a gate  $G$  that identify the sub-component of the current component to remove. The operation returns a packed value which contains the remove sub-component, after having removed the appropriate entry from the sub-component table of the current component.

**Lifecycle controller.** We define here the life cycle controller from the FRACTAL specification. This controller allows to start and stop the execution of a component (apart from its controllers).

```

proc{LifecycleCtrl K State Meta}
    Temp = temp(ifname:lifecycle ops:m(start:Start stop:Stop getState:GetState))
    Status = {NewCell stopped $}

    Stop = proc{$ M}
        T = Meta.ct in
            case M of stop(_ unit)
            then O N in {Exchange Status O N}
                if O == started
                then
                    R = {T toRecord($)}
                    L = {Record.arity R $}
                    P = proc{$ G}
                        V = {T get(G $)} K = {V getHand(_ $)}
                        W = {Pack K $} Z = {W.mark gate(G:G) $} in
                            {T remove(G)} {T put(G Z)}
                        end
                    in
                        {List.forAll L P} N = stopped
                    else skip
                    end
                else skip
                end
            end
        end

    Start = proc{$ M}
        T = Meta.ct in

```

```

case M of start(_ unit)
then O N in {Exchange Status O N}
  if O == stopped
  then
    R = {T toRecord($)}
    L = {Record.arity R $}
    P = proc{$ G}
      V = {T get(G $)} K W Comp in
      kcell{K} {V.mark top(K)}{Unpack V _} end
      {Receive G Comp}
      {T remove(G)} {T put(G Comp)}
    end
  in
    {List.forAll L P} N = started
  else skip
  end
else skip
end
end
end

GetState = proc{$ M}
  case M of getState(_ R) then R = @Status
  else skip
  end
end

in
  {Interface.newS Temp Component State Meta.cct _}
end

```

## 7 Related work

OZ/K is related to several bodies of work, which we can classify in, roughly, the following categories: component-based programming models, architecture description languages, programming languages, process calculi.

**Programming languages.** The reference language for open programming is the Java language [12, 54], with its comprehensive programming environment. A number of open programming facilities are provided by Java and its associated environment (e.g. through class loading, remoting and security mechanisms), but they still exhibit important limitations, e.g. with respect to modularization and componentization (no native notion of component, except through the notions of Java Beans or EJBs, but without hierarchical components and control over component interconnections; limited form of modules through OSGI bundles), explicit marshalling and pickling (no generic pickling mechanism, serialization provides only limited marshalling – e.g. code cannot be serialized), dynamic linking, and isolation (dynamic linking and sandboxing available through class loaders and security managers, with complex APIs and no formal semantics). Overall, support for open programming in the Java environment appears complex, with crucial aspects dealt with in environment libraries and associated APIs, and with no formal semantics.

A few programming languages are built around a notion of locality, notably JoCaml [48], Nomadic Pict [114], O’Klaim [18], ULM [25]. None of these languages provide the ability to build sandboxes with strong isolation properties as OZ/K provides. Except for JoCaml (which supports hierarchical localities and strong mobility), localities in these languages essentially represent execution sites.

A number of works have considered recently open programming issues, dealing in particular with software configuration, modules and dynamic linking, such as e.g. [8, 30]. These works focus on basic formalisms and calculi dealing with specific issues. There have been comparatively less work on programming language designs

taking open programming features into account. Recent ones include AchJava [3, 4], Assemblage [72], ComponentJ [97, 98], E [80], Jiazzi [78], Piccola [2, 75], Scala [83], Classages [71], Sing# [46], Oz [111], Alice [91, 90], Acute [99, 100, 101] and O-Klaim [19].

ArchJava, ComponentJ, Jiazzi, Assemblage and Classages focus on the notions of components and component composition. ArchJava, Assemblage, and Classages come closest to the notion of component as embodied in the *kell* notion in Oz/K. However, even though ArchJava and Classages components are units of encapsulation, and provide what ArchJava calls *communication integrity*, components in ArchJava and Classages are not units of fault isolation (multiple threads may traverse a given component at any point in time). Also, component configurations in ArchJava and Classages can evolve at run time, through the creation of new components and new connectors, but these evolutions are limited by what the behavior programmed in component classes and classages. ArchJava and Classages do not provide for the kind of unplanned reconfigurations and component mobility that can take place in Oz/K through the use of the `Pack` and `Unpack` primitives, and they do not support passivation, failure detection, and isolation, as Oz/K does. Assemblage has recently been extended to include explicit deployment [70], but still the language does not provide support for passivation and isolation.

Piccola is a scripting language developed on top of a formal kernel, the asynchronous  $\pi$ -calculus with extensible records (called *forms*). Piccola is intended as a composition language, which derives its expressive power from the combination of the  $\pi$ -calculus lexical scoping and name passing, together with extensible records, which allow e.g. the encoding of generic wrappers and higher-order composition schemas. Piccola's notion of component is that of a  $\pi$ -calculus process, and remains limited with respect to the handling of distribution, isolation, explicit marshalling and passivation, compared to Oz/K.

Scala combines object-oriented and functional programming in a statically typed programming language, which supports class mixins and views, with a very expressive type system. The notion of component and component composition in Scala is closer to the notion of module than to the run-time unit of isolation and reconfiguration that Oz/K provides with its notion of *kell*. In addition, Scala does not provide support for passivation, explicit marshalling and pickling, as available in Oz/K.

Alice can be understood as an extension of Standard ML [81] that offers higher-order modules, packages (essentially, an extension of the notion of dynamics [1], which combines a higher-module with its dynamic signature), pickles (marshalled forms of packages), components, and concurrency with futures and laziness. The Alice notion of component (or dynamic module) can be understood, following [90] as a function, taking packages as arguments (imports), and that evaluates to a package (containing the export module). The Alice notions of packages, pickles and components, formalize, in a strongly typed setting, similar notions of notions of functors and pickles that appear in Oz. Still, compared to Oz/K, Alice does not provide support for passivation, and the notion of sandbox in Alice, available through a notion of *component manager* that is part of the Alice library environment, is not accessible to programmers.

Acute is also a language in the ML family, with extensive support for open programming in a strongly typed setting, including explicit marshalling, dynamic linking, dynamic modules, support for versioning constraints, support for concurrency through threads, and even a form of passivation through the ability to thunkify running threads. Acute also introduces the notion of *mark* to control the extent of dynamic linking in module. The notion of *mark* in Acute is related to the `mark` operation on packed values in Oz/K, that can be exploited to obtain similar effects (e.g. shipping only the relevant portion of a module code, as illustrated in Section 4). Compared to Oz/K, Acute does not support sandboxing and isolation, and it supports open programming through a relative complex set of mechanisms that are subsumed in Oz/K by a smaller set of constructs (namely via *kells*, *gates*, and *packing*).

O-Klaim provides a Java-based, object-oriented programming language built on a formal kernel, the Klaim process calculus [82], that provides generative communication à la Linda [51, 52]. O-Klaim supports classes and a form of mixins with first-class status, that provide support for mobile code in a strongly-typed setting. In contrast to Oz/K, the notion of dynamic module provided by O-Klaim (also in comparison with Alice and Acute), through its *mobile mixins* appears limited (for instance, higher-order modules are not supported). In addition, O-Klaim does not provide support for sandboxing, isolation, and passivation.

The E programming language [80], whose aim is to be a secure language, combines object-oriented programming, capability-based access control, and concurrency control. Concurrency control in E is based on the notion of *vats*, which corresponds roughly to a thread communicating by its environment (other vats) via asynchronous remote method invocation with futures. E provides extensive support for capabilities, but fails to provide the sandboxing and passivation functionality that OZ/K supports.

The Sing# language [46], developed as part of the Microsoft Singularity operating system, that extends the C# programming language with isolated processes and asynchronous message passing communication. Processes in Sing# are isolated by virtue of being units of fault isolation, and of their code being unmodifiable, at run-time. However, Sing# does not provide the sandboxing and capabilities of OZ/K, and relies on standard C# notions and associated .Net capabilities for handling modules and code deployment.

OZ/K obviously builds upon OZ. The benefits brought by OZ/K have already been identified in the introduction. Our work on OZ/K is also related to recent proposals for extending OZ. A first attempt at exploiting a locality concept inspired by the Kell calculus was made in [63]. In this paper, localities (named “membranes”) are finer grained than kells in OZ/K, but they are used only for communication control (confinement), and do not constitute units of failure isolation, or of passivation. The kell construct in OZ/K seems in line with the proposed design guidelines for a *secure* OZ, presented in [105].

Like OZ, OZ/K is an essentially untyped language. This is in contrast to most of the languages cited above (Acute, Alice, ArchJava, Classages, ComponentJ, O-Klaim, Scala), which are statically typed languages (with forms of *dynamics* for some, such as Acute and Alice). Static type checking has well-known advantages, and languages such as Acute and Alice provide stronger safety guarantees during execution than OZ/K does. However an untyped setting provides more flexibility when trying to combine different language features, as we are doing in OZ/K. It also allows for a simpler formal operational semantics (compare e.g. the operational semantics of OZ/K, and that of Acute). Devising a strongly-typed variant of OZ/K is an item for further study.

**Component-based programming models.** Several component models have appeared during the last decade, in industry standards and specifications, such as Sun’s Java Beans, and Enterprise Java Beans (EJBs) [107], Microsoft COM and .Net [73], the OMG CORBA Component Model (CCM) [84], the OSGI Bundle model [86], IBM and BEA’s Service Component Architecture (SCA) [62], the Grid Forum Common Component Architecture (CCA) [11]. An overview and discussion of several of these models can be found in [108]. Some of these models (e.g. CCA, SCA) merely cater for interface specifications and managing inter component connection. Other, such as Java EJBs and .Net provide comprehensive programming environments. However, even the most complete ones fail to support all the open programming capabilities presented in this paper (i.e. support for components, dynamic modules, dynamic linking and binding, isolation, fault handling, and passivation) in an integrated fashion. Their programming support typically takes the form of complex and loosely integrated APIs, with no formal semantics, and the use of several different languages and formalisms to deal with open programming issues such as e.g. distributed deployment and configuration, and limited native support for dynamic reconfiguration. More experimental component models such as OpenCOM [38, 41] and Fractal [27] provide a stronger support for dynamic reconfiguration, but, as with other programming language independent models, such as CCM, their implementations are typically limited by the host programming environment. This is apparent e.g. with Java implementations, which suffer from the limitations of the language and its associated environment.

**Architecture Description Languages.** During the past fifteen years, several architecture description languages have been developed, that embody component models and linguistic support for component-based specification and programming (see e.g. [79] for a survey). Of particular interest are ADLs that provide the ability to specify or program dynamically reconfigurable architectures (see e.g. [26] for a survey). These include in particular Rapide [74], Darwin [76, 77], CommUnity [112, 113], Olan [13], Dynamic Wright [7, 6],  $\pi$ -ADL [85]. Of these, CommUnity and  $\pi$ -ADL provide the more expressive power, especially with respect to dynamic reconfigurations. CommUnity is based on the Unity [34] and IP [49] specification languages, and describes a component configuration as a graph with nodes labelled by programs and arcs labelled by morphisms. In CommUnity, a reconfiguration

is thus specified by conditional graph rewriting rules. While CommUnity can describe complex reconfigurations, it does not appear possible to specify a situation where a component is to be replaced by an unknown one, received on a communication channel. In  $\pi$ -ADL, components and component specifications are specified as higher-order processes, with process specifications combining process descriptions as in the higher-order  $\pi$ -calculus [94], and behavioral properties expressed in a variant of the  $\mu$ -calculus [65]. and reconfigurations correspond to higher-order actions effected by processes. Thanks to its higher-order communication, only  $\pi$ -ADL provides the ability to specify dynamic reconfigurations that depend on components received from the environment (e.g. as arguments of messages). However,  $\pi$ -ADL does not provide the equivalent of the packing capability of OZ/K, with its ability to suspend and marshall a running component. Overall, the architecture description languages that provide the more extensive support for dynamic reconfiguration, allow high-level behavioral specifications, but do not explicitly support un-planned dynamic reconfigurations that can be expressed in OZ/K, through a combination of higher-order communication and passivation.

**Process calculi.** The notion of kell in OZ/K is directly inspired by the Kell calculus [96], and the kell calculus with sharing developed in [61]. The OZ/K notion of packing and unpacking is close to the passivation operator of the M-calculus [95]. For a more in-depth discussion of the relations between kells and localities in other distributed process calculi, such as Mobile Ambients [31] and their many variants, the Distributed  $\pi$ -calculus [89] and its higher-order variant SafeDpi [58], the Seal calculus [32], and Klaim [18], the reader can refer to [96]. The approach advocated in OZ/K, inherited from the Kell calculus and the M-calculus, is the only one to combine a higher-order approach as can be found in SafeDpi, and the possibility to passivate a locality. The Seal calculus can approximate to some extent the effect of passivation, but at the expense of complex encodings in order to simulate simple Kell calculus moves.

Two recent process calculi that offer the possibility of dynamic reconfiguration are the  $\gamma$ -calculus, that embodies a higher-order chemical computation model [14, 15], and Homer [53], that provides for locality passivation as in the Kell calculus. As [16] illustrates, it is possible to program some forms of dynamic reconfigurations in the  $\gamma$ -calculus. However the calculus does not allow for passivating executing chemical solutions (only inert solutions can be matched by pattern matching), which would be the equivalent of passivating a kell. As a result, it is unclear how to support un-planned reconfigurations in the  $\gamma$ -calculus, where part of the system can be modified even though it was not programmed to account for such a reconfiguration. Homer is very close to the Kell calculus, but it allows the passivation of localities from an arbitrary ancestor in the locality tree, provided the path from ancestor to descendant is known. In contrast, OZ/K, as in the Kell calculus, only allows an immediate parent locality to passivate a given locality. This preserves the local aspect of all OZ/K reductions. In addition, it is possible if necessary to encode Homer-type passivation through some form of content controllers à la FRACTAL (see Section 6.7 for an encoding of simple content controllers).

Finally, it is worth noting that, although very close to the kell constructs in OZ/K are close to those of the M-calculus and of the Kell calculus, there are some important differences. The packing operation is very similar to the passivation operation of the M-calculus, but the M-calculus relied on located channels, whereas gates in OZ/K are not. The `Open` and `Close` operations in OZ/K have no equivalent in the M-calculus and the Kell calculus. In these calculi, it is possible to model the opening of a given gate by the introduction of a relaying process, but it is not possible to model a statement of the form `{Open K#all}`. In fact, *transparent kells* introduced in Section 6, correspond to localities of the Kell calculus with sharing [61].

## 8 Conclusion

We have presented OZ/K, a kernel language for open distributed programming. The main contribution of OZ/K is the introduction of a notion of locality as a unit of modularity, isolation, and reconfiguration in the multi-paradigm OZ computation model. Localities in OZ/K can be used to model distributed sites, to construct sandboxes, or to program dynamic modules. We have presented a formal operational semantics for OZ/K, and given a number of programming examples illustrating how open distributed programming can be supported in OZ/K.

The language presented in this paper constitutes a first attempt at introducing localities in *OZ/K*. As mentioned above, several aspects of the language warrant further study: the granularity of localities, the semantics of gate communication, the handling of failures, improved support of component sharing, improved support for dynamic reconfiguration and state capture. In addition, several questions are worth investigating. First, it would be interesting to define a compositional semantics for (possibly a subset of) the *OZ/K* kernel language. This would allow the development of a behavioral theory for *OZ/K*, and would ease the definition of type systems for *OZ/K*. Defining type systems for *OZ/K* would also be of interest, especially targeting configuration errors as studied e.g. in [20], and dealing with evolving configurations and mobility scenarios as studied e.g. in [58, 115]. A third question would be the development of appropriate support for transactional behavior or recoverable actions, exploiting for instance recent studies on the subject such as [29, 42, 43].

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$$\begin{array}{ll}
v(u) \triangleq \emptyset & v(!\epsilon) \triangleq v(\epsilon) \\
v(\langle X \rangle) \triangleq \emptyset & v(\epsilon(\epsilon_1 : X_1 \dots \epsilon_n : X_n)) \triangleq v(\epsilon, \epsilon_1, \dots, \epsilon_n) \\
v(\mathbf{skip}) = \emptyset & v(S_1 \ S_2) = v(S_1, S_2) \\
v(\mathbf{thread}\{\epsilon\} \ S \ \mathbf{end}) = v(\epsilon, S) & v(\mathbf{local} \ X_1 \dots X_n \ \mathbf{in} \ S \ \mathbf{end}) = v(S) \\
v(\epsilon = \epsilon') = v(\epsilon, \epsilon') & v(\epsilon = !!\epsilon') = v(\epsilon, \epsilon') \\
v(\epsilon = u) = v(\epsilon) & v(\epsilon = l(f_1 : \epsilon_1 \dots f_n : \epsilon_n)) = v(\epsilon, \epsilon_1, \dots, \epsilon_n) \\
v(\mathbf{if} \ \epsilon \ \mathbf{then} \ S_1 \ \mathbf{else} \ S_2 \ \mathbf{end}) = v(\epsilon, S_1, S_2) & v(\mathbf{case} \ \epsilon \ \mathbf{of} \ J \ \mathbf{then} \ S_1 \ \mathbf{else} \ S_2) = v(\epsilon, J, S_1, S_2) \\
v(\mathbf{proc}\{\epsilon \ X_1 \dots X_n\} \ S \ \mathbf{end}) = v(\epsilon, S) & v(\{\epsilon \ \epsilon_1 \dots \epsilon_n\}) = v(\epsilon, \epsilon_1, \dots, \epsilon_n) \\
v(\mathbf{try} \ S_1 \ \mathbf{catch} \ X \ \mathbf{then} \ S_2 \ \mathbf{end}) = v(S_1, S_2) & v(\mathbf{raise} \ \epsilon \ \mathbf{end}) = v(\epsilon) \\
v(\mathbf{kell}\{\epsilon\} \ S \ \mathbf{end}) = v(\epsilon, S) & v(\{P \ \epsilon_1 \dots \epsilon_n\}) = v(\epsilon_1, \dots, \epsilon_n)
\end{array}$$

Figure 2: Variables of an extended statement

## A Auxiliary relations and predicates

The definition of the reduction relation relies on a number of functions, predicates and relations which we define in this section.

**Primitive operations.** We call *primitive operations* the operations `Unify`, `NewName`, `IsDet`, `NewCell`, `Exchange`, `WaitNeeded`, `FailedValue`, `NewGate`, `Send`, `Receive`, `Open`, `Close`, `Pack`, `Unpack`, `Mark`, `Status`, that appear in Tables 1 and 3.

**Variables.** Notions of free and bound variable identifiers are classical. Variable identifier binders are the following statements, which bind variable identifiers  $x_1, \dots, x_n$ , with scope the statement  $S_1$ :

```

local  $X_1 \dots X_n$  in  $S_1$  end
proc{ $P \ X_1 \dots X_n$ }  $S_1$  end
case  $X$  of  $V(V_1 : X_1 \dots V_n : X_n)$  then  $S_1$  else  $S_2$  end
try  $S$  catch  $X_1$  then  $S_1$  end

```

The set of variables of an extended statement  $S$ , noted  $v(S)$ , is defined inductively in Figure 2, where  $P$  denotes a primitive operation, where  $u$  denotes a base value (integer, atom or name), and where  $\epsilon, \delta$ , and their decorated variants, denote both variable identifiers and variables. By definition, we set  $v(\epsilon) = \{x\}$  if  $\epsilon = x$  (i.e.  $\epsilon$  is a variable), and  $v(\epsilon) = \emptyset$  if  $\epsilon = X$  (i.e.  $\epsilon$  is a variable identifier). Also, if  $T_1, \dots, T_n$  are terms, we set:

$$v(T_1, \dots, T_n) = v(T_1) \cup \dots \cup v(T_n)$$

The set of variables of task  $\mathcal{T}$ , relative to store  $\sigma$ , noted  $v(\mathcal{T}, \sigma)$ , is defined as the smallest set satisfying the inference rules in Figure 3.

The set of variables of a store  $\sigma$ , noted  $v(\sigma)$  is defined inductively in Figure 4.

We define the substitution of variable identifiers by variables in a statement. We write

$$\theta = \{X_1 \rightarrow x_1, \dots, X_n \rightarrow x_n\}$$

for the substitution that substitutes variables  $x_i$  to identifiers  $X_i$ , and  $S\theta$  for the application of substitution  $\theta$  to the extended statement  $S$ . We define  $\theta_{\{X_1, \dots, X_n\}}$  to be the substitution that coincides with  $\theta$  on  $\text{dom}(\theta) \setminus \{X_1, \dots, X_n\}$ , i.e.

$$\theta_{\{X_1, \dots, X_n\}} \triangleq \{X \rightarrow x \in \theta \mid X \notin \{X_1, \dots, X_n\}\}$$

$$\begin{array}{c}
\frac{x \in v(S) \vee x \in v(\tau : T, \sigma)}{x \in v(\tau(S \ T), \sigma)} \qquad \frac{x \in v(T, \sigma) \vee x \in v(\mathcal{U}, \sigma)}{x \in v(T \ \mathcal{U}, \sigma)} \qquad \frac{x \in v(T, \sigma) \ \sigma \models x = y}{y \in v(T, \sigma)} \\
\\
\frac{x \in v(T, \sigma) \ \sigma \models x = \xi \wedge \xi : \mathbf{cell}(y)}{y \in v(T, \sigma)} \qquad \frac{x \in v(T, \sigma) \ \sigma \models x = \xi \wedge \xi : \mathbf{thread}(y)}{y \in v(T, \sigma)} \\
\\
\frac{x \in v(T, \sigma) \ \sigma \models x = \xi \wedge \xi : \mathbf{kell}(\pi, y)}{y \in v(T, \sigma)} \qquad \frac{x \in v(T, \sigma) \ \sigma \models \mathbf{read}(x, y)}{y \in v(T, \sigma)} \qquad \frac{x \in v(T, \sigma) \ \sigma \models x = \mathbf{failed}(y)}{y \in v(T, \sigma)} \\
\\
\frac{x \in v(T, \sigma) \ \sigma \models x = f(l_1 : x_1, \dots, l_n : x_n)_m}{x_i \in v(T, \sigma)} \qquad \frac{x \in v(T, \sigma) \ \sigma \models x = \xi \wedge \xi : \mathbf{proc}\{\$ X_1 \dots X_n\} S \ \mathbf{end} \ y \in v(S)}{y \in v(T, \sigma)}
\end{array}$$

Figure 3: Variables of a task relative to a store

$$\begin{array}{ll}
v(x) = \{x\} & v(x = l(f_1 : x_1 \dots f_n : x_n)_m) = \{x, x_1, \dots, x_n\} \\
v(x = u) = \{x\} & v(x = y) = \{x, y\} \\
v(x = \mathbf{pack}(\xi, T, \sigma', \mu)) = v(T, \sigma') \cup v(\sigma') & v(x = \mathbf{failed}(y)) = \{x, y\} \\
v(\xi : \mathbf{proc}\{\$ X_1 \dots X_n\} S \ \mathbf{end}) = v(S, \sigma) & v(\xi : \mathbf{thread}(x, y)) = \{x, y\} \\
v(\xi : \mathbf{kell}(\pi, x)) = \{x\} & v(\xi : \mathbf{cell}(x)) = \{x\} \\
v(\xi : \mathbf{gate}) = \emptyset & v(\mathbf{need}(x)) = \{x\} \\
v(\mathbf{read}(x)) = \{x\} & v(\mathbf{read}(x, y)) = \{x, y\} \\
v(\mathbf{in}(\kappa, \kappa')) = \emptyset & v(\mathbf{subg}(\gamma, \gamma')) = \emptyset \\
v(\mathbf{inth}(\kappa, \tau)) = \emptyset & v(\sigma \wedge \sigma') = v(\sigma) \cup v(\sigma')
\end{array}$$

Figure 4: Variables of a store

$$\begin{array}{lll}
\epsilon\theta \triangleq \theta(\epsilon) \quad \text{if } \epsilon \in \text{dom}(\theta) & \epsilon\theta \triangleq \epsilon \quad \text{if } \epsilon \notin \text{dom}(\theta) & v\theta \triangleq v \\
(!\epsilon)\theta \triangleq !( \epsilon\theta ) & (\epsilon(\epsilon_1 : X_1 \dots \epsilon_n : X_n))\theta \triangleq \epsilon\theta(\epsilon_1\theta : X_1 \dots \epsilon_n\theta : X_n) & \\
\\
\mathbf{skip} \ \theta \triangleq \mathbf{skip} & (S_1 \ S_2)\theta \triangleq S_1\theta \ S_2\theta & \\
(\mathbf{thread}\{\epsilon\} \ S \ \mathbf{end})\theta \triangleq \mathbf{thread}\{\epsilon\theta\} \ S\theta \ \mathbf{end} & (\epsilon_1 = \epsilon_2)\theta \triangleq \epsilon_1\theta = \epsilon_2\theta & \\
(\epsilon = v)\theta \triangleq \epsilon\theta = v & (\epsilon_1 = \epsilon_2.\epsilon_3)\theta \triangleq \epsilon_1\theta = \epsilon_2\theta.\epsilon_3.\theta & \\
\{\mathbf{P} \ \epsilon_1 \dots \epsilon_n\}\theta \triangleq \{\mathbf{P} \ \epsilon_1\theta \dots \epsilon_n\theta\} & \{\epsilon \ \epsilon_1 \dots \epsilon_n\}\theta \triangleq \{\epsilon\theta \ \epsilon_1\theta \dots \epsilon_n\theta\} & \\
(\mathbf{kell}\{\epsilon_1\} \ S \ \mathbf{end})\theta \triangleq \mathbf{kell}\{\epsilon_1\theta\} \ S\theta \ \mathbf{end} & (\epsilon_1 = !!\epsilon_2)\theta \triangleq \epsilon_1\theta = !!\epsilon_2\theta & \\
\\
(\mathbf{if} \ \epsilon \ \mathbf{then} \ S_1 \ \mathbf{else} \ S_2 \ \mathbf{end})\theta \triangleq \mathbf{if} \ \epsilon\theta \ \mathbf{then} \ S_1\theta \ \mathbf{else} \ S_2\theta \ \mathbf{end} & & \\
(\mathbf{raise} \ \epsilon \ \mathbf{end})\theta \triangleq \mathbf{raise} \ \epsilon\theta \ \mathbf{end} & & \\
(\mathbf{local} \ X_1 \dots X_n \ \mathbf{in} \ S \ \mathbf{end})\theta \triangleq \mathbf{local} \ X_1 \dots X_n \ \mathbf{in} \ S\theta_{\{X_1, \dots, X_n\}} \ \mathbf{end} & & \\
(\mathbf{proc}\{\epsilon \ X_1 \dots X_n\} \ S \ \mathbf{end})\theta \triangleq \mathbf{proc}\{\epsilon\theta \ X_1 \dots X_n\} \ S\theta_{\{X_1, \dots, X_n\}} \ \mathbf{end} & & \\
(\mathbf{try} \ S_1 \ \mathbf{catch} \ X \ \mathbf{then} \ S_2 \ \mathbf{end})\theta \triangleq \mathbf{try} \ S_1\theta \ \mathbf{catch} \ X \ \mathbf{then} \ S_2\theta_{\{X\}} \ \mathbf{end} & & \\
(\mathbf{case} \ \epsilon \ \mathbf{of} \ J \ \mathbf{then} \ S_1 \ \mathbf{else} \ S_2 \ \mathbf{end})\theta \triangleq \mathbf{case} \ \epsilon\theta \ \mathbf{of} \ J\theta \ \mathbf{then} \ S_1\theta_{\text{bv}(J)} \ \mathbf{else} \ S_2\theta \ \mathbf{end} & &
\end{array}$$

Figure 5: Substitution on statements and patterns

Using  $\epsilon$  and its decorated variants to stand for a variable or a variable identifier, and  $P$  to stand for any of the primitive operations, we define by induction in Figure 5 the application of a substitution  $\theta$  to an extended statement  $S$  (and a pattern  $J$ ). In Figure 5, we define  $\text{bv}(J)$  as follows:

$$\text{bv}(v) \triangleq \emptyset \qquad \text{bv}(!X) \triangleq \emptyset \qquad \text{bv}(\epsilon(\epsilon_1 : X_1 \dots \epsilon_n : X_n)) \triangleq \{X_1, \dots, X_n\}$$

**Names.** We define here functions and predicate dealing with names. The set of gate names  $\text{gn}(\mathcal{T}, \sigma)$  of an execution structure  $(\mathcal{T}, \sigma)$  is defined as follows:

$$\text{gn}(\mathcal{T}, \sigma) = \{\gamma \in \text{name} \mid \exists x, x \in \text{v}(\mathcal{T}, \sigma), \sigma \models x = \gamma \wedge \gamma : \text{gate}\}$$

The set of thread names  $\text{thn}(\mathcal{T}, \sigma)$  of an execution structure  $(\sigma, \mathcal{T})$  is defined inductively as follows:

$$\text{thn}(\tau : \mathcal{T}, \sigma) = \{\tau\} \quad \text{thn}(\mathcal{T}_1 \ \mathcal{T}_2, \sigma) = \text{thn}(\mathcal{T}_1, \sigma) \cup \text{thn}(\mathcal{T}_2, \sigma)$$

The set of kell names  $\text{kn}(\mathcal{T}, \sigma)$  of an execution structure  $(\sigma, \mathcal{T})$  is defined as follows:

$$\text{kn}(\mathcal{T}, \sigma) = \{\kappa \in \text{name} \mid \exists x, y, \pi, x \in \text{v}(\mathcal{T}, \sigma) \wedge \sigma \models x = \kappa \wedge \kappa : \text{kell}(\pi, y)\}$$

The set of procedure names  $\text{pn}(\mathcal{T}, \sigma)$  of an execution structure  $(\mathcal{T}, \sigma)$  is defined as follows:

$$\text{pn}(\mathcal{T}, \sigma) = \{\xi \in \text{name} \mid \exists x, y, X_i, S, x \in \text{v}(\mathcal{T}, \sigma) \wedge \sigma \models x = \xi \wedge \xi : \text{proc}\{\$ X_1 \dots X_n\} S \text{ end}\}$$

The function  $\text{kgpn}$  that returns the set of gate, procedure and kell names of an execution structure  $(\mathcal{T}, \sigma)$  is defined as:

$$\text{kgpn}(\mathcal{T}, \sigma) = \text{gn}(\mathcal{T}, \sigma) \cup \text{pn}(\mathcal{T}, \sigma) \cup \text{kn}(\mathcal{T}, \sigma)$$

The function  $\text{tkn}_\sigma$  returns the names of top level kells in a task, relative to a store  $\sigma$ . It is defined as follows, where  $\top$  denotes by convention the name of the top-level kell:

$$\text{tkn}_\sigma(\mathcal{T}) = \{\eta \in \text{kn}(\mathcal{T}, \sigma) \mid \sigma \models \text{in}(\top, \eta)\}$$

**Equivalence relation.** The reduction relation makes use of an equivalence relation, noted  $\equiv$ , between statements, between tasks and between stores. The equivalence relation between statement, noted  $\equiv$ , is the smallest equivalence relation that obeys the rules given in Figure 6.

The equivalence relation between tasks, also noted  $\equiv$ , is the smallest equivalence relation that obeys the rules in Figure 7.

The equivalence relation between stores, also noted  $\equiv$ , is the smallest relation that obeys the rules in Figure 8, where  $\delta$  denotes a variable or a value, and where  $P$  and its decorated variants denote a store predicate of the form  $\text{proc}\{\$ X_1 \dots X_n\} S \text{ end}$ .

**Entailment between stores.** The rules in Figure 8 define also an entailment relation between stores and stores. The entailment relation  $\models$  is defined as the smallest relation that obeys the rules in Figure 8.

The domain of a store  $\sigma$ , noted  $\text{dom}(\sigma)$ , is the set of variables and names that occur in  $\sigma$ . It is defined as

$$\text{dom}(\sigma) = \text{v}(\sigma) \cup \{\xi \in \text{Name} \mid \exists x \in \text{dom}(\sigma), \sigma \models x = \xi \vee x = \xi(\dots)\}$$

$$\begin{array}{c}
\text{[S.}\alpha\text{]} \frac{S_1 =_\alpha S_2}{S_1 \equiv S_2} \quad \text{[S.SEQ]} \frac{S_1 \equiv S_2 \quad S'_1 \equiv S'_2}{S_1 S'_1 \equiv S_2 S'_2} \quad \text{[S.LOCAL]} \frac{S_1 \equiv S_2}{\mathbf{local } X_1 \dots X_n \mathbf{ in } S_1 \mathbf{ end} \equiv \mathbf{local } X_1 \dots X_n \mathbf{ in } S_2 \mathbf{ end}} \\
\text{[S.THREAD]} \frac{S_1 \equiv S_2}{\mathbf{thread}\{X\} S_1 \mathbf{ end} \equiv \mathbf{thread}\{X\} S_2 \mathbf{ end}} \\
\text{[S.IF]} \frac{S_1 \equiv S_2 \quad S_3 \equiv S_4}{\mathbf{if } X \mathbf{ then } S_1 \mathbf{ else } S_3 \mathbf{ end} \equiv \mathbf{if } X \mathbf{ then } S_2 \mathbf{ else } S_4 \mathbf{ end}} \\
\text{[S.CASE]} \frac{S_1 \equiv S_2 \quad S_3 \equiv S_4}{\mathbf{case } X \mathbf{ of } J \mathbf{ then } S_1 \mathbf{ else } S_3 \equiv \mathbf{case } X \mathbf{ of } J \mathbf{ then } S_2 \mathbf{ else } S_4} \\
\text{[S.TRY]} \frac{S_1 \equiv S_2 \quad S_3 \equiv S_4}{\mathbf{try } S_1 \mathbf{ catch } X \mathbf{ then } S_3 \mathbf{ end} \equiv \mathbf{try } S_2 \mathbf{ catch } X \mathbf{ then } S_4 \mathbf{ end}} \\
\text{[S.PROC]} \frac{S_1 \equiv S_2}{\mathbf{proc}\{P X_1 \dots X_n\} S_1 \mathbf{ end} \equiv \mathbf{proc}\{P X_1 \dots X_n\} S_2 \mathbf{ end}} \quad \text{[S.KELL]} \frac{S_1 \equiv S_2}{\mathbf{kell}\{K\} S_1 \mathbf{ end} \equiv \mathbf{kell}\{K\} S_2 \mathbf{ end}}
\end{array}$$

Figure 6: Equivalence between statements

$$\begin{array}{c}
\text{[T.THREAD]} \frac{S_1 \equiv S_2 \quad T_1 \equiv T_2}{\eta : \langle S_1 \ T_1 \rangle \equiv \eta : \langle S_2 \ T_2 \rangle} \quad \text{[T.COMM]} T_1 T_2 \equiv T_2 T_1 \quad \text{[T.ASSOC]} T_1 (T_2 T_3) \equiv (T_1 T_2) T_3 \\
\text{[T.PAR]} \frac{T_1 \equiv T_2}{T_1 T \equiv T_2 T}
\end{array}$$

Figure 7: Equivalence between tasks

$$\begin{array}{c}
\text{[E.PACK]} \frac{T_1 \equiv T_2 \quad \sigma_1 \equiv \sigma_2}{x = \mathbf{pack}(\zeta, T_1, \sigma_1, \mu) \equiv x = \mathbf{pack}(\zeta, T_2, \sigma_2, \mu)} \quad \text{[E.PROC]} \frac{P \equiv P'}{\xi : P \equiv \xi : P'} \quad \text{[E.EQUAL]} x = y \equiv y = x \\
\text{[E.ELIM1]} \frac{\sigma \equiv \sigma_1 \wedge \sigma_2}{\sigma \models \sigma_1} \quad \text{[E.ELIM2]} \frac{\sigma \equiv \sigma_1 \wedge \sigma_2}{\sigma \models \sigma_2} \quad \text{[E.INTRO]} \frac{\sigma \models \sigma_1 \quad \sigma \models \sigma_2}{\sigma \models \sigma_1 \wedge \sigma_2} \quad \text{[E.EQUALT]} \frac{\sigma \models y = \delta \wedge x = y}{\sigma \models x = \delta} \\
\text{[E.ENTAILS]} \frac{\sigma \models \sigma' \quad \sigma' \models \sigma}{\sigma \equiv \sigma'} \quad \text{[E.REFLEX]} \sigma \models \sigma \quad \text{[E.TRANS]} \frac{\sigma_1 \models \sigma_2 \quad \sigma_2 \models \sigma_3}{\sigma_1 \models \sigma_3}
\end{array}$$

Figure 8: Equivalence between stores

$$\begin{array}{c}
\text{[EQ.BASE]} \frac{u = u'}{u \equiv_{\sigma} u'} \quad \text{[EQ.EQUAL]} \frac{\sigma \models x = y}{x \equiv_{\sigma} y} \quad \text{[EQ.VAR]} \frac{\sigma \models x = v \wedge y = v' \quad v \equiv_{\sigma} v'}{x \equiv_{\sigma} y} \\
\text{[EQ.PACK]} \frac{\mathcal{T}_1 \equiv \mathcal{T}_2 \quad \sigma_1 \equiv \sigma_2}{\text{pack}(\zeta, \mathcal{T}_1, \sigma_1, \mu) \equiv_{\sigma} \text{pack}(\zeta, \mathcal{T}_2, \sigma_2, \mu)} \quad \text{[EQ.FAILED]} \frac{x \equiv_{\sigma} y}{\text{failed}(x) \equiv_{\sigma} \text{failed}(y)} \\
\text{[EQ.RECORD]} \frac{x_1 \equiv_{\sigma} y_1 \dots x_n \equiv_{\sigma} y_n}{l(f_1 : x_1 \dots f_n : x_n) \equiv_{\sigma} l(f_1 : y_1 \dots f_n : y_n)}
\end{array}$$

Figure 9: Equality between values

$$\begin{array}{c}
\text{[DIS.BASE]} \frac{u \neq u'}{u \not\equiv_{\sigma} u'} \quad \text{[DIS.VAR]} \frac{\sigma \models x = v \wedge y = v' \quad v \not\equiv_{\sigma} v'}{x \not\equiv_{\sigma} y} \quad \text{[DIS.PACK]} \frac{\neg \exists \mathcal{T}_2, \sigma_2, v = \text{pack}(\zeta, \mathcal{T}_2, \sigma_2, \mu)}{v \not\equiv_{\sigma} \text{pack}(\zeta, \mathcal{T}_1, \sigma_1, \mu)} \\
\text{[DIS.PACKD]} \frac{v = \text{pack}(\zeta, \mathcal{T}_2, \sigma_2, \mu) \quad \mathcal{T}_2 \neq \mathcal{T}_1 \vee \sigma_2 \neq \sigma_1}{v \not\equiv_{\sigma} \text{pack}(\zeta, \mathcal{T}_1, \sigma_1, \mu)} \quad \text{[DIS.FAIL]} \frac{\neg \exists x, v = \text{failed}(x)}{v \not\equiv_{\sigma} \text{failed}(y)} \\
\text{[DIS.FAILED]} \frac{v = \text{failed}(x) \quad x \not\equiv_{\sigma} y}{v \not\equiv_{\sigma} \text{failed}(y)} \quad \text{[DIS.REC]} \frac{\neg \exists x_1, \dots, x_n, v = l(f_1 : x_1 \dots f_n : x_n)}{v \not\equiv_{\sigma} l(f_1 : y_1 \dots f_n : y_n)} \\
\text{[DIS.RECD]} \frac{v = l(f_1 : x_1 \dots f_n : x_n) \quad \bigvee_{i=1}^n x_i \not\equiv_{\sigma} y_i}{v \not\equiv_{\sigma} l(f_1 : y_1 \dots f_n : y_n)}
\end{array}$$

Figure 10: Inequality between values

The extension of the entailment relation to first-order formulas is classical and is given by the rules below, where  $\epsilon$  denotes a variable  $x$  or a name  $\xi$ .

$$\begin{array}{ccc}
\sigma \models \neg \phi & \iff & \neg(\sigma \models \phi) \\
\sigma \models \phi \vee \psi & \iff & (\sigma \models \phi) \vee (\sigma \models \psi) \\
\sigma \models \forall \epsilon, \phi & \iff & \forall \epsilon \in \text{dom}(\sigma), (\sigma \models \phi) \\
\sigma \models \exists \epsilon, \phi & \iff & \exists \epsilon \in \text{dom}(\sigma), (\sigma \models \phi)
\end{array}$$

We note  $\sigma \models x = \perp$  if  $x \in \text{dom}(\sigma)$  and  $\neg \exists v, \sigma \models x = v$ .

**Equality between values.** The notion of equality between values during execution is captured by relations  $\equiv_{\sigma}$  and  $\not\equiv_{\sigma}$ . Intuitively, two values are equal, relative to a store  $\sigma$  if they correspond to the same base value, or to the same packed value, or to the same failed value, or to the same record. The relation  $\equiv_{\sigma}$  is defined as the smallest equivalence relation that satisfies the rules in Figure 9, where  $u$  denotes a base value (integer, atom, or name), and  $v, v'$  denote arbitrary values ( $v, v' \neq \perp$ ).

The relation  $\not\equiv_{\sigma}$  is defined as the smallest relation that satisfies the rules in Figure 10, where  $u$  denotes a base value (integer, atom, or name), and  $v, v'$  denote arbitrary values ( $v, v' \neq \perp$ ). Intuitively, the relation  $\not\equiv_{\sigma}$  expresses the fact that two values can be proved to be non-equal in store  $\sigma$ , regardless of future bindings.

**Invalid stores.** Intuitively, a store is invalid if it binds different values to the same variable or to the same name, or if it does not obey structural invariants such as e.g. the fact that a kcell can only have one parent kcell. We note  $\text{in}^+$  the transitive closure of the relation  $\text{in}$ , and  $\text{subg}^+$  the transitive closure of the relation  $\text{subg}$ . We note  $\sigma = \perp$  to indicate that store  $\sigma$  is invalid. We define the predicate  $\sigma \equiv \perp$  in Figure 11, where  $P, Q$  denote store predicates of the forms  $\text{cell}(x)$ ,  $\text{gate}$ ,  $\text{thread}(x)$ ,  $\text{kcell}(\pi, x)$ , or  $\text{proc}\{\$ X_1 \dots X_n\}S \text{ end}$ .

$$\begin{aligned}
\sigma \equiv \perp &\triangleq \exists x, v, v', \sigma \models x = v \wedge x = v' \wedge v \not\bowtie_{\sigma} v' & (1) \\
&\vee \exists \xi, P, Q, \sigma \models \xi : P \wedge \xi : Q \wedge P \not\equiv Q & (2) \\
&\vee \exists \tau, x, \sigma \models \tau : \mathbf{thread}(x) \wedge \neg \mathbf{read}(x) & (3) \\
&\vee \exists \kappa, \pi, x, \sigma \models \kappa : \mathbf{kell}(\pi, x) \wedge \neg \mathbf{read}(x) & (4) \\
&\vee \exists \kappa, \kappa', \kappa'', \kappa \neq \kappa' \wedge \sigma \models \mathbf{in}(\kappa, \kappa'') \wedge \mathbf{in}(\kappa', \kappa'') & (5) \\
&\vee \exists \kappa, \kappa', \tau, \kappa \neq \kappa' \wedge \sigma \models \mathbf{inth}(\kappa, \tau) \wedge \mathbf{in}(\kappa', \tau) & (6) \\
&\vee \exists \kappa, \kappa', \sigma \models \mathbf{in}(\kappa, \kappa') \wedge \neg \exists \pi, x, \pi', x', \sigma \models \kappa : \mathbf{kell}(\pi, x) \wedge \kappa' : \mathbf{kell}(\pi', x') & (7) \\
&\vee \exists \kappa, \tau, \sigma \models \mathbf{inth}(\kappa, \tau) \wedge \neg \exists \pi, x, y, \sigma \models \kappa : \mathbf{kell}(\pi, x) \wedge \tau : \mathbf{thread}(y) & (8) \\
&\vee \exists \kappa, \kappa', \sigma \models \mathbf{in}^+(\kappa, \kappa') \wedge \sigma \not\models \mathbf{in}^+(\kappa, \kappa') & (9) \\
&\vee \exists \gamma, \gamma', \sigma \models \mathbf{subg}(\gamma, \gamma') \wedge \neg \sigma \models \gamma : \mathbf{gate} \wedge \gamma' : \mathbf{gate} & (10) \\
&\vee \exists \gamma, \gamma', \sigma \models \mathbf{subg}^+(\gamma, \gamma') \wedge \sigma \not\models \mathbf{subg}^+(\gamma', \gamma) & (11)
\end{aligned}$$

Figure 11: Invalid stores

$$\begin{array}{c}
\frac{v \in \mathbf{int} \cup \mathbf{atom}}{\mathbf{strict}_{\sigma}(v)} \quad \frac{\xi \in \mathbf{name} \quad \sigma \not\models \xi : \mathbf{p}(\dots)}{\mathbf{strict}_{\sigma}(\xi)} \quad \frac{\xi \in \mathbf{name} \quad \sigma \models \xi : \mathbf{gate}}{\mathbf{strict}_{\sigma}(\xi)} \quad \frac{v = \mathbf{pack}(\zeta, T, \sigma, \mu)}{\mathbf{strict}_{\sigma}(v)} \\
\frac{\mathbf{strict}_{\sigma}(v) \quad \sigma \models x = v}{\mathbf{strict}_{\sigma}(x)} \quad \frac{\mathbf{strict}_{\sigma}(x)}{\mathbf{strict}_{\sigma}(\mathbf{failed}(x))} \quad \frac{\mathbf{strict}_{\sigma}(v_1) \dots \mathbf{strict}_{\sigma}(v_n) \quad \sigma \models x_1 = v_1 \wedge \dots \wedge x_n = v_n}{\mathbf{strict}_{\sigma}(f(l_1 : x_1, \dots, l_n : x_n))} \\
\frac{\xi \in \mathbf{name} \quad \sigma \models \xi : P \quad P = \mathbf{proc}\{\$ X_1 \dots X_n\} S \mathbf{end} \quad \forall x \in \mathbf{v}(S, \sigma) \mathbf{strict}_{\sigma}(x)}{\mathbf{strict}_{\sigma}(\xi)}
\end{array}$$

Figure 12: Definition of strictness

**Strictness.** We say that a value  $v$  is *strict* relative to  $\sigma$ , noted  $\mathbf{strict}_{\sigma}(v)$ , if  $v$  is either an integer or an atom, if  $v$  is a pure name (i.e. not a gate name, a cell name, a thread name, a kell name, or a procedure name), if  $v$  is a record value which contains only variables bound to strict values, or if  $v$  is a name bound to a gate, or a procedure whose free variables are bound, recursively, to strict values. Formally, the predicate  $\mathbf{strict}$  is defined as the smallest predicate verifying the rules given in Figure 12, where  $\mathbf{p}$  ranges over the set of semantical predicates  $\{\mathbf{proc}, \mathbf{cell}, \mathbf{thread}, \mathbf{kell}, \mathbf{gate}\}$ . We extend the  $\mathbf{strict}$  function into a predicate on pairs of the form  $\langle \text{extended statement, set of variables} \rangle$  thus:

$$\mathbf{strict}_{\sigma}(S, V) \triangleq \forall x \in \mathbf{v}(S, \sigma) \setminus V, \mathbf{strict}_{\sigma}(x)$$

**Matching.** The reduction relation depends also on a function  $\mathbf{match}$  that operates on lists of values and patterns. Function  $\mathbf{match}$  is defined inductively by the table below ( $\mathbf{match}_{\sigma}(x, J)$  is defined to be  $\perp$ , where  $\perp$  denotes a match failure, in all other cases). We note  $\mathbf{Id}$  the trivial substitution, i.e. the substitution whose domain is empty (and thus, for all terms  $S$ ,  $S\mathbf{Id} = S$ ).

$\sigma$	$J$	$\mathbf{match}_{\sigma}(x, J)$
$\sigma \models x = v$	$v$	$\mathbf{Id}$
$\sigma \models x = v \wedge y = v$	$!y$	$\mathbf{Id}$
$\sigma \models x = v_0(v_1 : x_1 \dots v_n : x_n)_x \wedge \epsilon_i = v_i$	$\epsilon_0(\epsilon_1 : X_1 \dots \epsilon_n : X_n)$	$\{X_1 \rightarrow x_1, \dots, X_n \rightarrow x_n\}$

**Unification** The function  $\mathbf{Unify}$  is defined by

$$\mathbf{Unify}(x, y, \sigma) = \mathbf{fst}(\mathbf{U}(x, y, \sigma, \emptyset))$$

with the function  $\mathbb{U}$  defined inductively by the following rules, where the function  $\text{fst}$  returns the first element of a pair, the function  $\text{snd}$  returns the second element of a pair,  $v$  denotes an arbitrary value, and  $u, u'$  denote arbitrary base values, i.e. integers, names or atoms (note that a failed value  $\text{failed}(z)$  is considered for the purpose of the algorithm as a unary tuple):

$$\begin{array}{ll}
\mathbb{U}(x, y, \sigma, B) = \langle \sigma \wedge x = y, B \cup \{\{x, y\}\} \rangle & \text{if } \{x, y\} \notin B \wedge \sigma \models x = \perp \wedge y = v \wedge \neg \text{rread}(x) \\
\mathbb{U}(x, y, \sigma, B) = \langle \sigma \wedge x = y, B \cup \{\{x, y\}\} \rangle & \text{if } \{x, y\} \notin B \wedge \sigma \models x = v \wedge y = \perp \wedge \neg \text{rread}(y) \\
\mathbb{U}(x, y, \sigma, B) = \langle \sigma \wedge x = y, B \cup \{\{x, y\}\} \rangle & \text{if } \{x, y\} \notin B \wedge \sigma \models x = \perp \wedge y = \perp \wedge \neg \text{rread}(x) \wedge \neg \text{rread}(y) \\
\mathbb{U}(x, y, \sigma, B) = \langle \sigma \wedge x = y \wedge \text{read}(y), B \cup \{\{x, y\}\} \rangle & \text{if } \{x, y\} \notin B \wedge \sigma \models x = \perp \wedge y = \perp \wedge \text{rread}(x) \wedge \neg \text{rread}(y) \\
\mathbb{U}(x, y, \sigma, B) = \langle \sigma \wedge x = y \wedge \text{read}(x), B \cup \{\{x, y\}\} \rangle & \text{if } \{x, y\} \notin B \wedge \sigma \models x = \perp \wedge y = \perp \wedge \neg \text{rread}(x) \wedge \text{rread}(y) \\
\mathbb{U}(x, y, \sigma, B) = \langle \sigma, B \cup \{\{x, y\}\} \rangle & \text{if } \{x, y\} \notin B \wedge \sigma \models x = u \wedge y = u' \wedge u = u' \\
\mathbb{U}(x, y, \sigma, B) = \langle \sigma \wedge \sigma', B \cup B' \rangle & \text{if } \{x, y\} \notin B \wedge \sigma \models x = f(a_1 : x_1 \dots a_n : x_n) \wedge y = f(a_1 : y_1 \dots a_n : y_n) \\
& \text{with } \sigma' = \bigwedge_{i=1}^n \text{fst}(U_i) \quad B' = \bigcup_{i=1}^n \text{snd}(U_i) \quad U_i = \mathbb{U}(x_i, y_i, \sigma, B \cup \{\{x, y\}\}) \\
\mathbb{U}(x, y, \sigma, B) = \langle \sigma, B \rangle & \text{if } \{x, y\} \in B \\
\mathbb{U}(x, y, \sigma, B) = \langle \perp, B \rangle & \text{otherwise}
\end{array}$$

**Subkells.** The assertion  $\text{subk}_\sigma(\zeta, \{\zeta_1, \dots, \zeta_n\})$  indicates that the set  $\{\zeta_1, \dots, \zeta_n\}$  corresponds to the set of all *active* subkells of kell  $\zeta$ , in store  $\sigma$ . The assertion  $\text{subth}_\sigma(\zeta, \{\zeta_1, \dots, \zeta_n\})$  indicates that the set  $\{\zeta_1, \dots, \zeta_n\}$  corresponds to the set of all threads executing in *active* subkells of kell  $\zeta$ , in store  $\sigma$ .

The predicates  $\text{subth}$  and  $\text{subk}$  are defined as follows:

$$\begin{array}{l}
\text{child}_\sigma(\xi, \eta) \equiv \sigma \models \xi : \text{kell}(\varpi, w) \wedge \eta : \text{kell}(\pi, z) \wedge z = \perp \wedge \text{in}(\xi, \eta) \\
\text{desc}_\sigma(\xi, \eta) \equiv \text{child}_\sigma(\xi, \eta) \vee \exists \zeta, \text{desc}_\sigma(\xi, \zeta) \wedge \text{child}_\sigma(\zeta, \eta) \\
\text{subk}_\sigma(\xi, \{\xi_1, \dots, \xi_n\}) \equiv \{\xi_1, \dots, \xi_n\} = \{\xi \mid \text{desc}_\sigma(\xi, \eta)\} \\
\text{subth}_\sigma(\xi, \{\zeta_1, \dots, \zeta_n\}) \equiv \{\zeta_1, \dots, \zeta_n\} = \{\zeta \mid \exists \eta, \text{desc}_\sigma(\xi, \eta) \wedge \sigma \models \text{inth}(\eta, \zeta)\}
\end{array}$$

**Gate access.** The assertion  $\text{access}_\sigma(\gamma, \kappa, \kappa')$  means that gate  $\gamma$  is accessible for communication between the threads in  $\kappa$  and threads in  $\kappa'$ . The predicate  $\text{access}$  is defined by cases as follows. In the first case,  $\text{access}_\sigma(\gamma, \kappa, \kappa)$ . In the second case, where  $\sigma \models \text{in}(\kappa, \kappa')$  or  $\sigma \models \text{in}(\kappa', \kappa)$ , we have  $\text{access}_\sigma(\gamma, \kappa, \kappa')$ , and  $\text{access}_\sigma(\gamma, \kappa', \kappa)$ . In the third case, let  $\kappa_0, \kappa_1, \dots, \kappa_{n+1}$ ,  $n \geq 1$ , be the smallest sequence such that  $\kappa_0 = \kappa$ ,  $\kappa_{n+1} = \kappa'$ ,  $\sigma \models \text{in}(\kappa_i, \kappa_{i+1})$  or  $\sigma \models \text{in}(\kappa_{i+1}, \kappa_i)$  for all  $i \in I = \{0, \dots, n\}$  (i.e.  $\kappa_1, \dots, \kappa_{n+1}$  is the minimal path from  $\tau$  to  $\tau'$  in the kell tree, where we assume a top-level kell named  $\top$ ). Let  $\pi_1, \dots, \pi_{n+1}$  be such that  $\sigma \models \kappa_i : \text{kell}(\pi_i, x_i)$ . We define  $\text{access}_\sigma(\gamma, \kappa, \kappa')$  by:

$$\begin{array}{l}
\text{access}_\sigma(\gamma, \kappa, \kappa') \triangleq \bigvee_{i \in I} \bigwedge_{j \in I \setminus \{i\}} \text{auth}_\sigma(\gamma, \kappa_j, \kappa_{j+1}) \\
\text{auth}_\sigma(\gamma, \kappa_i, \kappa_{i+1}) \triangleq \kappa_i \cdot \gamma \in \pi_{i+1} & \text{if } \sigma \models \text{in}(\kappa_{i+1}, \kappa_i) \\
\text{auth}_\sigma(\gamma, \kappa_i, \kappa_{i+1}) \triangleq \kappa_{i+1} \cdot \gamma \in \pi_i & \text{if } \sigma \models \text{in}(\kappa_i, \kappa_{i+1})
\end{array}$$

The truth value of the assertion  $\kappa \cdot \gamma \in \pi$  in store  $\sigma$  is defined inductively by the table below (the truth value of the assertion is that of the table cell predicate, depending on the form of  $\pi$ ), where  $\text{subg}^r$  is the reflexive closure of the relation  $\text{subg}$ , and  $\text{subg}^*$  is the reflexive and transitive closure of the relation  $\text{subg}$ :

$\pi$	$K \cdot G$	$K \cdot \gamma'$	$\kappa' \cdot G$	$\{\kappa' \cdot \gamma'\}$	$\{\kappa' \cdot \eta^r\}$	$\{\kappa' \cdot \eta^*\}$	$\pi_1 \cup \pi_2$	$\pi_1 \setminus \pi_2$
$\kappa \cdot \gamma \in \pi$	true	$\gamma = \gamma'$	$\kappa = \kappa'$	$\kappa = \kappa'$ $\wedge \gamma = \gamma'$	$\kappa = \kappa'$ $\wedge \sigma \models \text{subg}^r(\eta, \gamma)$	$\kappa = \kappa'$ $\wedge \sigma \models \text{subg}^*(\eta, \gamma)$	$\kappa \cdot \gamma \in \pi_1$ $\vee \kappa \cdot \gamma \in \pi_2$	$\kappa \cdot \gamma \in \pi_1$ $\wedge \kappa \cdot \gamma \notin \pi_2$

**Grants.** Granting access of a gate to a kell is specified using the function `grant`, which is defined below:

$\text{grant}(\sigma, k, g) \triangleq \kappa \cdot \gamma$	if $\sigma \models k = \kappa \wedge \kappa : \text{kell}(\dots) \wedge g = \gamma \wedge \gamma : \text{gate}$
$\text{grant}(\sigma, k, g) \triangleq \kappa \cdot \gamma^r$	if $\sigma \models k = \kappa \wedge \kappa : \text{kell}(\dots) \wedge g = \gamma \# \text{all} \wedge \gamma : \text{gate}$
$\text{grant}(\sigma, k, g) \triangleq \kappa \cdot \gamma^*$	if $\sigma \models k = \kappa \wedge \kappa : \text{kell}(\dots) \wedge g = \gamma \# \text{allrec} \wedge \gamma : \text{gate}$
$\text{grant}(\sigma, k, g) \triangleq \kappa \cdot G$	if $\sigma \models k = \kappa \wedge \kappa : \text{kell}(\dots) \wedge g = \text{all}$
$\text{grant}(\sigma, k, g) \triangleq K \cdot \gamma$	if $\sigma \models k = \text{all} \wedge g = \gamma \wedge \gamma : \text{gate}$
$\text{grant}(\sigma, k, g) \triangleq K \cdot \gamma^r$	if $\sigma \models k = \text{all} \wedge g = \gamma \# \text{all} \wedge \gamma : \text{gate}$
$\text{grant}(\sigma, k, g) \triangleq K \cdot \gamma^*$	if $\sigma \models k = \text{all} \wedge g = \gamma \# \text{allrec} \wedge \gamma : \text{gate}$
$\text{grant}(\sigma, k, g) \triangleq K \cdot G$	if $\sigma \models k = \text{all} \wedge g = \text{all}$
$\text{grant}(\sigma, k, g) \triangleq \emptyset$	otherwise

## B Failure rules

We gather in this section the failure rules of the OZ/K operational semantics.

### Thread creation failure

Thread creation fails when the thread name parameter is already bound. This is captured by the following rule.

$$[\text{THREADF}] \frac{\text{thread}\{x\} S \text{ end} \parallel \text{raise error}(\text{thread}(x)) \text{ end}}{\sigma \models x \neq \perp \parallel \sigma}$$

### Read-only variables

$$[\text{READF}] \frac{x \neq !y \parallel \text{raise error}(\text{read}(x y)) \text{ end}}{\sigma \models x \neq \perp \parallel \sigma}$$

### Binding

A binding statement  $x = v$  fails if variable  $x$  is already bound, or is read-only. The following rule captures this.

$$[\text{BINDVF}] \frac{x = v \parallel \text{raise error}(\text{bindV}(x)) \text{ end}}{\sigma \models x \neq \perp \vee \text{rread}(x) \parallel \sigma}$$

A binding statement  $x = y$  fails if both variables  $x$  and  $y$  are already bound, or both of them are read-only. The following rule captures this.

$$[\text{BINDXYF}] \frac{x = y \parallel \text{raise error}(\text{bindXY}(x y)) \text{ end}}{\sigma \models (x \neq \perp \wedge y \neq \perp) \vee (\text{rread}(x) \wedge \text{rread}(y)) \parallel \sigma}$$

A binding statement  $x = y.z$  fails if  $x$  is already bound,  $y$  is not a record or a chunk, or if  $z$  is not an integer, an atom or a name. The following rule captures this.

$$[\text{BINDRF}] \frac{x = y.z \parallel \text{raise error}(\text{bindR}(x y z)) \text{ end}}{\sigma \parallel \sigma} \text{ if } C$$

where

$$C \equiv (\sigma \models x \neq \perp) \vee (\sigma \models y \neq l(f_1 : w_1 \dots f_n : w_n)_m) \vee (\sigma \models z = v \wedge v \notin \text{Int} \cup \text{Atom} \cup \text{Name})$$

### Values

The operation `Equal` fails if the third parameter is already bound. This is captured by the following rule.

$$[\text{EQF}] \frac{\{\text{Equal } x y r\} \parallel \text{raise error}(\text{equal}(r)) \text{ end}}{\sigma \models r \neq \perp \parallel \sigma}$$

The operation `Status` fails if the first argument is not a thread, or is not a thread of the current cell.

$$[\text{STATUSF}] \frac{\{\text{Status } x y\} \parallel \text{raise error}(\text{status}(x y)) \text{ end}}{\sigma \parallel \sigma} \text{ if } C$$

where

$$C \equiv (\sigma \models y \neq \perp) \vee (\sigma \models x \neq \perp \wedge \sigma \not\models \exists \xi, x = \tau \wedge \tau : \text{thread}(w) \wedge \text{in}(\kappa, \tau))$$

**If statement**

An if statement fails if its condition evaluates to a non boolean value. This is captured by the following rule.

$$[\text{IFF}] \frac{\text{if } x \text{ then } S_1 \text{ else } S_2 \text{ end} \parallel \text{raise error}(\text{if}(x)) \text{ end}}{\sigma \models x = v} \quad \text{if } v \notin \{\text{true}, \text{false}\}$$

**Names**

Name creation fails if its argument is already bound. This is captured by the following rule.

$$[\text{NEWNAMEF}] \frac{\{\text{NewName } x\} \parallel \text{raise error}(\text{newName}(x)) \text{ end}}{\sigma \models x \neq \perp}$$

**Procedure abstraction**

Introducing a new procedure fails if the procedure name argument is already bound. This is captured by the following rule.

$$[\text{PNEWF}] \frac{\text{proc}\{x \ X_1 \dots X_n\} S \text{ end} \parallel \text{raise error}(\text{pNew}(x)) \text{ end}}{\sigma \models x \neq \perp}$$

Calling a procedure fails if the first argument of the call is not a procedure name, or if the number of arguments provided does not match that of the called procedure. This is captured by the following rule.

$$[\text{PCALLF}] \frac{\{x \ x_1 \dots x_n\} \parallel \text{raise error}(\text{pCall}(x \ [x_1 \dots x_n])) \text{ end}}{\sigma} \quad \text{if } C$$

where

$$C \equiv (\sigma \models x = v \wedge (v \notin \text{Name} \vee (v \in \text{Name} \wedge \sigma \not\models v : \text{proc}\{\$ \dots\} \text{end}))) \vee (\sigma \models x = \xi \wedge \xi : P \wedge P.\text{arity} \neq n)$$

Replacing a procedure fails if the first argument is not an existing procedure, or if the replacement closure does not have the same arity as the replaced one. This is captured by the following rule.

$$[\text{PREPBF}] \frac{\text{proc}\{x \ X_1 \dots X_n\} S \text{ end} \parallel \text{raise error}(\text{pRep}(x)) \text{ end}}{\sigma} \quad \text{if } C$$

where

$$C \equiv (\sigma \models x = v \wedge (v \notin \text{Name} \vee (v \in \text{Name} \wedge \sigma \not\models v : \text{proc}\{\$ \dots\} \text{end}))) \vee (\sigma \models x = \xi \wedge \xi : P \wedge P.\text{arity} \neq n)$$

**Checking determinacy**

Operation `IsDet` fails if its second argument is already bound. This is captured by the following rule.

$$[\text{DETf}] \frac{\{\text{IsDet } x \ y\} \parallel \text{raise error}(\text{isDet}(y)) \text{ end}}{\sigma \models y \neq \perp}$$

## Cells

Cell creation fails if its second argument is already bound. This is captured by the following rule.

$$[\text{NCELLF}] \frac{\{\text{NewCell } x \ y \ z\} \parallel \mathbf{raise\ error}(\text{nCell}(y)) \mathbf{end}}{\sigma \models y \neq \perp} \parallel \frac{}{\sigma}$$

Operation `Exchange` fails if its first argument is not a cell, or if its second argument is already bound. This is captured by the following rule.

$$[\text{ECELLF}] \frac{\{\text{Exchange } x \ y \ z\} \parallel \mathbf{raise\ error}(\text{eCell}(x)) \mathbf{end}}{\sigma} \parallel \frac{}{\sigma} \text{ if } C$$

where

$$C \equiv (\sigma \models x = v \wedge (v \notin \text{Name} \vee (v \in \text{Name} \wedge \neg \exists w, \sigma \models v : \text{cell}(w)))) \vee (\sigma \models y \neq \perp)$$

## Failed values

The creation of a failed value fails if its second argument is already bound. This is captured by the following rule.

$$[\text{FAILF}] \frac{\{\text{FailedValue } x \ y\} \parallel \mathbf{raise\ error}(\text{failC}(y)) \mathbf{end}}{\sigma \models y \neq \perp} \parallel \frac{}{\sigma} \quad z \notin \text{dom}(\sigma)$$

## Strictness check

$$[\text{STRICTF}] \frac{\{\text{IsStrict } x \ y\} \parallel \mathbf{raise\ error}(\text{strict}(y)) \mathbf{end}}{\sigma \models y \neq \perp} \parallel \frac{}{\sigma} \text{ if } \text{strict}_\sigma(x)$$

## Gate abstraction

Creating a gate fails if the argument is already bound. This is captured by the following rule.

$$[\text{NEWGF}] \frac{\{\text{NewGate } x\} \parallel \mathbf{raise\ error}(\text{newG}(x)) \mathbf{end}}{\sigma \models x \neq \perp} \parallel \frac{}{\sigma}$$

Creating a subordinate gate fails if the first element of the argument pair is not a gate or if the second element is already bound. This is captured by the following rule.

$$[\text{NEWGSF}] \frac{\{\text{NewGate } x \# z\} \parallel \mathbf{raise\ error}(\text{newGS}(x \ y)) \mathbf{end}}{\sigma} \parallel \frac{}{\sigma} \text{ if } C$$

where

$$C \equiv (\sigma \models x = v \wedge \sigma \not\models v : \text{gate}) \vee (z \neq \perp)$$

Sending a message fails if the first argument of the `Send` operation is not a gate. This is captured by the following rule.

$$[\text{SENDF}] \frac{\{\text{Send } g \ x\} \parallel \mathbf{raise\ error}(\text{send}(g)) \mathbf{end}}{\sigma} \parallel \frac{}{\sigma} \text{ if } C$$

where

$$C \equiv (\sigma \models g = v \wedge \sigma \not\models v : \text{gate}) \vee ()$$

Receiving a message fails if the first argument of the `Receive` operation is not a gate, or if the second argument is already bound. This is captured by the following rule.

$$[\text{RECEIVEF}] \frac{\frac{\{\text{Receive } g \ x\}}{\sigma} \parallel \frac{\mathbf{raise\ error}(\text{receive}(g \ x)) \ \mathbf{end}}{\sigma}}{\sigma}}{\sigma} \text{ if } C$$

where

$$C \equiv (\sigma \models g = v \wedge \sigma \not\models v : \text{gate}) \vee (x \neq \perp)$$

### Opening and closing

The `Open` and `Close` operation fail if their arguments are not of the correct type. This is captured by the rules below.

$$[\text{OPENF}] \frac{\frac{\{\text{Open } k \ g\} \mid_{\kappa}}{\sigma} \parallel \frac{\mathbf{raise\ error}(\text{open}(k \ g)) \ \mathbf{end}}{\sigma}}{\sigma}}{\sigma} \text{ if } C$$

where

$$\begin{aligned} C \equiv & \sigma \not\models \exists \kappa', \pi, w, k = \kappa' \wedge \kappa' : \mathbf{kell}(\pi, w) \\ & \vee \sigma \not\models \exists \gamma, g = \gamma \wedge \gamma : \text{gate} \\ & \vee \sigma \models k = \kappa' \wedge \kappa' : \mathbf{kell}(\pi, w) \wedge \neg \mathbf{in}(\kappa, \kappa') \end{aligned}$$

$$[\text{CLOSEF}] \frac{\frac{\{\text{Close } k \ g\} \mid_{\eta}}{\sigma} \parallel \frac{\mathbf{raise\ error}(\text{close}(k \ g)) \ \mathbf{end}}{\sigma}}{\sigma}}{\sigma} \text{ if } C$$

where

$$\begin{aligned} C \equiv & \sigma \not\models \exists \kappa', \pi, w, k = \kappa' \wedge \kappa' : \mathbf{kell}(\pi, w) \\ & \vee \sigma \not\models \exists \gamma, g = \gamma \wedge \gamma : \text{gate} \\ & \vee \sigma \models k = \kappa' \wedge \kappa' : \mathbf{kell}(\pi, w) \wedge \neg \mathbf{in}(\kappa, \kappa') \end{aligned}$$

### Kell abstraction

Kell creation fails if the kell name argument is already bound. This is captured by the rule below.

$$[\text{KNEWF}] \frac{\frac{\mathbf{kell}\{y\} \ S \ \mathbf{end}}{\sigma \models y \neq \perp} \parallel \frac{\mathbf{raise\ error}(\mathbf{kNew}(y)) \ \mathbf{end}}{\sigma}}{\sigma}}{\sigma}$$

Kell replacement fails if the kell name argument does not denote a packed subkell of the current kell. This is captured by the rule below.

$$[\text{KREPF}] \frac{\frac{\mathbf{kell}\{y\} \ S \ \mathbf{end} \mid_{\kappa}}{\sigma \models \neg \phi} \parallel \frac{\mathbf{raise\ error}(\mathbf{kRep}(y)) \ \mathbf{end}}{\sigma}}{\sigma}}{\sigma}$$

where

$$\phi \equiv \exists \kappa', w, y = \kappa' \wedge w = \mathbf{packed} \wedge \mathbf{in}(\kappa, \kappa')$$

### Packed values

The failure rule for gate replacement is given below.

$$[\text{MARKGF}] \frac{\frac{\{\text{Mark } z \ \text{gate}(x \ y) \ p\}}{\sigma} \parallel \frac{\mathbf{raise\ error}(\mathbf{markG}(z \ x \ y \ p)) \ \mathbf{end}}{\sigma}}{\sigma}}{\sigma} \text{ if } \neg C$$

where

$$C \equiv (\sigma \models p = \perp \wedge \exists \kappa, \mathcal{T}, \sigma', \mu, z = \text{pack}(\kappa, \mathcal{T}, \sigma', \mu)) \wedge \phi$$

$$\phi \equiv \exists \gamma, \gamma', \sigma \models x = \gamma \wedge \gamma : \text{gate} \wedge y = \gamma' \wedge \gamma' : \text{gate} \wedge \sigma' \models \gamma : \text{gate}$$

The failure rule for procedure replacement is given below.

$$[\text{MARKPF}] \frac{\frac{\{\text{Mark } z \text{ proc}(x \ y) \ p\}}{\sigma} \parallel \frac{\{\text{raise error}(\text{markP}(z \ x \ y \ p)) \ \text{end}\}}{\sigma}}{\sigma}}{\sigma} \text{ if } \neg C$$

where

$$C \equiv (\sigma \models p = \perp \wedge \exists \kappa, \mathcal{T}, \sigma', \mu, z = \text{pack}(\kappa, \mathcal{T}, \sigma', \mu)) \wedge \phi$$

$$\phi \equiv \exists \eta, \zeta, \tilde{X}, S, S' \ x = \eta \wedge \eta : \text{proc}\{\$ \tilde{X}\}S \ \text{end} \wedge y = \zeta \wedge \zeta : \text{proc}\{\$ \tilde{X}\}S' \ \text{end} \wedge \sigma' \models \eta : \text{proc}\{\$ \tilde{X}\}S \ \text{end}$$

### Packing

The failure rule for packing is given below.

$$[\text{PACKF}] \frac{\frac{\{\text{Pack } x \ y\} \mid_{\kappa} \ \mathcal{T}}{\sigma} \parallel \frac{\{\text{raise error}(\text{pack}(x \ y)) \ \text{end}\} \ \mathcal{T}}{\sigma}}{\sigma}}{\sigma} \text{ if } \neg C$$

where

$$\mathcal{T} \equiv \tau_1 : \mathcal{T}_1 \ \dots \ \tau_n : \mathcal{T}_n$$

$$C \equiv \exists \kappa_0, \pi_0, w_0, \dots, \kappa_m, \pi_m, w_m \ (\sigma \models \bigwedge_{i=0}^m \phi_i \wedge \phi) \wedge \text{subth}_{\sigma}(\kappa_0, \{\tau_1, \dots, \tau_n\}) \wedge \text{subk}_{\sigma}(\kappa_0, \{\kappa_1, \dots, \kappa_m\})$$

$$\phi \equiv x = \kappa_0 \wedge y = \perp \wedge \text{in}(\kappa, \kappa_0)$$

$$\phi_i \equiv \kappa_i : \text{kell}(\pi_i, w_i) \wedge w_i = \perp$$

The failure rule for unpacking is given below.

$$[\text{UNPACKF}] \frac{\frac{\{\text{Unpack } y \ x\} \mid_{\kappa}}{\sigma} \parallel \frac{\{\text{raise error}(\text{pack}(y \ x)) \ \text{end}\}}{\sigma}}{\sigma}}{\sigma} \text{ if } \neg C$$

where

$$C \equiv \exists \kappa', \mathcal{T}, \sigma', \mu, \pi, z, \pi', z', \sigma \models \kappa : \text{kell}(\pi, z) \wedge x = \perp \wedge y = \text{pack}(\kappa', \mathcal{T}, \sigma', \mu) \wedge \sigma' \models \kappa' : \text{kell}(\pi', z')$$

## C Proofs

We gather in this section proofs of the properties in Section 5.2. We first have a few auxiliary lemmas.

**Lemma 1** *Let  $\sigma$  be a store,  $S$  and  $S'$  be statements such that  $S \equiv S'$ , and  $\theta$  a substitution on variables and names such that  $\text{ran}(\theta) \cap \text{v}(S, \sigma) = \emptyset$ . Then,  $S\theta \equiv S'\theta$ .*

**Proof:** By induction on the derivation of  $S \equiv S'$ . In the case of rule S. $\alpha$ ,  $S =_{\alpha} S'$ , then  $S\theta =_{\alpha} S'\theta$ , and hence  $S\theta \equiv S'\theta$ . In the case of rule S.SEQ  $S = S_1 ; S_2$  and  $S' = S'_1 ; S'_2$ , with  $S_1 \equiv S'_1$  and  $S_2 \equiv S'_2$ , then by induction  $S_1\theta \equiv S'_1\theta$  and  $S_2\theta \equiv S'_2\theta$ , and thus  $S\theta \equiv S'\theta$ . Other cases are similar to that of rule S.SEQ.  $\square$

**Lemma 2** *Let  $\sigma$  be a store,  $T$  and  $T'$  be tasks such that  $T \equiv T'$ , and  $\theta$  a substitution on variables and names such that  $\text{ran}(\theta) \cap \text{v}(T, \sigma) = \emptyset$ . Then,  $T\theta \equiv T'\theta$ .*

**Proof:** By induction on the derivation of  $T \equiv T'$ , using Lemma 1 for the case of rule T.THREAD.  $\square$

**Lemma 3** *Let  $\sigma$  and  $\sigma'$  be stores such that  $\sigma \equiv \sigma'$ , and  $\theta$  a substitution on variables and names such that  $\text{ran}(\theta) \cap \text{dom}(\sigma) = \emptyset$ . Then,  $\sigma\theta \equiv \sigma'\theta$ .*

**Proof:** By induction on the derivation of  $\sigma \equiv \sigma'$ , using Lemma 1 and Lemma 2 for the case of rule E.PACK, and Lemma 1 for the case of rule E.PROC.  $\square$

**Lemma 4** *Let  $\sigma$  be a store,  $v, v'$  be values such that  $v \equiv_{\sigma} v'$ , and  $\theta$  be a substitution on variables and names such that  $\text{ran}(\theta) \cap \text{dom}(\sigma) = \emptyset$ . Then,  $v\theta \equiv_{\sigma\theta} v'\theta$ .*

**Proof:** By induction on the derivation of  $v \equiv_{\sigma} v'$ , using Lemma 3 and Lemma 2 for the case of rule EQ.PACK.  $\square$

**Lemma 5** *Let  $\sigma$  be a store,  $v, v'$  be values such that  $v \bowtie_{\sigma} v'$ , and  $\theta$  be a substitution on variables and names such that  $\text{ran}(\theta) \cap \text{dom}(\sigma) = \emptyset$ . Then,  $v\theta \bowtie_{\sigma\theta} v'\theta$ .*

**Proof:** By induction on the derivation of  $v \bowtie_{\sigma} v'$ , using Lemma 3 and Lemma 2 for the case of rule DIS.PACKD.  $\square$

**Lemma 6** *Let  $\sigma$  be a valid store, and let  $\theta$  be a substitution on variables and names such that  $\text{ran}(\theta) \cap \text{dom}(\sigma) = \emptyset$ . Then  $\sigma\theta$  is a valid store.*

**Proof:** The proof proceeds by contradiction. Assume that  $\sigma\theta$  is invalid. Then one of the properties in the definition of store invalidity must hold. Assume for instance (dealing with other properties is similar) that the first property holds, i.e.  $\exists x, v, v', v \bowtie_{\sigma} v' \wedge \sigma \models x = v \wedge x = v'$ . Since  $\text{ran}(\theta) \cap \text{dom}(\sigma) = \emptyset$ , there exists  $\theta'$  such that  $\text{ran}(\theta') \cap \text{dom}(\sigma\theta) = \emptyset$  and  $\sigma\theta\theta' = \sigma$ . Now  $\sigma\theta \models x = v \wedge x = v'$  implies  $\sigma\theta \equiv \sigma' \wedge x = v \wedge x = v'$  for some  $\sigma'$ , and thus  $\sigma \equiv \sigma'\theta' \wedge y = w \wedge y = w'$ , where  $y = x\theta'$ ,  $w = v\theta'$ , and  $w' = v'\theta'$ . Now, by Lemma 5,  $w \bowtie_{\sigma} w'$ , and hence  $\sigma$  is not valid, a contradiction.  $\square$

**Lemma 7** *Let  $\sigma, \sigma'$  be stores such that  $\sigma \equiv \sigma'$ . If  $\sigma$  is valid, then  $\sigma'$  is valid.*

**Proof:** By induction on the derivation of  $\sigma \equiv \sigma'$ . For the cases E.PACK, E.PROC, and E.EQUAL, the result is immediate, by definition of store validity. For the case E.ENTAILS, the result is obtained reasoning by contradiction and using E.TRANS.  $\square$

**Lemma 8** *Let  $\sigma_1$  and  $\sigma_2$  be valid stores, and  $\theta$  be a substitution such that  $\text{ran}(\theta) \cap \text{dom}(\sigma_1, \sigma_2) = \emptyset$ . Then  $\sigma_1 \wedge \sigma_2\theta$  is a valid store.*

**Proof:** Immediate since  $\sigma_2\theta$  is valid by Lemma 6, and  $\text{dom}(\sigma_1) \cap \text{dom}(\sigma_2\theta) = \emptyset$ .  $\square$

**Lemma 9** *Let  $\langle \sigma, T \rangle$  execution structure that results from the execution of an OZ/K statement. Then  $\sigma$  is valid.*

**Proof:** By induction on the length of the reduction that leads to  $\langle \sigma, T \rangle$ . In the base case, we have  $\sigma_0 \equiv \sigma$ , where  $\sigma = \tau : \mathbf{thread}(w) \wedge w \wedge \mathbf{read}(w) \wedge \mathbf{inth}(\top, \tau)$ . Since  $\sigma$  is trivially valid, so is  $\sigma_0$  by Lemma 7. Assume that  $(\sigma_0, T_0) \rightarrow \dots \rightarrow (\sigma_n, T_n) \rightarrow (\sigma, T)$ , with  $\sigma_j$  valid for  $0 \leq j \leq n$ . We reason by induction on the derivation that has been used to obtain  $(\sigma_n, T_n) \rightarrow (\sigma, T)$ :

- [PAR] In this case,  $T_n = \mathcal{U} \mathcal{V}, T = \mathcal{U}' \mathcal{V}$ , and  $(\sigma_n, \mathcal{U}) \rightarrow (\sigma, \mathcal{U}')$ . By induction, we have  $\sigma$  valid, as required.
- [EQUIV] In this case,  $T_n \equiv \mathcal{U}_n, T \equiv \mathcal{U}, \sigma_n \equiv \sigma'_n, \sigma \equiv \sigma'$ , and  $(\sigma'_n, \mathcal{U}_n) \rightarrow (\sigma', \mathcal{U})$ . By induction,  $\sigma'$  is valid, and hence  $\sigma$  is valid by Lemma 7.
- Failure rules [THREADF] to [UNPACKF]: in these cases, we have  $\sigma_n = \sigma$ , hence  $\sigma$  is valid, as required.
- [SKIP], [SEQTH], [UNIF], [IFTRUE], [IFFALSE], [CASE], [CASEU], [PCALL], [TRYU], [TRYC], [RAISEW], [RAISE], [WAITN], [FAILW] In these cases, we have  $\sigma_n = \sigma$ , hence  $\sigma$  is valid, as required.
- [NIL] In this case,  $\sigma_n \models x = \perp$ , for some  $x$ , and  $\sigma = \sigma_n \wedge x = \mathbf{terminated}$ . Since  $\sigma_n$  is valid, by induction, the only possibility to make  $\sigma$  invalid would be if (1)  $\sigma \models x = v \wedge x = v'$ , with  $v \not\bowtie_\sigma v'$ . But since  $\sigma_n \models x = \perp$ , we have only  $\sigma \models x = \mathbf{terminated}$ , and hence property (1) does not hold. Hence  $\sigma$  is valid, as required.
- [NEWT] In this case,  $\sigma_n \models x = \perp \wedge \mathbf{inth}(\kappa, \tau)$ , for some  $x, \kappa, \tau$ , and  $\sigma = \sigma_n \wedge x = \tau' \wedge \tau' : \mathbf{thread}(w) \wedge w \wedge \mathbf{read}(w) \wedge \mathbf{inth}(\kappa, \tau')$ , with  $\tau', w \notin \mathbf{dom}(\sigma_n)$ . By induction,  $\sigma_n$  is valid. For  $\sigma$  to be invalid one of the following properties from the definition of store invalidity must hold: (1) with  $x, \tau'$ , (3) with  $\tau', w$ , (6) with  $\kappa, \tau'$ , or (8) with  $\kappa, \tau'$ . Now: (1) does not hold since  $\sigma_n \models x = \perp$ ,  $\sigma_n$  valid; (3) does not hold since  $\sigma_n$  valid and  $\sigma \models \tau' : \mathbf{thread}(w) \wedge \mathbf{read}(w)$ ; (6) does not hold since  $\sigma_n$  valid and  $\tau'$  fresh; (8) does not hold since  $\sigma_n$  valid (and thus  $\sigma_n \models \kappa : \mathbf{cell}(\pi, z)$  for some  $\pi, z$ ), and  $\sigma \models \tau' : \mathbf{thread}(w)$ . Hence  $\sigma$  is valid, as required.
- [VAR] Immediate since in this case  $\sigma = \sigma_n \wedge x_1 \wedge \dots \wedge x_n$  with  $x_i$  fresh, and  $\sigma_n$  is valid by induction.
- [READ] Immediate since in this case  $\sigma = \sigma_n \wedge \mathbf{read}(x, y)$ , for some  $y$ ,  $\sigma_n$  valid by induction, and the added assertion  $\mathbf{read}(x, y)$  does not change the properties required for store invalidity.
- [READU] In this case,  $\sigma_n = \sigma' \wedge \mathbf{read}(z, y), \sigma = \sigma' \wedge z = y$ , and  $\sigma_n \models y \neq \perp$ , for some  $y, z$ . Now  $\sigma_n$  is valid by induction, and the added store assertion  $z = y$  cannot invalidate store validity. Indeed, if  $\sigma$  were invalid, this would be because  $\sigma_n \models x = z \wedge x \neq \perp$  for some  $x$  (property (1) in the definition of store invalidity). But this is only possible if the rule [BINDXY], and the rule [BINDV] or the rule [BINDR] have been used in the chain of reduction leading to  $(\sigma_n, T_n)$ , with statements of the form  $x = z, x = v$  or  $x = r.f$ , for some value  $v$ , and variables  $r, f$ , respectively. Assume that rule [BINDXY] has been used at step  $j$  with statement  $x = z$ , followed by rule [BINDV] at step  $k \geq j$  with statement  $x = v$ . In this case we would have  $\sigma_j \models \mathbf{read}(x)$  following the conditions with [BINDXY]. But then, since the assertion  $\mathbf{read}(x)$  is not erased by any rule, rule [BINDV] could not be applied at step  $k$  in the reduction chain. The other cases are likewise impossible, which leads to a contradiction. Hence  $\sigma$  is valid, as required.
- [UNI] In this case, we have  $\sigma = \sigma_n \wedge \mathbf{Unify}(x, y, \sigma_n)$  for some  $x, y$ . This case is handled similarly to the case [READU] above, noting that the added store assertions in  $\mathbf{Unify}(x, y, \sigma_n)$  do not invalidate store validity.
- [BINDV] In this case, we have  $\sigma = \sigma_n \wedge x = v, \sigma_n \models x = \perp \wedge \neg \mathbf{rread}(x)$ , for some variable  $x$  and value  $v$ . Now  $\sigma_n$  is valid by induction, and since  $\sigma_n \models x = \perp$ , the addition of assertion  $x = v$  does not make property (1) of store invalidity true. Hence  $\sigma$  is valid, as required.
- [BINDXY] In this case, we have  $\sigma = \sigma_n \wedge \sigma'$ , where  $\sigma'$  can be any of five possibilities. We handle the first one, the other ones are handled similarly. We have in this case  $\sigma' \equiv y = v$  and  $\sigma_n \models x = v \wedge y = \perp \wedge \neg \mathbf{rread}(y)$ , for some  $x, y, v$ . No, since  $\sigma_n$  is valid by induction, and  $\sigma_n \models x = v \wedge y = \perp$ , the assertion  $y = v$  does not make property (1) of store invalidity true. Hence  $\sigma$  is valid, as required.
- [BINDXY] [EQTRUE] [EQFALSE] [STATUS] [NEWNAME] These cases are similar to case [BINDV].
- [PNEW] In this case, we have  $\sigma = \sigma_n \wedge x = \xi \wedge \xi : \mathbf{proc}\{\$ X_1 \dots X_n\} S \mathbf{end}, \sigma_n \models x = \perp$ , for some  $x, X_i, S$ , with  $\xi \notin \mathbf{dom}(\sigma_n)$ . Since  $\sigma_n$  is valid by induction and  $\sigma_n \models x = \perp$ , the addition of assertion  $x = \xi$  does not make property (1) of store invalidity true. Furthermore, since  $\xi$  is fresh and only the assertion  $\xi : \mathbf{proc}\{\$ X_1 \dots X_n\} S \mathbf{end}$  is added, property (2) of store invalidity does not hold either. Hence  $\sigma$  is valid, as required.
- [PREP] In this case, we have  $\sigma_n = \sigma' \wedge \xi : Q$  and  $\sigma = \sigma' \wedge \xi : P$ , for some closures  $Q, P$ . Since  $\sigma_n$  is valid by induction, property (2) of store invalidity does not hold for  $\sigma_n$ , nor for  $\sigma$ . Hence,  $\sigma$  is valid, as required.

- [DETRUE] [DETFALSE] These cases are similar to case [BINDV].
- [NCELL] This case is similar to case [PNEW].
- [ECELL] In this case, we have  $\sigma_n = \sigma' \wedge \xi : \text{cell}(t)$ ,  $\sigma = \sigma' \wedge \xi : \text{cell}(z) \wedge y = t$ ,  $\sigma_n \models y = \perp$ , for some  $y, z, t$ . By induction,  $\sigma_n$  is valid, hence  $\sigma' \wedge \xi : \text{cell}(z)$  is valid. And since  $\sigma_n \models y = \perp$ , the addition of the assertion  $y = t$  does not make property (1) of store invalidity true. Hence  $\sigma$  is valid, as required.
- [RAISES] This case is similar to case [BINDV].
- [NEED] [NEEDD] Immediate since  $\sigma_n$  is valid by induction, and  $\sigma = \sigma_n \wedge \text{need}(x)$  for some  $x$ , and the addition of assertion  $\text{need}(x)$  does not make any property of store invalidity true.
- [FAILC] [STRICTTRUE] [STRICTFALSE] [NEWG] [NEWGS] [COM] These cases are similar to case [BINDV].
- [OPEN] [CLOSE] In this cases, we have  $\sigma_n = \sigma' \wedge \kappa : \text{kell}(\pi, x)$ ,  $\sigma = \sigma' \wedge \kappa : \text{kell}(\pi', x)$ , for some  $\kappa, \pi, \pi', x$ . Since  $\sigma_n$  is valid by induction, the change from  $\pi$  to  $\pi'$  does not make any of the properties (2), (4), (6)-(8) of store invalidity true. Hence,  $\sigma$  is valid, as required.
- [NEWKELL] In this case, we have  $\sigma = \sigma_n \wedge y = \kappa \wedge \kappa : \text{kell}(\emptyset, w) \wedge w \wedge \text{read}(w) \wedge \tau : \text{thread}(r) \wedge r \wedge \text{read}(r) \wedge \text{inth}(\kappa, \tau) \wedge \text{in}(\kappa', \kappa)$ ,  $\sigma_n \models y = \perp \wedge \kappa' : \text{kell}(\pi, z)$ , for some  $\kappa, \kappa', \tau, y, w, r, \pi, z$ , with  $\kappa, \tau, w, r \notin \text{dom}(\sigma_n)$ . Now,  $\sigma_n$  is valid by induction. Since  $\sigma_n \models y = \perp$ , then the addition of the assertion  $y = \kappa$  does not make property (1) of store invalidity true. Likewise, since  $\kappa, \tau, w, r \notin \text{dom}(\sigma_n)$ , property (2) of store invalidity remains false; since  $\sigma \models \tau : \text{thread}(r) \wedge r \wedge \text{read}(r)$ , property (3) of store invalidity remains false; since  $\sigma \models \kappa : \text{kell}(\emptyset, w) \wedge w \wedge \text{read}(w)$ , property (4) remains false; since  $\kappa, \tau \notin \text{dom}(\sigma_n)$  and only the assertions  $\text{inth}(\kappa, \tau) \wedge \text{in}(\kappa', \kappa)$  are added, properties (5), (6) and (9) of store invalidity remain false; since  $\sigma \models \kappa : \text{kell}(\emptyset, w) \wedge \tau : \text{thread}(r) \wedge \text{inth}(\kappa, \tau)$ , property (8) of store invalidity remains false. Hence  $\sigma$  is valid, as required; finally, since  $\sigma \models \kappa' : \text{kell}(\pi, z) \wedge \kappa : \text{kell}(\emptyset, w) \wedge \text{in}(\kappa', \kappa)$ , property (7) of store invalidity also remains false. Hence  $\sigma$  is valid, as required.
- [KREP] Similar to the case [NEWKELL].
- [MARKG] [MARKP] [PACK] Similar to the case [BINDV].
- [UNPACK] Immediate, since  $\sigma$  valid is a condition for the application of the rule.

□

**Lemma 10** *The set of kells in an execution structure that results from the execution of an OZ/K statement forms a tree, with root  $\top$ .*

**Proof:** By Lemma 9 and the definition of store validity. □

**Lemma 11** *Let  $(\sigma, \mathcal{T})$  and  $(\sigma', \mathcal{T}')$  be execution structures such that  $\sigma \equiv \sigma'$  and  $\mathcal{T} \equiv \mathcal{T}'$ . Then  $\mathfrak{v}(\mathcal{T}, \sigma) = \mathfrak{v}(\mathcal{T}', \sigma')$ .*

**Proof:** We first show by induction on the derivation of the statement  $\mathcal{T} \equiv \mathcal{T}'$  that  $\mathfrak{v}(\mathcal{T}, \sigma) = \mathfrak{v}(\mathcal{T}', \sigma)$ . We then show by induction on the derivation of the statement  $\sigma \equiv \sigma'$  that  $\mathfrak{v}(\mathcal{T}', \sigma) = \mathfrak{v}(\mathcal{T}', \sigma')$ . □

**Proposition 1** *Assume  $(\sigma, \mathcal{T})$ , with  $\mathcal{T} \equiv \mathcal{T}_1 \ \mathcal{T}_2 \ \mathcal{T}'$ , is an execution structure that result from the execution of an OZ/K statement, where  $\mathcal{T}_1$  belongs to kell  $\kappa_1$ ,  $\mathcal{T}_2$  belongs to kell  $\kappa_2$ , and  $\kappa_1 \neq \kappa_2$ . If  $\sigma \models x = \perp$ , and  $x \in \mathfrak{v}(\mathcal{T}_1, \sigma)$ , then  $x \notin \mathfrak{v}(\mathcal{T}_2, \sigma)$ .*

**Proof:** We reason by induction on the length of the reduction to  $(\sigma, \mathcal{T})$ . We actually prove a stronger property,  $(P)$ , which is the conjunction of the following properties:

1. if  $\sigma \models x = \perp$ , and  $x \in \mathfrak{v}(\mathcal{T}_1, \sigma)$ , then  $x \notin \mathfrak{v}(\mathcal{T}_2, \sigma)$ .
2. if  $\sigma \models x = \xi \wedge \xi : \text{cell}(t)$ , and  $x \in \mathfrak{v}(\mathcal{T}_1, \sigma)$ , then there exists no  $y$  such that  $\sigma \models y = \xi$  and  $y \in \mathfrak{v}(\mathcal{T}_2, \sigma)$ .
3. if  $\sigma \models x = \tau \wedge \tau : \text{thread}(t)$ , and  $x \in \mathfrak{v}(\mathcal{T}_1, \sigma)$ , then there exists no  $y$  such that  $\sigma \models y = \tau$  and  $y \in \mathfrak{v}(\mathcal{T}_2, \sigma)$ .
4. if  $\sigma \models x = \kappa \wedge \kappa : \text{kell}(\pi, t)$ , and  $x \in \mathfrak{v}(\mathcal{T}_1, \sigma)$ , then there exists no  $y$  such that  $\sigma \models y = \kappa$  and  $y \in \mathfrak{v}(\mathcal{T}_2, \sigma)$ .

5. if  $\sigma \models x = \xi \wedge \xi : \text{proc}\{\$ \tilde{X}\}S \text{ end}$ , and  $x \in v(\mathcal{T}_1, \sigma)$ , then if there exists  $y$  such that  $\sigma \models y = \xi$  and  $y \in v(\mathcal{T}_2, \sigma)$ , we have  $\text{strict}_\sigma(S, \emptyset)$ .

We first note that we can replace the condition ‘there exists no  $y$  such that  $\sigma \models y = \eta$  and  $y \in v(\mathcal{T}_2, \sigma)$ ’ in the definition of the property above by the condition ‘ $x \notin v(\mathcal{T}_2, \sigma)$ ’. Indeed, cells, threads, kells, and procedures are objects created through explicit creation operations (given by rules [NCELL], [NEWT], [PNEW], [NEWKELL], respectively), that bind the fresh name  $\eta$  of the newly created object to a single variable. Now, all relevant binding operations in the language, given by rules [READU], [BINDXY], [BINDR], [UNI], and [COM] proceed by adding bindings of the form  $z = z'$  to the store, where  $z, z'$  are variables. Thus a simple induction, using rule [E.EQUALT] shows that to obtain  $\sigma \models x = \eta \wedge y = \eta$ , where  $x$  and  $y$  are two distinct variables, one must have  $\sigma \models x = y$ . Thus, if  $y \in v(\mathcal{T}_2, \sigma)$ , we must have, by definition of variables of a task relative to a store,  $x \in v(\mathcal{T}_2, \sigma)$ .

The base case is immediate since there only one thread in the initial execution structure. Assume the property holds for  $n$ , and let  $(\sigma_n, \mathcal{T}_n) \rightarrow (\sigma, \mathcal{T})$ . Without loss of generality, we can consider that the derivation of the reduction  $(\sigma_n, \mathcal{T}_n) \rightarrow (\sigma, \mathcal{T})$  has been obtained through an application of one of the rules in Section 5.1, except [PAR] and [EQUIV], or one of the rules in Appendix B (base rules); an application of [PAR]; and an application of [EQUIV]. The application of rule [EQUIV] is handled immediately thanks to the remark above and Lemma 11. Hence we can consider without loss of generality that  $(\sigma_n, \mathcal{T}_n) \rightarrow (\sigma, \mathcal{T})$  is obtained by an application of a base rule followed by an application of [PAR]. We consider the different base rules in turn.

Failure rules are easily handled since they leave the store unchanged and modify only a single thread. Since  $(P)$  holds for  $(\sigma_n, \mathcal{T}_n)$  by induction assumption, then  $(P)$  holds for  $(\sigma, \mathcal{T})$ . Base rules that leave the store unchanged and modify only a single thread are handled similarly: they are [SKIP], [SEQTH], [UNIF], [IFTRUE], [IFFALSE], [CASE], [CASEU], [PCALL], [TRYU], [TRYC], [RAISEW], [RAISE], [WAIT], [FAILW]. We now consider the remaining base rules:

- [NIL] We have  $\sigma_n \models \tau : \text{thread}(x) \wedge x = \perp, \mathcal{T}_n = \tau \langle \rangle \mathcal{U}, \sigma = \sigma_n \wedge x = \text{terminated}, \mathcal{T} = \mathcal{U}$ . Since  $(P)$  holds of  $(\sigma_n, \tau \langle \rangle \mathcal{U})$  by induction assumption,  $(P)$  holds also of  $(\sigma_n \wedge x = \text{terminated}, \mathcal{U})$  for  $v(\tau \langle \rangle, \sigma_n) = \emptyset$ . Hence  $(P)$  holds of  $(\sigma, \mathcal{T})$ , as required.
- [NEWT] We have  $\sigma_n \models x = \perp \wedge \text{inth}(\kappa, \tau), \mathcal{T}_n = \tau \langle \text{thread}\{x\} S \text{ end } T \rangle \mathcal{U}, \sigma = \sigma_n \wedge x = \tau' \wedge \tau' : \text{thread}(w) \wedge w \wedge \text{read}(w) \wedge \text{inth}(\kappa, \tau'), \mathcal{T} = \tau : T \tau' : \langle S' \rangle \mathcal{U}$ , with  $w$  fresh. Now, we have  $v(\tau : T \tau' : \langle S' \rangle, \sigma) = v(\tau \langle \text{thread}\{x\} S \text{ end } T \rangle, \sigma_n) \cup \{w\}$ . Since  $(P)$  holds of  $(\sigma_n, \tau \langle \rangle \mathcal{U})$  by induction assumption, we need only check whether clause 1 and clause 3 of  $(P)$  hold of  $(\sigma, \mathcal{T})$ . Since  $\sigma_n \models x = \perp$ ,  $x$  does not belong to the variables of any thread in  $\mathcal{U}$  that is not in  $\text{kell } \kappa$ , hence clause 3 of  $(P)$  holds  $(\sigma, \mathcal{T})$ . Also,  $w$  is fresh, and is only reachable through  $x$ , hence  $w$  does not belong to the variables of any thread in  $\mathcal{U}$  that is not  $\kappa$ . Hence  $(P)$  holds of  $(\sigma, \mathcal{T})$ , as required.
- [VAR] We have  $\sigma = \sigma_n \wedge x_1 \wedge \dots \wedge x_n, \mathcal{T}_n = \tau : T \mathcal{U}, \mathcal{T} = \tau : T' \mathcal{U}$ , with  $x_i$  fresh, and  $x_i$  reachable only from  $\tau : T'$ . Hence, since  $(P)$  holds of  $(\sigma_n, \mathcal{T}_n)$  by induction, it holds also of  $(\sigma, \mathcal{T})$ , as required.
- [READ] This case is similar to [VAR].
- [BINDV] We have  $\sigma_n \models x = \perp \wedge \neg \text{read}(x), \mathcal{T}_n = \tau \langle x = v \ T \rangle \mathcal{U}, \mathcal{T} = \tau : T \mathcal{U}, \sigma = \sigma_n \wedge x = v$ . We have  $v(\tau : T, \sigma) \subseteq v(\tau \langle x = v \ T \rangle, \sigma_n)$ , and  $(P)$  holds of  $(\sigma_n, \mathcal{T}_n)$  by induction. Hence  $(P)$  holds of  $(\sigma, \mathcal{T})$ , as required.
- [BINDXY], [BINDR], [UNI], [EQTRUE], [EQFALSE], [STATUS] These cases are similar to [BINDV].
- [NEWNAME], [PNEW] These cases are similar to [NEWT].
- [PREP] We have  $\sigma_n = \sigma' \wedge \xi : Q, \sigma' \models x = \xi, \mathcal{T}_n = \tau : T \mathcal{U}, \mathcal{T} = \tau : T' \mathcal{U}, \sigma = \sigma' \wedge \xi : P$ , with  $\text{strict}_{\sigma'}(P, \emptyset)$ . By induction,  $(P)$  holds of  $(\sigma_n, \mathcal{T}_n)$ , and in particular clause 5 of  $(P)$  in relation with  $x$  and  $\xi$ . Since  $\text{strict}_{\sigma'}(P, \emptyset)$ , we have  $\text{strict}_\sigma(P, \emptyset)$ , and hence  $(P)$  holds of  $(\sigma, \mathcal{T})$ , as required.
- [DETTRUE], [DETFALSE] Similar to [BINDV].
- [NCELL] Similar to [NEWT].
- [ECELL], [RAISES] Similar to [BINDV].
- [NEED] We have  $\sigma_n \not\models \text{need}(x), \sigma = \sigma_n \wedge \text{need}(x), \mathcal{T}_n = \mathcal{T}$ . Since  $(P)$  holds of  $(\sigma_n, \mathcal{T}_n)$  by induction, it holds of  $(\sigma, \mathcal{T})$ , as required.
- [NEEDD] Similar to [NEED].
- [FAILC], [STRICTTRUE], [STRICTFALSE] Similar to [BINDV].
- [NEWG], [NEWGS] Similar to [NEWT].

- [COM] We have  $\sigma_n \models y = \perp \wedge g = \gamma \wedge h = \gamma \wedge \gamma : \mathbf{gate} \wedge \mathbf{inlh}(\kappa, \tau) \wedge \mathbf{inlh}(\kappa', \tau'), \mathcal{T}_n = \tau : T \ \tau' : T' \ \mathcal{U}, T = \langle \{\mathbf{Send} \ g \ x\} \ U \rangle, T' = \langle \{\mathbf{Receive} \ h \ y\} \ U' \rangle, \sigma = \sigma_n \wedge y = x, \mathcal{T} = \tau : U \ \tau' : U' \ \mathcal{U},$  with  $\mathbf{strict}_{\sigma_n}(x)$ . By induction  $(P)$  holds of  $(\sigma_n, \mathcal{T}_n)$ . We thus need only check whether  $(P)$  holds with  $\mathcal{T}_1 = \tau : U$  and  $\mathcal{T}_2 = \tau : U'$ . But this is immediate since  $\mathbf{strict}_{\sigma_n}(x)$ . Hence  $(P)$  holds of  $(\sigma, \mathcal{T})$ , as required.
- [OPEN], [CLOSE] Similar to [NEED].
- [NEWKELL] Similar to [NEWTHT], thanks to the condition  $\mathbf{strict}_{\sigma}(S, \{y\})$ .
- [KREP] Similar to [NEWKELL].
- [MARKG], [MARKP] Similar to [BINDV].
- [PACK] We have  $\sigma_n \models x = \kappa_0 \wedge y = \perp \wedge \mathbf{inlh}(\kappa, \tau), \sigma = \sigma_n \wedge y = \mathbf{pack}(\kappa_0, \mathcal{U}, \sigma_n, \emptyset), \mathcal{T}_n = \tau \langle \{\mathbf{Pack} \ x \ y\} \ T \rangle \ \mathcal{U} \ \mathcal{V}, \mathcal{T} = \tau : T \ \mathcal{V}$ . By the definition of variables of a task relative to a store, we have  $\mathbf{v}(\tau : U, \sigma) \subseteq \mathbf{v}(\tau : T, \sigma_n)$ . Since,  $(P)$  holds of  $(\sigma_n, \mathcal{T}_n)$ , we can conclude immediately that it holds for  $(\sigma, \mathcal{T})$ , as required. Also, note that  $(P)$  holds for  $(\sigma_n, \mathcal{U})$ .
- [UNPACK] We have  $\sigma_n \models \kappa : \mathbf{kell}(\pi, z) \wedge x = \perp \wedge \mathbf{inlh}(\kappa, \tau) \wedge y = \mathbf{pack}(\kappa_0, \mathcal{U}, \sigma', \mu), \sigma' = \sigma'' \wedge \kappa_0 : \mathbf{kell}(\pi', z'), \sigma = \sigma_n \wedge \sigma'' \theta \wedge \bigwedge_{\kappa' \in \mathbf{tkn}_{\sigma'}(\mathcal{U})} \mathbf{in}(\kappa, \kappa' \theta) \wedge x = l, \mathcal{T}_n = \tau \langle \{\mathbf{Unpack} \ y \ x\} \ T \rangle \ \mathcal{V}, \mathcal{T} = \tau : T \ \mathcal{U} \theta \ \mathcal{V}$ , where  $l$  is the name list,  $\theta$  is a substitution that replaces  $\kappa_0$  with  $\kappa$ , and that renames all variables and names appearing in  $\sigma'$  with fresh variables and fresh names, respectively. By induction, we have that  $(P)$  holds of  $(\sigma_n, \mathcal{T}_n)$  and of  $(\sigma', \mathcal{U})$ . Since all the names and variables in  $\sigma' \theta$  and  $\mathcal{U} \theta$  are distinct from those in  $\sigma_n$  and  $T, \mathcal{V}$ , and since  $x$  is bound to a list of pairs of gate names (which are strict values),  $(P)$  holds of  $(\sigma, \mathcal{T})$ , as required.

□

**Proposition 2** *Let  $(\mathcal{T} \ \mathcal{T}_\kappa, \sigma)$  be an execution structure that results from the execution of a OZ/K statement, where  $\kappa$  appears at the top level,  $\mathcal{T}_\kappa$  is the set of all threads that belong to  $\kappa$ ,  $\kappa$  is not referenced in  $\mathcal{T}$ , there is no thread  $\tau$  such that  $\sigma \models \mathbf{inlh}(\kappa, \tau)$ , and  $\sigma \equiv \sigma_0 \wedge \kappa : \mathbf{kell}(\emptyset, w)$ , for some  $\sigma_0, w$ . The reductions possible from  $\langle \sigma, \mathcal{T} \ \mathcal{T}_\kappa \rangle$  can only be of one of the following two forms:*

$$\frac{\mathcal{T} \ \mathcal{T}_\kappa \parallel \mathcal{T}' \ \mathcal{T}_\kappa}{\sigma} \quad \text{or} \quad \frac{\mathcal{T} \ \mathcal{T}_\kappa \parallel \mathcal{T} \ \mathcal{T}'_\kappa}{\sigma}$$

where  $\mathcal{T}'_\kappa$  is the set of threads that belong to  $\kappa$  in execution structure  $(\mathcal{T} \ \mathcal{T}'_\kappa, \sigma')$ , and  $\sigma'$  is such that there is no  $\tau$  such that  $\sigma' \models \mathbf{inlh}(\kappa, \tau)$ , and  $\sigma' \equiv \sigma'_0 \wedge \kappa : \mathbf{kell}(\emptyset, w)$ , for some  $\sigma'_0$ .

**Proof:** Because of the assumption  $\sigma \models \kappa : \mathbf{kell}(\emptyset, w)$ , we have  $\mathbf{access}_\sigma(\gamma, \kappa_1, \kappa_2) = \mathbf{false}$  for  $\kappa_i$  such that  $\kappa_i$  is a descendant kell of  $\kappa$ , and  $\kappa_{i \oplus 1}$  is not a descendant of  $\kappa$ . This implies that we cannot apply rule [COM] between a thread in  $\mathcal{T}$  and a thread in  $\mathcal{T}_\kappa$ . Because of the assumption  $\kappa$  not referenced in  $\mathcal{T}$ , we cannot apply rule [PACK] with  $\kappa$  as the target from any thread in  $\mathcal{T} \ \mathcal{T}_\kappa$ . Because of the assumption that there is no thread  $\tau$  such that  $\sigma \models \mathbf{inlh}(\kappa, \tau)$ , we cannot apply any of the rules [OPEN] and [CLOSE] from within  $\kappa$ , and hence  $\sigma' \models \kappa : \mathbf{kell}(\emptyset, w)$ . □



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