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IMELL Cut Elimination with Linear Overhead

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Abstract

Recently, Accattoli introduced the Exponential Substitution Calculus (ESC) given by untyped proof terms for Intuitionistic Multiplicative Exponential Linear Logic (IMELL), endowed with rewriting rules at-a-distance for cut elimination. He also introduced a new cut elimination strategy, dubbed *the good strategy*, and showed that its number of steps is a time cost model with polynomial overhead for ESC/IMELL, and the first such one.

Here, we refine Accattoli's result by introducing an abstract machine for ESC and proving that it implements the good strategy and computes cut-free terms/proofs within a *linear* overhead.

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1 Introduction

One of the advances of the last decade in the theory of *λ*-calculus is the study of reasonable cost models carried out by Accattoli, Dal Lago, and co-authors [14, 15, 10, 17, 19], completing the research program started by Dal Lago and Martini almost 20 years ago [27, 34, 29, 28].

The General Problem. Focusing on time, the underlying problem is whether the number of β -steps (of a fixed evaluation strategy) can be taken as a measure of time that is polynomially equivalent to that of random access machines (RAMs); or, equivalently, to that of Turing machines. This is a priori unclear because there are families of terms suffering of *size explosion*: the size of terms grows exponentially with the number of β -steps, independently of the adopted evaluation strategy. The difficulty is finding a strategy, together with a simulation on RAMs of the strategy, working within a overhead polynomial in:

- *Length*: the number of steps of the strategy, and
- *Input*: the size of the initial term.

When a strategy admits such a simulation we say that it is a *a polynomial cost model*. Note that the number of steps of the strategy can be whatever (it need not be polynomial), it is the *overhead* of the simulation that has to be polynomial in the above parameters.

The overhead is required to be polynomial because reasonable frameworks are not necessarily linearly related; this is the reason why the complexity class P is so important. For instance, Turing machines simulate RAMs only within a quadratic overhead. When possible, however, it is interesting to know what is the exact degree of the overhead with

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respect to RAMs, which are the commonly accepted abstraction of modern computers. When the overhead is linear – which is the optimal situation – then the polynomial cost model is trustable in practice: it does not hide an acceptable but possibly costly polynomial overhead.

Today, it is well-known that strategies such as weak head, head, and leftmost *β*-reduction provide polynomial time cost models, and that the same is true for their call-by-value and call-by-need variants [24, 42, 34, 14, 15, 10, 17, 22, 20, 23].

LSC and the Sub-Term Property. The key tool for the polynomially bounded simulations underlying the recent advances is Accattoli and Kesner's *linear substitution calculus* (shortened to LSC) [2, 9], which is an intermediary setting between the *λ*-calculus and RAMs.

The LSC is a simple *λ*-calculus with explicit substitutions refining a previous calculus by Milner [40, 32] and where evaluation is *micro-step*, that is, substitution acts on a variable occurrence at a time, rather than on all at once (which is *small-step*). A distinguished feature of the LSC is its rewriting rules *at a distance*, that is, rules where explicit substitutions do not percolate through the term structure but rather act through some contexts. The relevance of the LSC for reasonable time is linked to the *sub-term property* of its standard strategies [15]: at any point, only sub-terms of the initial term are duplicated, thus allowing one to bound the cost of each duplication with the size of the input.

No strategy of the *λ*-calculus has the sub-term property, which, as discussed by Accattoli [4], is the essence of the size explosion problem. The LSC crystallizes exactly what is needed for refining the *λ*-calculus to retrieve the sub-term property and circumvent size explosion.

LSC and Abstract Machines. It is also well-known that strategies of the LSC can usually be implemented with environment-based abstract machines whose overhead is *bi-linear*, that is, linear in both the input and length parameters, as shown by Accattoli et al. [6]. Abstract machines handle three tasks, namely the *decomposition of the substitution process*, the *search for redexes*, and *α-renaming*. The difference between the LSC and abstract machines is that the LSC only handles the substitution process, the critical one for avoiding size explosion. Search and α -renaming, indeed, tend to take only a linear overhead. That said, handling search and α -renaming provides an in-depth understanding of evaluation and it is mandatory for obtaining precise bounds from the cost model.

Lifting to Linear Logic. In 2022, Accattoli started a generalization of the mentioned recent results for the λ -calculus to the wider framework of linear logic [4]. Linear logic can be seen as a micro-step system with a tight control over duplications, somewhat similar to the LSC. Accattoli's starting point is the observation that, despite such similarity, no reasonable cut elimination strategy for linear logic was available. In particular, no cut elimination strategy with the sub-term property was known.

In [4], he introduces a generalization of the LSC to intuitionistic multiplicative exponential linear logic (IMELL). His *exponential substitution calculus* (ESC) is an untyped calculus of proof terms for the sequent calculus proofs of IMELL, endowed with cut elimination at a distance and having IMELL as typing system. He then designs a new cut elimination strategy, dubbed *good strategy*, which has the sub-term property and which is a polynomial cost model for ESC, the first such cost model for an expressive fragment of linear logic.

This Paper. Here, we design an environment-based abstract machine implementing the good strategy of ESC, show that it has bi-linear overhead, and provide an OCaml implementation. The result is not surprising, and yet we believe that it is interesting, for various reasons.

Firstly, it is the first linear overhead result for an expressive fragment of linear logic. It allows one to extract precise time bounds from the length of cut elimination, and can thus be useful, for instance, in linear-logic-based implicit computational complexity.

Secondly, our study differs in many details from other environment-based machines. There are in fact at least two literatures related to our work (surveyed in the next paragraph). A recent one about cost analyses of machines for *λ*-calculi and an older one about machines for linear logic, here referred to as *linear machines*. The main differences are the following:

- *Natural deduction vs sequent calculus*: machines usually implement calculi based on natural deduction, while ESC is based on sequent calculus, and some aspects are different. For instance, our machine has no argument stack (as it is expected, after Herbelin [31]).
- *Micro-step source and no structural equivalence*: the implemented calculus is usually small-step, while here it is micro-step. In the literature, when the calculus is micro-step it is usually implemented by the machine up to a notion of structural equivalence. Here, there is no need of structural equivalence.
- *Strong vs weak*: our machine computes cut-free proofs, while other linear machines in the literature do not, as they usually only perform weak (or surface) evaluation (that is, they do not evaluate under abstraction and inside promotions). To our knowledge, ours is the first strong linear machine.
- *Strong with no backtracking*: machines for strong evaluation usually have transitions for sequentially backtracking once the evaluation of a sub-term is over. Here, we adopt a recent new technique for strong machines by Accattoli and Barenbaum [5], that avoids sequential backtracking. The idea is to assign distinct jobs to each sub-term and simply jumping to the next job once the current one is over. This approach structures the machine in a very different way and provides drastically simpler strong machines.
- *Complexity analysis*: the complexity of linear machines is never studied in the literature.
- *OCaml implementation*: linear machines are usually studied theoretically and never implemented. We provide an OCaml implementation verifying our complexity analysis. In particular, it is the first implementation of the technique for strong machines of [5].

Summing up, our machine is simple, given the complex setting that it implements, thanks to the absence of stacks and backtracking. In fact, it uses just one (non-trivial) data structure.

Methodologically, the main difference between our work and the literature on linear machines is that those machines were developed to provide insights about *λ*-calculi and functional programming (such as no garbage collection, in-place updates, and single pointer property) while here we proceed the other way around, using the recent theory of the *λ*-calculus to provide insights about linear logic.

Architecture of the Result. The good strategy that we implement is a non-deterministic but *diamond* strategy, where diamond means that the choices do not affect termination nor evaluation lengths (otherwise the number of steps would not be a well-defined cost model). Our implementation is in two phases. The first and main one, performed by a deterministic machine dubbed *SESAME* (Strong Exponential Substitution Abstract Machine without Erasure), never erases. The second one is a simple garbage collection pass over the output of SESAME, that produces a cut-free term. For the correctness of SESAME, we thus need to relate a non-deterministic strategy and a deterministic machine, which is slightly unusual. For that, we follow the abstract recipe of Accattoli et al. [12] which is here simplified because our case is not up to structural equivalence and there is no implosiveness.

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Related Work. Accattoli and co-authors studied at length the overhead of machines for *λ*-calculi both micro-step [6, 7, 10] and small-step [3, 8, 18, 13, 12, 19, 5]. Biernacka et al. similarly studied machines for strong call-by-value [22] and strong call-by-need [23].

Lafont considered the first linear logic (LL) machine, based on categorical combinators for intuitionistic LL (ILL) [33]. Abramsky considered an environment-based machine for ILL and a chemical-style machine for LL, both doing only weak evaluation [1]. The latter machine was then studied by Mikami and Akama [39] and Sato and co-authors [43, 38]. Turner and Wadler study the use of IMELL for memory management and give two machines, one with the single pointer property and one without it but with a memoization mechanism [45]. Alberti and Ritter also deal with the single pointer property [21]. Bonelli gives a machine based on an unusual sequent calculus for ILL [25].

The interaction abstract machine (IAM) is an unusual token-based machine for LL proofs first studied by Danos and Regnier [30] and Mackie [35]. Recent work by Accattoli et al. showed that the IAM overhead is unreasonable for both time and space [16, 17, 46]. Mackie also studied interaction nets-based implementations of LL [36, 37].

2 The Exponential Substitution Calculus

In this section, we briefly recall Accattoli's exponential substitution calculus (ESC) [4] which is an untyped calculus having IMELL as system of simple types.

For more explanations, we refer the reader to [4]. The main differences with respect to [4] is that, for lack of space, we omit tensor and we only deal with micro-step rewriting rules (omitting the small-step exponential rule, which is derivable). In IMELL, linear implication is "more important" than tensor, as tensors are not needed to simulate the *λ*-calculus. In Appendix B of the technical report [11], we explain how to straightforwardly extend our study to tensor.

Values and Terms. The grammars of ESC are in the upper part of Fig. 1. Variables are of two disjoint kinds, multiplicative and exponential, and we refer to variables of unspecified kind using x, y, z . Values are the proof terms associated to axioms or to the right rules and, beyond variables, are *abstractions λx.t* and *promotions* !*t*. The proof terms decorating left rules are *subtractions* $[m\otimes v, x]t$, *derelictions* $[e^x x]t$, and *cuts* $[v\rightarrow x]t$, which is in red because of its special role. Note that cuts and subtractions are *split*, that is, have values (rather than terms) as left sub-terms. The constructors $\lambda x.t.$ $[m\otimes v, x]t$, $[e?x]t$, and $[v\rightarrow x]t$ bind x in *t*. We identify terms up to *α*-renaming. Free variables (resp. multiplicative/exponential variables) are defined as expected, and noted $f v(t)$ (resp. $m f v(t)$ and $e f v(t)$). We use |t| for the number of constructors in *t*, and $|t|_x$ for the number of free occurrences of x in t. There is a notion of *proper term* ensuring the linearity of multiplicative variables and the exponential boundary of promotions, detailed in Appendix A of the tech report [11]. The only relevant case is: *!t is proper if t is proper and* $m f v(t) = \emptyset$. In the following, terms are assumed to be proper.

Contexts and Plugging. The broadest notion of context that we consider is *general contexts C*, which simply allow the hole $\langle \cdot \rangle$ to replace any sub-term in a term. Because of split cuts and subtractions (that is, the fact that their left sub-term is a value rather than a term), the definition relies on the auxiliary notion of *value context V* . The definition also uses *left contexts L*, which are contexts under left constructors (or, for binary left constructors, under the right sub-term) that play a key role in the system – their use in defining *C* is just to keep the grammar compact. We also need *cut contexts E*, which are noted with *E* because they shall play the role of machine environments in Sect. 5.

A fact used pervasively is that every term *t* writes, or *splits* uniquely as $t = L(v)$. For instance $[e?m][v \rightarrow n]\lambda g$.[$m \otimes n$, $f]f$ splits as $L = [e?m][v \rightarrow n]\langle \cdot \rangle$ and $v = \lambda g$.[$m \otimes n$, $f]f$.]

Because of split cuts and subtractions, the definition of plugging $C\langle t \rangle$ (or $C\langle D \rangle$) of a term t (or a context D) in a context C is slightly tricky, as it has to preserve the split shape. We refer the reader to [4] for such details, the definition is mostly as expected (the only two subtle cases are for $[\langle \cdot \rangle \rightarrow x]t$ and $[m\otimes \langle \cdot \rangle, x]$). Plugging can capture variables and we use $C\langle t \rangle$ when we want to prevent it.

Types. The formulas of IMELL, and the deductive rules of the sequent calculus annotated with ESC terms, are in Fig. 2. They are taken directly from [4]. The typing system for ESC is exactly the standard sequent calculus for IMELL. Both formulas and rules are standard, but for the decoration with proof terms and the side conditions about variable names of the form $\Gamma \# \Delta$, which is a shortcut for dom $\Gamma \cap \text{dom}\Delta = \emptyset$. Linear implication \sim is also referred to as *lolli*. The only atomic formula that we consider, X_m , is multiplicative. There is no multiplicative unit because, in presence of the exponentials, 1 can be simulated by !(*X*^m ⊸ *X*m). We distinguish between multiplicative and exponential axioms, in order to decorate them with the corresponding kind of variable.

Note that the weakening and contraction rules do not add constructors to terms. This is crucial in order to keep the calculus manageable. Note also that the decorations of the cut and ⊸*^l* rules are *split*, as explained in the previous section. Clearly, typed terms are proper.

In this paper, types are only referred to in Lemma 3 below.

Multiplicative Cut Elimination Rules. The rewriting rules are in Fig. 1. The ESC has three multiplicative rules, in particular two for axioms, depending on whether they are acted upon $(\rightarrow_{\mathtt{axm}_1})$ or used to rename another multiplicative (thus linear) variable $(\rightarrow_{\mathtt{axm}_2})$. Rule

Figure 2 IMELL has a type system for ESC.

 \rightarrow _{axm₁} is expressed generically for multiplicative values $v_{\rm m}$ (that is, multiplicative variables *m* and abstractions $\lambda x.t$). In \rightarrow _{axm₁}, \rightarrow _{axm₂}, and \rightarrow _⊸, it is silently assumed that *C* does not capture *m* in \rightarrow _{axm₂} and \rightarrow _→ (what is noted $C\langle\!\langle m \rangle\!\rangle$ in \rightarrow _{axm₁}, while $\langle\!\langle \cdot \rangle\!\rangle$ is not used in →axm² , and →[⊸] because *C* might capture other variables in *t* and *v*). Note that, since *C* cannot capture *m* in these rules and terms are assumed to be proper, the hole of *C* cannot be contained in a !; this kind of context is called a *multiplicative context* in [4].

In \rightarrow_{\neg} , the rule has to respect split cuts, which is why, for writing the reduct, the sub-term *s* is split on-the-fly. An example of \rightarrow_{∞} step follows:

 $[\lambda e. [e?m]m \rightarrow n][n \otimes !f, o]o \rightarrow \Box$ [! $f \rightarrow e][e?m][m \rightarrow o]o.$

Exponential Rules. There are also four exponential rules, with again two rules for axioms. Replacement of variables $(\rightarrow_{\mathtt{axe}_1})$ and erasure $(\rightarrow_{\mathtt{w}})$ are expressed generically for exponential values v_e (that is, exponential variables f and promotions $\{t\}$), interaction with derelictions $(in \rightarrow_{\texttt{axe}_2} \text{ and } \rightarrow)$ instead requires inspecting $v_{\texttt{e}}$. Rule \rightarrow ! removes the dereliction, copies the promotion body, and puts it in a cut – in proof nets jargon, *it opens the box*. To preserve the split shape, the body of the promotion is split and only the value is cut. An example:

```
[![f \rightarrow g]g \rightarrow e]\lambda m.[e?e'][m\otimes e', n]n \rightarrow [![f \rightarrow g]g \rightarrow e]\lambda m.[f \rightarrow g][g \rightarrow e'][m\otimes e', n]n.
```
Note that →! entangles *interaction with a dereliction* and *duplication*, which is not what proof nets usually do (but this is what the LSC does). It is silently assumed that *C* does not capture e in \rightarrow _{axe₂} and \rightarrow ! but it might capture other variables in *t*.

Comments about the Rewriting Rules. Unsurprisingly, proper terms are stable by reduction [4]. We also use a notion of position in terms.

▶ **Definition 1** (Positions). *A* position *in a term t is a decomposition* $t = C\langle s \rangle$ *such that s is a sub-term of t.*

For later defining the good strategy, we identify a redex with its position, which is a context. Every step $t \rightarrow_{\text{ESC}_{\text{avg}}} s$ reduces a redex of shape $t = C \langle v \rightarrow x \rangle D \langle t_x \rangle$ where t_x is an *occurrence* of *x*, *i.e.* a sub-term of *t* of shape *x*, $[x \otimes v, y]$ *s*, or $[x?y]$ *s*. The *redex position of* → $_{\text{ESC}_{\neg y}}$ *steps* is the context *C*⟨[*v*_e→*x*]*D*⟩. The *redex position of* →w *steps* is the context clearing the rest step. We write *C* it is a few a redex of position *C* in t closing the root step. We write $C: t \to_{\text{ESC}} s$ for a redex of position C in t .

Clashes. The presence of many constructors in an untyped setting gives rise to *clashes*, that is, irreducible cuts.

 \triangleright **Definition 2** (Clashing, smooth, clash-free terms). *A* clash *is a term of the form* $[v_n \rightarrow e]t$ *or* $[v_e \rightarrow m]t$ *. A term t is clashing if it has a clash as sub-term. Moreover, t is clash-free if, co-inductively, t is not clashing and s is clash-free for any step* $t \rightarrow_{\text{FSC}} s$ *.*

Clash-freeness is guaranteed by all forms of types (simple, polymorphic, recursive, and multi types, for instance), in particular by the IMELL types in Fig. 2, as ensured by the next lemma. Here, we adopt the untyped calculus but consider only clash-free terms, as in [4].

- \blacktriangleright **Lemma 3** ([4]). Let *t* be a proper term.
- **1.** If *t* has no clashes and $t \nleftrightarrow_{\text{ESC}}$ then *t* is cut-free.
- **2.** *If t is typable (by the IMELL type system in Fig. 2) then t is clash-free.*

Postponement of Garbage Collection. The erasing rule \rightarrow _y, that models garbage collection, can be postponed. This point shall be crucial for our study.

▶ Proposition 4 (Postponement of garbage collection, [4]). *If* $t \to_{\texttt{ESC}}^* s$ *then* $t \to_{\texttt{ESC}}^* \to_w^* s$.

Basic Evaluation. As an intermediary step towards our results, we consider also a restricted form of ESC evaluation, forbidding reduction under all constructors but the right sub-terms of cuts, and dub it *basic evaluation*. It is one possible ESC analogous of weak evaluation in the λ -calculus (which forbids evaluation under abstraction). In λ -calculi with explicit substitutions, weak normal forms of closed terms are *answers*, i.e. abstractions possibly surrounded by explicit substitutions. We obtain a similar property for basic evaluation.

▶ **Definition 5** (Basic evaluation, answer). *A step* $C: t \rightarrow_{\texttt{ESC}} s$ *(that is, a redex of position C* in *t*) is basic if *C* is a cut context, which is also noted with $t \rightarrow_b s$, or $t \rightarrow_{ba} s$ with $a \in \{\text{axm}_1, \text{axm}_2, \neg o, \text{axe}_1, \text{axe}_2, \neg l, \text{w}\}\$ if we want to specify the kind of step. Moreover, $\rightarrow_{b \neg w}$ *denotes a non-erasing (that is, not* **w**) basic step. A term *t* is an answer if $t = E \langle v \rangle$ with E a *cut context and v not a variable.*

Lemma 6. Let *t* be a closed clash-free term. Either *t* has $a \rightarrow_{b \rightarrow w}$ -step or it is an answer.

Basic evaluation is enough to simulate in ESC the call-by-name/value weak evaluation of closed *λ*-terms, via Girard's translations to linear logic. Moreover, basic non-erasing evaluation is deterministic (and diamond, a property defined in Sect. 3, when erasing steps are considered). We omit the proofs of these facts, because neither basic evaluation nor *λ*-terms are the focus of the paper.

Dominating free variables of contexts

Figure 3 Definitions for the good strategy: dominating free variables and good contexts.

Out Cuts and Garbage Collection. We shall mainly deal with cuts that are not contained in any other cut, the *out cuts*, which induce a notion of *out variable*.

▶ **Definition 7** (Out cuts, out variables)**.** *The* out cuts *of a term t are those cuts in t that are not contained in any other cut, that is, if* $t = C \langle v \rangle | v \rangle |$ *then* $| v \rangle | s$ *is an out cut of t if C cannot be written as* $C = D\langle [V \rightarrow y]u \rangle$ *for some D*, *V*, *y*, and *u. The set of* out variables $\mathbf{v}(t)$ *of t contains the variables having at least one occurrence out of all cuts of t.*

Out variables allow us to define a lax notion of cut-free terms where there are some cuts, but they can only be garbage collection cuts, or concern variable occurrences contained in garbage, so that the term becomes cut-free after garbage collection.

▶ **Definition 8** (Cut-freeness up to garbage). A term t is cut-free up to garbage if $x \notin o\nu(s)$ *for all the out cuts* $[v\rightarrow x]$ *s of t.*

3 The Good Strategy

Here, we define the *good cut elimination strategy* \rightarrow _G for ESC, recalling the discussion from [4] that explains the design of the strategy. The strategy is conceived as to have the *sub-term property*, which is crucial for time analyses and which is defined as follows: every duplicated (or erased) sub-term during a cut elimination sequence is a sub-term of the initial term (up to variable renamings). See [4] for extensive discussions about the sub-term property.

Breaking the Sub-Term Property. When does the sub-term property not hold? One has to duplicate an exponential value *v*^e *touched* by previous steps. In our setting, *touched* can mean two things. Either a redex *fully* contained in $v_{\rm e}$ is reduced, obtaining $v'_{\rm e}$, and then $v'_{\rm e}$ is duplicated (or erased), as in the step marked with \star in the following diagram (the other, dashed path of which has the sub-term property):

Preventing these situations from happening, thus forcing evaluation to follow the other (dashed) side of the diagram, is easy. It is enough to forbid the position of the reduced redex

to be inside the left sub-term of a cut – we say inside a *cut value* for short. It is however not enough, because cuts are also *created*. Consider:

$$
\begin{array}{ccc}\n[\lambda e.e \rightarrow m][m \otimes v_{\mathbf{e}}, x]t & \rightarrow_{\text{ESC}} & [\lambda e.e \rightarrow m][m \otimes v_{\mathbf{e}}', x]t & \rightarrow_{\multimap} \\
& [v_{\mathbf{e}}' \rightarrow e][e \rightarrow x]t & \xrightarrow{\star}_{\text{axe}_1} & [v_{\mathbf{e}}' \rightarrow e][v_{\mathbf{e}}' \rightarrow x]t\n\end{array}
$$

Reducing inside the subtraction value v_e leads to a *later* breaking of the sub-term property by the \texttt{axe}_1 step, because the \rightarrow_{\neg} step creates a cut with $v'_\texttt{e}$ inside. Preventing these cases is tricky, because forbidding reducing subtraction values leads to cut elimination stopping too soon, without producing a cut-free term. In the *λ*-calculus, it corresponds to forbidding reducing inside arguments, which leads to *head* reduction, that does not compute normal *λ*-terms. We shall then forbid reducing only subtraction values which are *at risk* of becoming cuts. In λ -calculus, leftmost reduction does reduce arguments but only when the left sub-term of the application is normal and not an abstraction, so that the argument is not involved in a β -redex. We shall do something similar here, but the sequent calculus formalizes this constraint differently, by checking that some variables are not captured.

Dominating Variables. The key notion is the one of *dominating (free) variables* $dv(C)$ of a context (where *C* is meant to be the position of a redex), defined in Fig. 3, the base case of which is for $[m\otimes V, x]t$. If *C* is a position and $x \in dv(C)$ then $[v\rightarrow x]C$ turns *C* into a dangerous position, that is, a redex of position $[\nu \rightarrow x]C : t \rightarrow_{\text{ESC}} s$ might lead to a breaking of the sub-term property later on during cut elimination. In the example, e belongs to $dv(C)$ for every context $C := [e^{\gamma}m][m\otimes V, x]$ of $[e^{\gamma}m][m\otimes v, x]$ *s*, for every *V*.

The Good Strategy. These considerations lead to the notion of *good contexts* in Fig. 3. A good context forbids the two ways of breaking the sub-term property: its hole cannot be in a cut value (note the absence of the production $[V_G \rightarrow x]t$) nor in a subtraction value such that one of its dominating variables is cut (because of the production $[v\rightarrow x]G$ if $x \notin dv(G)$).

▶ **Definition 9** (Good steps, good strategy). *A step* $C: t \rightarrow_{\texttt{ESC}} s$ *is* good *if its position* C *is good. In such a case, we write* $t \rightarrow_S s$ *. The* good cut elimination strategy *is simply* \rightarrow_S *. We also use* \rightarrow_{Ga} *to stress that the good step is of kind* $a \in \{axm_1, axm_2, \neg o, axe_1, axe_2, \ldots\}$ *. The non-erasing good strategy* $\rightarrow_{\mathsf{G}_{\neg w}}$ *is the variant of* \rightarrow_{G} *excluding* $\rightarrow_{\mathsf{Gw}}$ *steps.*

The sub-term property in the following theorem captures the quantitative aspect for cost analyses, i.e. the bound on the size of duplicated values by the size of the initial term.

- ▶ **Theorem 10** (Properties of the good strategy, [4])**.**
- **1.** Quantitative sub-term property: if $e: t \to_{\mathsf{G}}^* s$ and v be a value erased or duplicated along e *, then* $|v| \leq |t|$ *.*
- **2.** Diamond: if $s_1 \notin \{-t \to \infty\}$ s_2 and $s_1 \neq s_2$ then $s_1 \to \infty$ $u \notin \{-s_2\}$ for some u .
- **3.** Fullness: if t is clash-free and not $\rightarrow_{\texttt{ESC}}$ -normal then $t \rightarrow_{\texttt{G}} s$ for some *s*.
- **4.** Good polynomial cost model: if $\Gamma \vdash t : A$ is a typed term then there exist k and a cut-free *term s such that* $t \rightarrow_{\mathsf{G}}^k s$, *and such a reduction sequence is implementable on RAMs in time polynomial in k and* |*t*|*.*

The algorithmic aspect of Theorem 10.4 is proved in [4] via arguments that do not establish the degree of the polynomial bound. The aim of this paper is exactly to show that a carefully designed abstract machine provides a bound that is linear in both *k* and |*t*|.

The Diamond Property. Let us provide some background about the diamond property. Following Dal Lago and Martini [34], we say that a relation \rightarrow _r is *diamond* if s_1 _r \leftarrow *t* \rightarrow _r s_2 and $s_1 \neq s_2$ imply $s_1 \rightarrow_\tau u_\tau \leftarrow s_2$ for some *u*. The terminology in the literature is inconsistent: Terese [44, Exercise 1.3.18] dubs this property $CR¹$, and defines the diamond more restrictively, without requiring $s_1 \neq s_2$ in the hypothesis: s_1 and s_2 have to join even if $s_1 = s_2$.

- Standard corollaries of Dal Lago and Martini's notion are that, if \rightarrow _r is diamond, then:
- 1. *Confluence*: \rightarrow_r is confluent, that is, $s_1 \overset{*}{\rightarrow} \leftarrow t \rightarrow_r^* s_2$ implies $s_1 \rightarrow_r^* u \overset{*}{\rightarrow} \leftarrow s_2$ for some *u*; **2.** *Length invariance*: all r-evaluations with the same start and r-normal end terms have
- the same length (*i.e.* if $e: t \to_{\mathbf{r}}^k s$ and $e': t \to_{\mathbf{r}}^h s$ with s **r**-normal, then $h = k$);

3. *Uniform normalization*: *t* is weakly r-normalizing if and only if it is strongly r-normalizing. Basically, the diamond property captures a more liberal form of determinism. In particular, length invariance is essential in order to take the number of steps of a strategy as a cost model. Without it, indeed, the number of steps of a non-deterministic strategy would be an ambiguously defined notion of cost.

4 Preliminaries on Abstract Machines

Abstract Machines Glossary. Abstract machines manipulate *pre-terms*, that is, terms without implicit α -renaming. In this paper, an *abstract machine* is a quadruple $M =$ (States, $\rightsquigarrow, \rightsquigarrow, \cdot \triangleleft \cdot, \cdot$) the components of which are as follows.

- *States.* A state *Q* ∈ States is composed by the *active term t*, plus one data structure which depends on the actual machine. Terms in states are actually pre-terms.
- *Transitions.* The pair (States, \rightarrow) is a transition system with transitions \rightarrow partitioned into *principal transitions*, whose union is noted \leadsto_{pr} and that are meant to correspond to cut-elimination steps on the calculus, and *search transitions*, whose union is noted \rightsquigarrow_{sea} , that take care of searching for (principal) redexes.
- *Initialization.* The component $\triangleleft \subseteq \Lambda \times$ States is the *initialization relation* associating terms to initial states. It is a *relation* and not a function because *t ◁ Q* maps a *λ*-term *t* (considered modulo *α*) to a state *Q* having a *pre-term representant* of *t* (which is not modulo α) as active term. Intuitively, any two states Q and Q' such that $t \triangleleft Q$ and $t \triangleleft Q'$ are α -equivalent. The initializing terms (i.e. those *t* such that $t \triangleleft Q$ for some *Q*) are always *proper* and *clash-free*. A state *Q* is *reachable* if it can be reached starting from an initial state, that is, if $Q' \leadsto^* Q$ where $t \triangleleft Q'$ for some t and Q' , shortened as $t \triangleleft Q' \leadsto^* Q$.
- *Read-back.* The read-back function \cdot : States $\rightarrow \Lambda$ turns reachable states into terms and \equiv satisfies the *initialization constraint*: if $t \triangleleft Q$ then $Q =_{\alpha} t$.

Further Terminology and Notations. A state is *final* if no transitions apply. A *run* $r: Q \leadsto^* Q'$ is a possibly empty finite sequence of transitions, the length of which is noted |*r*|; note that the first and the last states of a run are not necessarily initial and final. If *a* and *b* are transitions labels (that is, $\leadsto_a \subseteq \leadsto$ and $\leadsto_b \subseteq \leadsto$) then $\leadsto_{a,b} := \leadsto_a \cup \leadsto_b$ and $|r|_a$ is the number of *a* transitions in *r*, and $|r|_{\neg a}$ is the number of transitions in *r* that are not \leadsto_a .

Well-Boundness and Renamings. For the machines at work in this paper, the pre-terms in initial states shall be *well-bound*, that is, they have pairwise distinct bound names; for instance $[m\otimes\lambda n.n,e]\lambda f.f$ is well-bound while $[m\otimes\lambda n.n,e]\lambda n.n$ is not. We shall also write t^{α}
in a state O fang freeh well have a graming of this a t^o is a convinctor to the pull have dead in a state Q for a *fresh well-bound renaming* of *t*, *i.e.* t^{α} is α -equivalent to *t*, well-bound, and its bound variables are fresh with respect to those in *t* and in the other components of *Q*.

Implementation Theorem, Abstractly. We now formally define the notion of a machine implementing a strategy. Since the good strategy is non-deterministically diamond but the machine that shall implement it is deterministic, we need a slightly unusual form of implementation theorem. As it is standard, machine transitions shall be mapped to equalities or steps on the calculus. For the other direction, however, we obtain only a *big-step* simulation, that is, if the strategy on the calculus terminates / diverges then the same does the machine, and with a related number of steps. But there is no step-by-step simulation of the calculus by the machine, because the non-deterministic strategy might do a step which is not the one done by the machine. Everything works fine because of the properties of diamond strategies. The general scheme is inspired by Accattoli et al.'s scheme for strong call-by-value [12], but it is here simpler because of the absence of structural equivalence and implosive sharing.

It would also be possible to have a more symmetric setting by either designing a more complex diamond machine, as done by Accattoli and Barenbaum [5], or by designing a deterministic variant of the good strategy. For the sake of simplicity, we prefer the asymmetric setting as to keep a simple machine and reuse the notion of good strategy from the literature.

▶ **Definition 11** (Big-step implementations). *A machine* $M = (\text{States}, \rightsquigarrow, \cdot \triangleleft, \cdot, \cdot)$ *is a* big-step implementation of a strategy \rightarrow _{str} on terms when, given a (proper and clash-free) term *t*:

- **1.** Runs to evaluations: for any M-run $r : t \triangleleft Q' \leadsto^* Q$ there is a \rightarrow_{str} -evaluation $e : t \rightarrow_{str}^* Q$ *with* $|r|_{\text{pr}} = |e|$ *.*
- **2.** Normalizing evaluations to runs: if $e: t \to_{str}^* s$ with $s \to_{str}$ -normal then there is a M-run $r: t \triangleleft Q' \leadsto^* Q$ *such that* $Q = s$ *with* $|r|_{pr} = |e|$ *.*
- **3.** Diverging evaluations to runs: if $t \triangleleft Q$ and \rightarrow str *diverges on t then* M *diverges on Q doing infinitely many principal transitions.*

Next, we isolate sufficient conditions for big-step implementations.

▶ **Definition 12** (Lax implementation system)**.** *A lax implementation system is given by a* machine $M = (\text{States}, \leadsto, \cdot \triangleleft \cdot, \cdot)$ and a strategy \rightarrow_{str} such that for every reachable state Q:

- **1.** Principal projection: if $Q \rightarrow_{\text{spr}} Q'$ then $Q \rightarrow_{\text{str}} Q'$;
- **2.** Search transparency: if $Q \rightsquigarrow_{sea} Q'$ then $Q = Q'$;
- **3.** Search terminates*:* ⇝*sea terminates;*
- **4.** Halt: if *Q* is final then *Q* is \rightarrow _{str}-normal;
- **5.** Diamond: \rightarrow _{str} *is diamond.*

▶ **Theorem 13** (Abstract big-step implementation). Let M and \rightarrow _{str} form a lax implementation *system. Then,* M *is a big-step implementation of* \rightarrow _{str}.

Clash-Free and Proper States. Note that we have *not* taken into account properness and clashes on states. One can say that a state *Q* is proper (resp. clash-free) if its decoding *Q* is. In this way, these notions are trivially seen to be preserved by runs in a lax implementation system. Note, indeed, that transitions are mapped via decoding to either equalities or rewriting steps (and the proofs of these facts shall not need properness nor clash-freeness). Since states are initialized with proper and clash-free terms, and that these notions on terms are preserved by reduction (clash-freeness by definition, for properness see [4]), proper and clash-free states are preserved by transition. Therefore, we shall omit all considerations about properness and clashes for machines.

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 $\#$ with $L'\langle v' \rangle = L\langle v \rangle^{\alpha}$

Figure 4 The Basic Abstract Machine (BAM).

5 A Machine for the Closed Basic Case

Here, we study a machine implementing basic evaluation on closed ESC terms. The aim is to give a gentle introduction to some machine concepts.

BAM. The *basic abstract machine* (BAM) is defined in Fig. 4. States $Q = (E | t)$ are simply given by the active (pre-)term t and a cut context E which is a list containing the cuts encountered so far by the search mechanism, playing the role of the (global) environment in machines such as Accattoli et al.'s Milner Abstract Machine [6].

The initialization relation $t \triangleleft (\langle \cdot \rangle | t^{\alpha} \rangle)$ pairs (proper and clash-free) terms *t* with states composed by an empty cut context $\langle \cdot \rangle$ and a well-bound renaming t^{α} of t. The BAM has six principal transitions, mimicking the ESC rewriting rules but for the weakening one (BAM never erases), plus one search transition \rightsquigarrow_{sea} , moving cuts from the term to the cut context.

The BAM looks at the topmost constructor of the active term and proceeds applying a transition belonging to one of the following three groups. If the constructor is:

- Search: a cut $[v\rightarrow x]$, the BAM adds it to the cut context, by applying \rightsquigarrow_{sea} ;
- *Computation*: a subtraction $[m\otimes v, x]$ or a dereliction $[e^n x]$, then the BAM looks for the \blacksquare associated cut in the cut context, it applies the corresponding cut elimination rule, and goes on to execute the modified active term, by applying $\rightsquigarrow_{\text{axm}_2}$, $\rightsquigarrow_{\multimap}$, $\rightsquigarrow_{\text{axe}_2}$, or \rightsquigarrow !
- *Terminal replacements*: a variable *m* or *e*, the BAM looks for the associated cut in the cut $\mathcal{L}_{\mathcal{A}}$ context and it applies the matching replacement via $\leadsto_{\texttt{axm}_1}$ or $\leadsto_{\texttt{axe}_1}$. Note that terminal replacements can only be followed by further terminal replacements, hence the name.

Note that in transitions \rightsquigarrow ! and $\rightsquigarrow_{\texttt{axe}_1}$ some renaming takes place, using names that are fresh with respect to the whole state. The domain $\text{dom}(E)$ of a cut context is defined as $dom(\langle \cdot \rangle) := \emptyset$ and $dom([v \rightarrow x]E) := \{x\} \cup dom(E).$

- \blacktriangleright **Lemma 14** (BAM qualitative invariants). Let $Q = (E | t)$ be a BAM reachable state.
- 1. Closure: $f \mathbf{v}(t) \subseteq \text{dom}(E)$ and if $E = E' \langle [v \rightarrow x] E'' \rangle$ then $f \mathbf{v}(v) \subseteq \text{dom}(E')$.
2. Well have dentify the subsequence of the second in Q and m
- **2.** Well-bound: if $\lambda x.s$, $[e?x]s$, $[m\otimes v,x]s$, or $[v\rightarrow x]s$ occur in *Q* and *x* has any other *occurrence in Q then it is a free variable of s, and if* $E = E' \langle [v \rightarrow x] E'' \rangle$ *and x has any athen assumence in Q then it is a free variable in* E'' and *other occurrence in Q then it is a free variable in E*′′ *or t.*

▶ **Proposition 15.** *Let Q be a BAM reachable state and* $a \in \{axm_1, axm_2, axe_1, axe_2, -\circ, !\}.$

- **1.** Search transparency: if $Q \rightsquigarrow_{sea} Q'$ then $Q = Q'$.
- **2.** Principal projection: if $Q \rightsquigarrow_a Q'$ then $Q \rightarrow_{ba} Q'$.
- **3.** Search termination: transition \rightsquigarrow_{sea} terminates.

* *b* is fresh. # with $L'(v') = L(v)^\alpha$

Figure 5 The Strong Exponential Substitution Abstract Machine without Erasure (SESAME).

- **4.** Halt: if Q is final then $Q = (E | v)$ with v not a variable, and Q is normal for non-erasing *basic evaluation.*
- ▶ **Theorem 16.** *The BAM implements ESC non-erasing basic evaluation on closed terms.*

6 SESAME

In this section, we extend the BAM as to perform the analogous of strong evaluation, that is, as to perform cut-elimination also inside values, and implement the good strategy. The obtained machine shall ignore garbage collection and – when it terminates – it returns a term that is cut-free *up to garbage* (Def. 8). Garbage collection shall be addressed in Sect. 8.

Pools and Jobs. The *Strong Exponential Substitution Abstract Machine without Erasure* (SESAME), defined in Fig. 5, relies on a technique for strong evaluation recently introduced by Accattoli and Barenbaum [5], which we are now going to explain.

When evaluation goes under binders, the closure invariant of the basic case (Lemma 14.1), for which free variables of the active term are associated to a cut in the cut context, is lost. Thus, when the active term is a subtraction $[m\otimes v, x]t$ with no associated cut for *m*, the subtraction is kept, and the machine has to evaluate *v* and *t*. Now, the evaluations of *v* and *t* cannot affect each other, they are independent, but one of the two sub-terms has to be evaluated first, say *v*. Usually, strong machines (such as Crégut's [26]) would run through v , and if such process does terminate, producing a value v' , then they *backtrack* to the subtraction generating the fork by *moving sequentially* through v' , and then start evaluate t .

Accattoli and Barenbaum's technique simply circumvents the sequential backtracking process by directly jumping back to the forking point. For that, the machine is equipped with a *pool P* of *jobs*, each one paired to a unique name *a*. In our example, there would be a job $\langle v \rangle_a$ for *v* and a job $\langle t \rangle_b$ for *t*. The idea is that when one job ends then the machine jumps to the next one, *without* moving through the structure of the finished job.

In [5], the pool is any data structure satisfying a certain interface. The idea is that different data structures implementing the interface realize different job scheduling policies, compactly accounting for different strong strategies within the same framework. We here omit this abstract aspect and fix the pool to be the simplest such structure, namely a LIFO list of named jobs. In the *λ*-calculus, LIFO list pools implement leftmost evaluation [5].

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Another simplification with respect to [5] is that here jobs are simply named terms, while in [5] they are named pairs of a term and an applicative stack. The change induces a simpler notion of read back. This difference is not a design choice, it is simply induced by having a machine for sequent calculus terms (here) rather than natural deduction terms (in [5]).

A crucial point of SESAME is that job forking happens on subtractions only, that is, it does not happen on cuts $[v \rightarrow x]t$: the machine ignores *v* and goes straight to evaluate *t*. This happens in particular to prevent the breaking of the sub-term property.

We write $name(P)$ for the set of names associated to the jobs in *P*, that is, if $P =$ $\langle t_1 \rangle_{a_1} : \ldots : \langle t_k \rangle_{a_k}$ then $\texttt{name}(P) := \{a_1, \ldots, a_k\}.$

Multi-Contexts and Approximants. Another aspect of the technique in [5] is that the parts of the term that have been evaluated and that shall not be touched again by the machine – sometimes referred to as *stable parts* – are accumulated in the *approximant* A (of the normal form), which is a multi-context (that is, a context with possibly many holes, possibly none). The idea is that the name *a* of a job $\langle t \rangle_a$ in the pool is associated to a (unique) named hole $\langle \cdot \rangle_a$ in A, and that, whenever a stable piece of term is produced by the job $\langle t \rangle_a$, that piece is moved to $\langle \cdot \rangle_a$ in A, incrementally building the normal form.

We follow this pattern from [5], but our setting induces a few differences. Firstly, in [5] the machine has also a global environment, akin to the cut context of the BAM. Here, we include the environment/cut context *into* the approximant, having only one data structure. Thus, the approximant shall have cuts, but these cuts bind variables that have non-garbage occurrences only in the active term, i.e., only out of the approximant itself, where instead they have only garbage occurrences. At the end of a complete run, the approximant shall be normal for the non-erasing good strategy, i.e. it shall be cut-free up to garbage (Def. 8).

Secondly, in [5] the machine is defined using approximants, while here we define it using the weaker notion of multi-contexts, and then prove invariants guaranteeing that the multi-contexts of reachable states are approximants.

 \triangleright **Definition 17** (Multi-contexts and approximants). *A* (named) multi-context $\mathbb C$ *is an ESC term in which there might be occurrences of holes* $\langle \cdot \rangle_a$ *indexed with names, as follows:*

NAMES $a, b, c, a_1, b_2, \ldots$ | VALUE MULTI-CTXS $\mathbb{V} ::= \langle \cdot \rangle_a | x | \lambda x. \mathbb{C} | ! \mathbb{C}$ MULTI-CTXS $\mathbb{C} ::= \mathbb{V} \mid [\mathbb{V} \rightarrow x] \mathbb{C} \mid [m \otimes \mathbb{V}, x] \mathbb{C} \mid [e?x] \mathbb{C}$

The plugging $\mathbb{C}\langle \mathbb{C}' \rangle_a$ *of* \mathbb{C}' *on a in* \mathbb{C} *, is the capture-allowing substitution of* $\langle \cdot \rangle_a$ *by* \mathbb{C}' *in* C*. We write* C⟨C′ ⟩◦ *for when* ◦ *is an irrelevant name occurring exactly once in* C*; this notation is meant to be used for decomposing a multi-context in two, as in* $\mathbb{C} = \mathbb{C}' \langle \mathbb{C}'' \rangle$ ^{*.*} *We write* $name(\mathbb{C})$ *for the set of names that occur in* \mathbb{C} *. Out variables and out cuts extend to multi-contexts as expected.*

The domain $\text{dom}(\mathbb{C}) = \{x_1, \ldots, x_n\}$ *of a multi-context* $\mathbb C$ *contains the variables on which there is a cut in* $\mathbb C$ *(the formal definition is in Appendix E of the tech report [11]).*

An approximant A *is a multi-context such that:*

- **1.** Unique names: every name $a \in \text{names}(A)$ has exactly one occurrence in A;
- **2.** Out cuts are hereditary garbage and hole-free: for every out cut $[\mathbb{V} \rightarrow x] \mathbb{C}'$ in A, if $x \in \text{fv}(\mathbb{C}')$ *then x occurs only inside cut values, i.e.* $x \notin \text{ov}(\mathbb{C}')$ *, and* \mathbb{V} *is a term.*

Note that a multi-context $\mathbb C$ without holes is simply a term, thus the defined notion of plugging subsumes the plugging $\mathbb{C}\langle t \rangle_a$ of terms in multi-contexts.

Transitions. The principal transition of SESAME are as for the BAM up to the generalization of the cut context E to a multi-context $\mathbb C$. Note the mute name \circ for decomposing the multi-context in the transitions. Clearly, the name \circ on the LHS and the RHS of each transition is the same. SESAME has a search transition for each constructor of the calculus. Note that transitions $\leadsto_{\mathtt{axm}_1}$ and $\leadsto_{\mathtt{axe}_1}$ are no longer *terminal*, because now they might be followed by a search transition, after which the run might jump to a different job.

Read Back. The read back *Q* of a state $Q = (\mathbb{C} \mid P)$ is defined in Fig. 5 and simply plugs each job $\langle t \rangle_a$ of the pool *P* in the hole of name *a* of the multi context \mathbb{C} . We also write $\mathbb{C}\langle P \rangle$ for *Q*. Since the holes of C are all independent and jobs contains terms (with no holes), we have that $\mathbb{C}\langle P\rangle = \mathbb{C}\langle P'\rangle$ for any pool P' obtained by permuting the elements of P.

Example of SESAME run. As an example, we show a SESAME run on the following term:

 $t := [\lambda m_1.m_1 \rightarrow e_1][e_1?m_2][e_1?m_3][m_2 \otimes m_3, m_4]m_4$

that just applies an identity function to itself, by making two copies of $\lambda m_1.m_1$ and applying one to the other via the subtraction. To fit it into the margins, we use the abbreviation $s := [m_2 \otimes m_3, m_4]m_4.$

Note that in the last state the cut $[\lambda m_1.m_1 \rightarrow e_1]$ is garbage but it is not removed. This shall be taken care of by an extra garbage collection phase described in Sect. 8.

7 Analysis of SESAME

Invariants. As for the BAM, we prove some invariants. The first one states that the multi context of a reachable state is an approximant. A second invariant states that names in the multi context are exactly those in the pool, where they have exactly one occurrence each. Then, we need a well-bound invariant about binders, relying on a technical definition in Appendix E of the tech report [11], along the lines of the invariant of the BAM. Lastly, the contextual decoding invariant, the most important and sophisticated invariant, states that a reachable state less the first job reads back to a good context. For the invariant to hold, the statement has to be generalized in a technical way, analogously to similar invariants in [5].

 \blacktriangleright **Lemma 18** (SESAME qualitative invariants). Let $Q = (\mathbb{C} \mid P)$ be a SESAME state reachable *from a well-bound initial term* t_0 *.*

- **1.** Approximant*:* C *is an approximant.*
- **2.** Names: jobs in the pool P have pairwise distinct names, and $name(\mathbb{C}) = names(P)$.
- **3.** Well-bound*: Q is a well-bound state.*
- **4.** Contextual decoding: if $P = \langle t_1 \rangle_{a_1} : \ldots : \langle t_k \rangle_{a_k}$ has length $k \geq 1$ then:

$$
\mathbb{C}_{s_1,\ldots,s_{i-1}|a_i|s_{i+1},\ldots,s_k}^Q := \mathbb{C}\langle s_1 \rangle_{a_1}\ldots \langle s_{i-1} \rangle_{a_{i-1}} \langle \langle \cdot \rangle \rangle_{a_i} \langle s_{i+1} \rangle_{a_{i+1}} \ldots \langle s_k \rangle_{a_k}
$$

is a good context for $i \in \{1, \ldots, k\}$ *and any terms* $s_1, \ldots, s_{i-1}, s_{i+1}, \ldots, s_k$ *such that* \mathbb{C}^Q *s*1*,...,si*−1|*ai*|*si*+1*,...,s^k is a term.*

Implementation Theorem. The well-bound and contextual decoding invariants are used to prove principal projection. The approximant and names invariants are used to prove the halt property. Search transparency and termination are straightforward. Then we apply the abstract implementation theorem (Theorem 13), and obtain the implementation theorem.

- ▶ **Proposition 19.** *Let* $a \in \{ \text{axm}_1, \text{axm}_2, \text{axe}_1, \text{axe}_2, -\diamond, ! \}.$
- **1.** Search transparency: if $Q \rightsquigarrow_{sea} Q'$ then $Q = Q'$.
- **2.** Principal projection: if $Q \rightsquigarrow_a Q'$ then $Q \rightarrow_{\mathsf{Ga}} Q'$.
- **3.** Search termination: transition \rightsquigarrow_{sea} terminates.
- **4.** Halt: if *Q* is final then $Q = (t | \epsilon)$ and $Q = t$ is cut-free up to garbage.

▶ **Theorem 20** (SESAME is good)**.** *SESAME is a big-step implementation of the non-erasing good strategy* $\rightarrow_{\mathbf{G}_{\neg w}}$ *of ESC.*

Complexity Analysis

The complexity analysis of SESAME amounts to bound the cost of implementing a run $r: t \triangleleft Q \leadsto_{\texttt{SESAME}}^* Q'$ on random access machines (RAMs) as a function of two parameters: the number $|r|_{\text{pr}}$ of principal transitions in *r* (which are in bijection with the number of $\rightarrow_{\text{G}_{\text{m}}}$ steps) and the size $|t|$ of the initial term/state t/Q .

Sub-Term Property. The analysis relies on the *sub-term property*, ensuring that the duplicating principal transitions \rightsquigarrow ! and \rightsquigarrow _{axe₁} manipulate only sub-terms of the initial term. Therefore, the cost of duplications is connected to size of the initial term. The property for SESAME can be inferred by the one for the good strategy, but we prefer to give a direct simple proof. The statement of the related invariant is about cut values, not duplications, but note that such values are the only terms duplicated by SESAME.

- ▶ **Lemma 21** (Sub-term)**.** *Let r* : *t ◁ Q* ⇝[∗] SESAME (C | *P*) *be a SESAME run.*
- **1.** Invariant: if *v* is a cut value in $\mathbb C$ or a value in P then $|v| \leq |t|$.
- **2.** Property: if *v* is a value duplicated along *r* (by \rightsquigarrow) or $\rightsquigarrow_{\text{axe}_1}$) then $|v| \leq |t|$.

Since search transitions decrease the number of term constructors in the pool, which is only increased by principal transitions and of a quantity bounded by |*t*| (by the sub-term invariant), we obtain the following bound.

▶ **Lemma 22** (Search transitions are bi-linear in number)**.** *Let r* : *t◁Q* ⇝[∗] SESAME *Q*′ *be a SESAME run.* Then $|r|_{\text{sea}} \leq |t| \cdot (|r|_{\text{pr}} + 1)$.

Cost of Single Transitions. For bounding the total cost, we need to make some hypotheses on how SESAME is going to be itself implemented on RAMs:

- **1.** *Variable occurrences, binders, and cuts*: a variable is a memory location, a variable occurrence is a reference to it, and a cut $[v\rightarrow x]$ is the fact that the location associated with *x* contains *v*;
- **2.** *Random access to variables*: the cuts in $\mathbb C$ can be accessed in $\mathcal O(1)$ by just following the reference given by the variable occurrence, with no need to search through \mathbb{C} ;
- **3.** *Named holes and jobs:* a named hole $\langle \cdot \rangle_a$ is again a memory location *a*, and a job $\langle t \rangle_a$ is a pointer to *a* and expresses the fact that location *a* contains *t*;
- **4.** *Sequences of left rules*: cuts have a back pointer to the constructors before them (for instance in $[e^{\alpha}m][v \rightarrow x][m\otimes v', y]t$ the cut $[v \rightarrow x]$ has a pointer to $[e^{\alpha}m]$) so that the removal
of a cut from the multi-sentent \mathbb{C} of a state-peopled for the multiplicative transitions of a cut from the multi context $\mathbb C$ of a state – needed for the multiplicative transitions \rightsquigarrow _{axm₁}, \rightsquigarrow _{axm₂}, and \rightsquigarrow _→ – can be performed in $\mathcal{O}(1)$.

As it is standard for time analyses, we also assume that pointers can be managed in $\mathcal{O}(1)$. These hypotheses mimic similar ones behind machines for *λ*-calculi, which are shown to be implementable in OCaml by Accattoli and Barras [8] and Accattoli et al. [12], and are followed by the OCaml implementation outlines in Sect. 9. They allow us to consider search transitions as having constant cost, and principal transitions as having cost bound by the initial term, by the sub-term property.

▶ **Lemma 23** (Cost of single transitions)**.** *Let r* : *t ◁ Q* ⇝[∗] SESAME *Q*′ *be a SESAME run. Search (resp. principal) transitions of r are implementable in* $\mathcal{O}(1)$ *(resp.* $\mathcal{O}(|t|)$ *).*

Summing Up. By putting together the bounds on the number of search transitions with the cost of single transitions we obtain the complexity of SESAME.

▶ **Theorem 24** (SESAME bi-linear overhead bound)**.** *Let r* : *t ◁ Q* ⇝[∗] SESAME *Q*′ *be a SESAME run. Then r is implementable on RAMs in* $\mathcal{O}(|t| \cdot (|r|_{\text{pr}} + 1)).$

8 Final Garbage Collection and Full Cut Elimination

By the halt property (Proposition 19.4), SESAME stops on final state $Q = (\mathbb{A} \mid \epsilon)$ where \mathbb{A} is a (pre-)term *t* that is cut-free *up to garbage*. The final garbage collection process turning *t* into a cut-free proof term $GC(t)$ is formalized as the following function:

Final garbage Collection

 $\mathsf{GC}(x) := x$ $\qquad \qquad \mathsf{GC}(\lambda x.t) := \lambda x.\mathsf{GC}(t)$ $\qquad \qquad \mathsf{GC}(!t) := !\mathsf{GC}(t)$ $\texttt{GC}([\textit{v} \rightarrow \textit{x}]\textit{t}) \ := \ \texttt{GC}(t) \quad | \quad \texttt{GC}([\textit{m} \otimes \textit{v}, \textit{x}]\textit{t}) \ := \ [\textit{m} \otimes \texttt{GC}(\textit{v}), \textit{x}] \texttt{GC}(t) \quad | \quad \texttt{GC}([\textit{e}?\textit{x}]\textit{t}) \ := \ [\textit{e}?\textit{x}] \texttt{GC}(t)$

Clearly, $\mathsf{GC}(t)$ is cut-free. The following proposition states the qualitative and quantitative properties of garbage collection. Its first point uses the lemma before it, while the second point rests on the SESAME bi-linear bound.

▶ **Lemma 25.** *Let t be a term that is cut-free up to garbage but not cut-free. Then in t there is at least an out cut that is a* \rightarrow _{Gw}-redex.

• Proposition 26. Let $r : t \triangleleft Q \rightsquigarrow_{\text{SESAME}}^* (s \mid \epsilon)$ a SESAME run ending on a final state.

- **1.** Good weakening steps simulate GC: $s \rightarrow_{\mathsf{Gw}}^k \mathsf{GC}(s)$ where k is the number of out cuts in s,
- **2.** GC is bi-linear: the final garbage collection function $s \mapsto \text{GC}(s)$ can be implemented on *RAMs in* $\mathcal{O}(|t| \cdot (|r|_{\text{pr}} + 1)).$

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By simulating good cut-elimination via the SESAME followed by final garbage collection, we obtain our main result, which – beyond the bi-linear bound – is a further slight (but sort of obvious) refinement of Accattoli's result, as it allows one to count only *non-erasing* good steps, rather than general good steps.

▶ **Theorem 27** (Good cut-elimination is big-step implementable with bi-linear overhead)**.** *Let* $e : t \rightarrow_{\mathsf{G}}^* s$ *be a good evaluation sequence with s cut-free. Then s is computable from t on RAMs in* $\mathcal{O}(|t| \cdot (|e|_{\neg \mathbf{w}} + 1)).$

Proof. If $e: t \to_{\mathsf{G}}^* s$ with s cut-free then \to_{G}^* terminates, by the *uniform normalization* corollary of the diamond property for \rightarrow_G (see the end of Sect. 3). Then there is an evaluation $e': t \to_{\mathsf{G}_{\neg \mathsf{w}}}^* s'$ with s' cut-free up to garbage. Since SESAME is a big-step implementation of $\rightarrow_{\mathsf{G}_{\neg w}}$ (Theorem 20), we obtain a SESAME run $r : t \triangleleft Q \rightsquigarrow_{\mathsf{SESAME}}^* Q'$ with $Q' = s'$, of cost $\mathcal{O}(|t| \cdot (|r|_{\text{pr}} + 1)) = \mathcal{O}(|t| \cdot (|e'| + 1)).$ By the halt property of SESAME and cut-freeness up to garbage of SESAME, we obtain that Q' is final, that is, $Q' = (u | \epsilon)$ with $u = \alpha s'$.

Since good weakening steps simulate GC (Proposition 26.1), we obtain $e'' : u \rightarrow_{\mathsf{Gw}}^* \mathsf{GC}(s')$ with $GC(s')$ cut-free. Concatenating e' and e'' we obtain a good and normalizing evaluation sequence $e''': t \to_{\mathsf{G}_{\neg \mathsf{w}}}^* \to_{\mathsf{G}_{\mathsf{w}}}^* \mathsf{GC}(s').$ The diamond property of the good strategy (Theorem 10.2) implies *confluence* and *length invariance* of \rightarrow_G (see the end of Sect. 3), that is, we obtain $s = \texttt{GC}(s')$ and $|e| = |e'''|$. Since computing GC is bi-linear (Proposition 26.1), and precisely costs again $\mathcal{O}(|t| \cdot (|r|_{\text{pr}} + 1)) = \mathcal{O}(|t| \cdot (|e'| + 1))$, we obtain that the total cost of SESAME followed by GC is $2 \cdot \mathcal{O}(|t| \cdot (|e'| + 1)) = \mathcal{O}(|t| \cdot (|e'| + 1)).$

Lastly, the diamond property of the good strategy (Theorem 10.2) as stated in [4] does not give information about the kinds of steps, but by looking at its proof it is easily seen that diamond diagrams preserve the kind of steps. Therefore, not only we have $|e| = |e'''|$, but also $|e|_{\neg w} = |e'''|_{\neg w}$. Finally, note that $|e'''|_{\neg w} = |e'|$, so the cost actually is $\mathcal{O}(|t| \cdot (|e|_{\neg w} + 1))$.

9 Implementation in OCaml

An implementation in OCaml can be found on GitHub at [https://github.com/sacerdot/](https://github.com/sacerdot/sesame/) [sesame/](https://github.com/sacerdot/sesame/). All the datatypes and functions of the implementation are documented at [https:](https://sacerdot.github.io/sesame) [//sacerdot.github.io/sesame](https://sacerdot.github.io/sesame).

Aim and Design. The implementation is provided to support evidence for the cost of transitions claimed by Lemma 23. Moreover, it allows one to easily study the computational behaviour of family of terms by observing their evaluation.

The implementation has *not* been heavily optimized, in order to keep the code readable and close to the pen-and-paper presentation. Nevertheless, the employed data structures are reasonably close to those of an optimized implementation. Advanced recent OCaml features, like Generalized Algebraic Data Types, could have been used to statically enforce more invariants. We sticked instead to a simpler subset of OCaml, to make the code readable to non experts of the language. As a consequence, in several places there are assertions to abort the program in case the invariants are violated (possible only in case of bugs).

Overview. The implementation consists of a Read-Eval-Print-Loop (REPL) that asks the user to enter an IMELL term to be reduced, in ESC syntax. The accepted BNF is printed by the executable before starting the REPL. The term is first checked to be proper, then its strong normal form up to garbage is computed by the SESAME machine and finally garbage is removed. All intermediate machine steps are shown.

Before starting the REPL, a few test terms are reduced and printed (among which the example at the end of Sect. 6), to check the machine functionalities and as examples of the input syntax. In particular, the last tests are terms from the exploding family discussed in the next section.

Data Structures. Terms are encoded at runtime as term graphs, which are Direct Acyclic Graphs (DAG) augmented with a few back edges. Nodes in the graph are given by constructors of Algebraic Data Types whose arguments are mutable, to imperatively change the graph during reduction. All type declarations are given in the termGraphs.ml file. The data structures used in the implementation are described in Appendix H of the tech report [11].

Machine Runs: Auxiliary Functions and Their Complexity. The code that implements the runs of SESAME to normal form and the final garbage collection process can be found in reduction.ml, which is independent from all the remaining files but termGraphs.ml. Both files together amount to 412 lines of commented OCaml code.

A quick inspection of the code in reduction.ml shows only a few non-trivial functions, all the others being constant time:

- **1.** copy_{term,value,var,bvar} that copies the DAG in input while visiting it. A $\mathcal{O}(1)$ fresh name generator is used to assign a new name value to copies of variables.
- **2.** alpha and enter bo, used respectively in the ! and \sim transitions, that take the body of a promotion/abstraction, traverse it to split it into a left context and a value, and build a new term by glueing together the obtained left context, a new cut that uses the value, and a remaining term. The two functions differ only in the fact that alpha also copies (α -renames) the term in input, calling the copy $*$ functions on the sub-terms, while enter_bo consumes the given term.
- **3.** steps, the SESAME main loop, that runs until the pool is empty and a normal form is therefore reached, or it diverges otherwise.
- **4.** gc_{value,term} that traverse in linear time the input to remove all garbage cuts from a normal form.

Linearity for all the previous functions but steps is easily established observing that the functions are based on visits of DAGs that never visit a node twice. In particular, the functions of Point 2 are linear in the size of the given term, which, by the sub-term invariant, is a (copy of a) sub-term of the initial term.

The only major source of technicalities – which however do not affect the complexity – is the fact that multiplicative transitions remove the acting cut from the multi context. For example, consider a multiplicative step $[e?f][v+m][v' \rightarrow y]t \rightarrow [e?f][v' \rightarrow y]t'$ involving the cut on *m*. To implement it, beyond manipulating v , t , and t' , one also has to connect $[e^{\gamma t}]$ and $[v'\rightarrow y]$. For that, cuts have a back-pointer to the preceding constructor (as mentioned at R_{disk}). This is due a independent importing Point 4, labeled *sequences of left rule*, before Lemma 23), which induces inelegant imperative manipulations of the term graph, in particular in the copy function. The back-pointer on $[v\rightarrow m]$ is used to retrieve the dereliction $[e^{\gamma}f]$ from $[v\rightarrow m]$, while making the new connection of $[e?f]$ and $[v'\rightarrow y]$ requires changing two pointers: the one from the dereliction to its body, which has to target $[v'\rightarrow y]$, plus the back-pointer from $[v'\rightarrow y]$, which now has to target $[e?f]$.

The general scheme for addressing this issue is to augment the input *t* of functions that take a term with the pointer to the parent node of *t*, in order to be able to reassign the back-pointer when *t* is a cut.

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Parsing, Pretty Printing, and Check for Properness. We have not attempted to achieve the best asymptotic costs for these functionalities since they are not relevant for the paper and since in practice we are manipulating small terms in input. To keep the code reproducible in the long term, we have not used any external library or tool for parsing and pretty-printing. The printed version of the example run at the end of Sect. 6 is shown in Appendix I of the tech report [11].

10 What's Next: an Interesting Family of Terms

We here hint at what we consider the most interesting future work, already mentioned by Accattoli in the conclusions of [4]. The difference is that here we provide a family of terms showing that such a future work is challenging.

The Question. *One β*-step of the *λ*-calculus is simulated in IMELL by *one* multiplicative step (actually a \rightarrow_{∞} step) followed by possibly *many* exponential steps. Since for many strategies the number of *β*-steps is a polynomial cost model, it means that one can count only the number of multiplicative (or even $\rightarrow_{-\infty}$) steps, that is, *one can count zero for exponential steps*. Such a surprising fact is established easily in the case of weak evaluation with closed terms (roughly, it can be proved via standard abstract machines), while for strong evaluation it requires a sophisticated additional technique called *useful sharing* [15, 12, 20]. A question naturally arises: *is the number of ESC multiplicative/*→[⊸] *good steps a polynomial time cost model as well?* This question – left to future work – is far from obvious. In the *λ*-calculus, there is a strong, hardcoded correlation between multiplicatives and exponentials, not present in IMELL. In the standard call-by-name/value encodings of *λ*-calculus in IMELL, indeed, multiplicatives and exponentials connectives *rigidly alternate*, while IMELL also has consecutive exponentials, as in !!*A*, enabling wilder exponential behaviour. A hint that that question might have a positive answer is given by the strong normalization of untyped exponentials in ESC/IMELL shown in [4] (while they are *not* SN in MELL, see [4]).

The Question is Challenging. We here show that the question mentioned above is challenging, and $-$ surprisingly $-$ it is already challenging in the apparently simple setting of basic evaluation with closed terms, that is the ESC analogous of weak evaluation for the *λ*-calculus, for which instead the question has an easy answer. The challenging aspect is here shown by building a family of ESC terms σ_n having size linear in *n*, whose evaluation is basic and made out of exponential steps only, and of a number of steps that is exponential in *n*. This is impossible in the *λ*-calculus, and it is crucially related to iterated exponential terms such as !!*t*. Since the evaluation of σ_n uses 0 multiplicative steps, it suggests that the number of multiplicative steps is *not* a polynomial cost model. More precisely, it shows that a smart additional mechanism – akin to useful sharing – is needed in order to circumvent the exponential number of exponential steps. Useful sharing as it appears in the literature, however, cannot be the answer, since useful sharing addresses exponential inefficiencies that are induced by strong evaluation, which is an orthogonal issue.

The Exponential Exploding Family. The family is built in three steps. *k*

1. For $k \geq 1$, define $\pi_k := \boxed{f^? \dots [f^? \dots [f^?e]} e$, where \dots is an exponential variable with no occurrences, and whose name is irrelevant. These terms can be composed as to reduce to (the cut-free term) $\pi_{k \cdot h}$ via exponential basic evaluation, that is, $[!\pi_k \to f] \pi_h \to_{\mathbf{e}}^* \pi_{k \cdot h}$.

n−1

- 2. Define $\delta_n :=$ $\overline{[! \pi_2 \rightarrow f]}$ $\overline{}$ $\overline{[! \pi_2 \rightarrow f]}$ π_2 , and note that, by Point 1, one has $\delta_n \rightarrow_e^* \pi_2$ ⁿ.
- **3.** Finally, define the exponential exploding family as $\sigma_n := [!! \lambda m.m \rightarrow f] \delta_n$. By Point 2, we have $\sigma_n \rightarrow_{\mathbf{e}}^* [!! \lambda m.m \rightarrow f] \pi_{2^n}$. Then note that $[!! \lambda m.m \rightarrow f] \pi_{2^n} \rightarrow_{\mathbf{e}}^{\Omega(2^n)} ! \lambda m.m$.

Summing up, σ_n is an example of term of size $\mathcal{O}(n)$ that reduces in $\Omega(2^n)$ exponential steps and 0 multiplicative steps to a cut-free proof using only basic evaluation. As mentioned in the previous section, the OCaml implementation starts by running some tests, including examples of the three points above, respectively for $k = 3, h = 4$, for $n = 3$, and for $n = 3$.

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