

Useful applications of correctly-rounded operators of the form ab + cd + e

Tom Hubrecht, Claude-Pierre Jeannerod, Jean-Michel Muller

► To cite this version:

Tom Hubrecht, Claude-Pierre Jeannerod, Jean-Michel Muller. Useful applications of correctly-rounded operators of the form ab + cd + e. 2024 IEEE 31st Symposium on Computer Arithmetic (ARITH 2024), Jun 2024, Málaga, Spain. hal-04461089v2

HAL Id: hal-04461089 https://inria.hal.science/hal-04461089v2

Submitted on 25 Apr 2024

HAL is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers. L'archive ouverte pluridisciplinaire **HAL**, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d'enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.



Distributed under a Creative Commons Attribution 4.0 International License

Useful applications of correctly-rounded operators of the form ab + cd + e

Tom Hubrecht*, Claude-Pierre Jeannerod[†], Jean-Michel Muller[‡]

* Département d'informatique de l'ENS, École Normale Supérieure, CNRS, PSL University, F-75005 Paris, France

[†] Inria, Univ. Lyon, Laboratoire LIP (UMR 5668, CNRS, ENS de Lyon, Inria, UCBL), F-69342 Lyon, France

[‡] CNRS, Univ. Lyon, Laboratoire LIP (UMR 5668, CNRS, ENS de Lyon, Inria, UCBL), F-69342 Lyon, France

Abstract—We show that the availability of fused arithmetic operators that evaluate expressions of the form ab + cd (FD2 instruction) or ab + cd + e (FD2A instruction) in floating-point arithmetic with one final rounding only would significantly facilitate many calculations that are hard to perform with high accuracy at small cost using only the traditional operations +, $-, \times, \div, \sqrt{}$, and fused multiply-add (FMA).

Index Terms—Floating-point arithmetic, correct rounding, fused operators, dot-product instructions, complex arithmetic, double-word arithmetic, rounding error analysis.

I. INTRODUCTION

A. Fused dot-product operators

The recent years have seen the emergence of new arithmetic instructions, that allow to perform fused, so-called "exact" dotproduct or dot-product-add calculations for low-dimensional vectors: the arithmetic operations involved are fused, with one final rounding only. Most frequently, these instructions correspond to some types of dot products, such as ab + cd or ab+cd+e or longer [1], [2], [3], [4], [5]. Typical applications of such instructions are the implementation of ML algorithms, but the FFT is also a target [6]. The predecessor of these instructions is the fused multiply-add (FMA), which evaluates expressions of the form ab + c with only one final rounding. The first widely available processor with an FMA was the IBM POWER [7]. In addition to speeding up (and generally making more accurate) the computation of dot products and the evaluation of polynomials, the availability of an FMA instruction allows efficient software implementation of correctly rounded division and square root [8], [9]. By making it easy to compute the error of a floating-point multiplication, it also paves the way for efficient double-word multiplication [10] and better complex arithmetic [11]. The goal of this paper is to show that these new fused instructions can bring to the field of numerical computing a progress comparable to the progress brought by the FMA 30 years ago.

So far, the fused dot-product-add instructions presented in the literature are mainly targeted at low-precision arithmetic. Furthermore, they frequently mix different formats, with the products being typically accumulated in a format larger than the format of the input operands. For example, Brunie [2] considers 16-bit inputs, an 80-bit accumulator, and w-bit outputs with $w \ge 32$. Bertaccini et al. [5] present such a dot-product-add operator for NN training, which takes four inputs a, b, c, d in some w-bit format with $w \in \{8, 16\}$ and one input e in some 2w-bit format, and outputs ab + cd + erounded (stochastically) to the 2w-bit format. Kalray's MPPA3 processors offer an FD2A operator for the binary32 format [6], and Desrentes et al. [4] investigate the general case of 2n + 1input operands with $n \ge 2$. The case of 8-bit input operands is studied further in [12], for various formats.

Lauter [13] describes software implementations of the fused, correctly-rounded operations a+b+c and ab+cd, and already lists many applications (accurate complex multiplication, faster compensated algorithms, simplification of the last rounding step in correctly-rounded elementary functions, accurate computation of discriminants of quadratic polynomials, ...).

Several other fused sum or dot-product operators have been considered in the literature, especially for ML applications; see in particular [14], [15], [16] as well as [4, §I.B]. They are of little use for our study, either because they do not provide correct rounding or because the way they round the results is not fully specified.

B. Our contributions

In this paper, we consider only homogeneous operators (that is, operators whose input and output operands are all represented in the same format) that evaluate expressions of the form ab+cd or ab+cd+e with correct rounding to nearest. We show that these correctly-rounded fused operators allow for simple and accurate implementation of *complex arithmetic* (section II); that they simplify the evaluation of some rounding errors, either exactly (error-free transforms) or with correct rounding, paving the way for efficient compensated algorithms (section III); that they allow for more accurate computations of products of three or four numbers, with an application to the accurate computation of integer powers (section IV); that they simplify the evaluation of the sign of the discriminant of some quadratic and cubic equations (section V) as well as the implementation of correctly-rounded multiplication by constants such as π (section VI); that they allow for implementing sums and inner products with fewer instructions and smaller error bounds than the classical algorithms (section VII); and that they allow for evaluating polynomials as accurately as the classical and compensated FMA-based Horner schemes but with significantly fewer instructions (section VIII). Finally, we show that such operators allow for simple and efficient implementations of *double-word arithmetic* (section IX).

For each of these eight application domains, the possible gains are quantified precisely and systematically via detailed operation counts and/or the derivation of rigorous, worst-case accuracy bounds (several of them being shown to be tight).

C. Notation and background

We assume radix-2, precision-p, floating-point (FP) arithmetic, with an unbounded exponent range (which means that the algorithms and properties presented below apply to an arithmetic conforming to the IEEE 754 standard [17] as long as underflows and overflows do not occur).

We write \mathbb{F} to denote the set of FP numbers. We also assume that the rounding function, denoted RN, is round to nearest with ties to even, which is the default in IEEE 754 arithmetic. The associated unit roundoff is then $u = 2^{-p}$.

Given $a, b, c, d, e \in \mathbb{F}$, we assume that the following two instructions are available for performing *fused dot-products of order two* (FD2), possibly *followed by an addition* (FD2A):

• **FD2**(a, b, c, d) returns RN(ab + cd);

• **FD2A**(a, b, c, d, e) returns **RN**(ab + cd + e).

The necessary background in FP arithmetic and error analysis can be found for example in [18]. In particular, recall that for $x \in \mathbb{R}$, $ufp(x) = 2^{\lfloor \log_2 |x| \rfloor}$ if $x \neq 0$ (and zero otherwise), that $ulp(x) = 2u \cdot ufp(x)$, and that the absolute and relative errors due to rounding to nearest are bounded, respectively, as $|RN(x) - x| \leq \frac{1}{2}ulp(x)$ and as $RN(x) = x(1 + \delta)$, $|\delta| \leq u/(1 + u)$. Recall also that if $x \in \mathbb{F}$, then it is an integral multiple of its ulp: $x \in ulp(x)\mathbb{Z}$.

II. COMPLEX ARITHMETIC

For complex FP numbers a + ib and c + id, addition and subtraction can be implemented perfectly well with basic FP arithmetic, as $RN(a \pm c) + i RN(b \pm d)$. However, for complex multiplication and division, even FMA-based implementations can suffer heavy cancellation in the real or imaginary part of the result, and so an FD2 instruction will be most useful here to get highly-accurate results.

Specifically, for complex products (a + ib)(c + id) with $a, b, c, d \in \mathbb{F}$, two FD2 calls yield the correctly-rounded real and imaginary parts RN(ac - bd) and RN(ad + bc). When c+id = a-ib (complex multiplication by complex conjugate), this ensures that the imaginary part of the result is zero; with an FMA, this algebraic property can be lost, since $RN(-ab + RN(ab)) \neq 0$ unless $ab \in \mathbb{F}$.

For complex division, we have (a + ib)/(c + id) = R + iIwith $R = \frac{ac+bd}{c^2+d^2}$ and $I = \frac{bc-ad}{c^2+d^2}$, so that three FD2 calls and two FP divisions suffice to get approximations \hat{R} and \hat{I} having high relative accuracy. More precisely, $\hat{R} = \text{RN}(\text{RN}(ac + bd)/\text{RN}(c^2 + d^2))$, where the relative error of FD2 is at most u/(1+u) and the one of FP division is at most $u - 2u^2$ [19]; the same holds for \hat{I} , and it is then easily checked that this implies

$$\widehat{R} = R(1+\delta_R), \quad \widehat{I} = I(1+\delta_I), \quad |\delta_R|, |\delta_I| \le 3u.$$

Another immediate consequence of the availability of an FD2 instruction is a more accurate complex absolute value

 $\sqrt{a^2 + b^2}$ (i.e., hypotenuse): with or without an FMA, the relative error is known to be tightly bounded by 2u [19], [20]; with an FD2, we now compute $\hat{h} := \text{RN}(\sqrt{\text{RN}(a^2 + b^2)})$. By applying twice the relative error bound u/(1+u), we deduce that

$$\widehat{h} = \sqrt{a^2 + b^2} (1 + \delta_h), \qquad |\delta_h| < \frac{3}{2}u.$$

Furthermore, this new bound is tight: taking a = 1 and b =RN $(\sqrt{3u}(1-u))$ gives $|\delta_h| > 1 - 1/\sqrt{1+3u-12u^2}$ for $p \ge 3$, which is $3u/2 - O(u^2)$ as $u \to 0$.

Note also that having an FD2A (and not just an FD2) gives directly a *complex fused multiply-add* operation, which is for example key for the FFT [4]: since (a+ib)(c+id)+e+if =ac - bd + e + i(ad + bc + f), two FD2A calls suffice to get the correctly-rounded values of the real and imaginary parts of this complex number.

Finally, another advantage of these FD2(A)-based solutions is to allow to avoid spurious underflows and overflows, either completely (for multiplication and complex FMA) or partially (for division and absolute value).

III. ERROR-FREE TRANSFORMS

It is well known that under mild conditions the error of an FP multiplication and the error of a (rounded to nearest) FP addition are FP numbers, and that these errors can be computed using the 2Sum, Fast2Sum, and 2MultFMA algorithms; see for example [18, §4.2]. These algorithms are called error-free transformations (EFT) in [21].

The 2Sum algorithm requires 6 FP additions and, assuming some kind of ordering on the input, the Fast2Sum algorithm needs only 3 FP additions. Without an FMA, computing the exact error of an FP product can be done using 17 FP operations [22], but with an FMA this becomes much easier and cheaper: if one computes $P_h = \text{RN}(ab)$ and $P_{\ell} = \text{RN}(ab - P_h)$, then $P_h + P_{\ell} = ab$ exactly.¹ In other words, the FMA makes it possible to implement the EFT of multiplication with only two operations. Similarly, as noted in [21], the availability of an ADD3 operation, which for $a, b, c \in \mathbb{F}$ returns RN(a + b + c), would greatly simplify the computation of the error of an FP addition: if $S_h = \text{RN}(a+b)$ and $S_{\ell} = \text{RN}(a+b-S_h)$, then $S_h + S_{\ell} = a + b$ exactly. This EFT was advocated in [24], motivated by efficiency issues and especially more efficient reproducible summation [25], [26].

Since the FMA and ADD3 are special cases of the FD2 and FD2A operations, respectively, having an FD2 makes it easy to obtain the error of an FP multiplication, and having an FD2A makes it easy to obtain the error of an FP addition: in each case, two operations now suffice to implement the EFT.

What about the error of the FMA ab + c - RN(ab + c)? As shown in [27], [28], this error is in general not an FP number but can always be expressed as the exact sum $e_1 + e_2$ of two FP numbers such that $e_1 = RN(e_1 + e_2)$; furthermore, the pair (e_1, e_2) can be produced using only basic FP operations, but the cost is high: Algorithm ErrFmac from [27] uses 2

¹We recall that we have assumed an unbounded exponent range. If this is not the case, then there is a condition on the exponents; see for example [23].

FMA, 1 multiplication, and 17 additions, for a total of 20 basic FP operations. Interestingly, with an ADD3 (and thus a fortiori with an FD2A), this operation count drops to 10: by inspecting Algorithm ErrFmac and its proof in [27], one sees that 2 FMA, 1 multiplication, 3 additions, and 4 ADD3 are sufficient.

If one needs only the correctly-rounded value of the error of the FMA, that is,

$$e_1 = \mathbf{RN}(ab + c - \mathbf{RN}(ab + c)), \tag{1}$$

then Algorithm ErrFmaNearest from [28] can be used: it performs 18 basic FP operations and, again, it is easily seen that with an ADD3 this operation count is halved too, dropping to 9. However, in this case, having an FD2A makes an even bigger difference, since e_1 in (1) can now be obtained using only 2 operations: one FMA followed by one FD2A. We will see in section VIII how this can be used to design a new, FMA/FD2A-based, compensated Horner algorithm, which avoids using the expensive EFT of the FMA without sacrificing for accuracy.

Similarly, one call to the FD2A is enough to deduce the correct rounding of the error of an FD2 (and thus the correct rounding of the errors in a complex product):

$$P_h = \mathbf{RN}(ab + cd),$$

$$P_\ell = \mathbf{RN}(ab + cd - P_h).$$
(2)

As for the FMA, we will have $P_h + P_\ell \neq ab + cd$ in general. However, the theorem below shows that the scheme in (2) does define an EFT for ab + cd provided some conditions on the ratio ab/cd are satisfied.

Theorem 1. Given $a, b, c, d \in \mathbb{F}$, let $P_h, P_\ell \in \mathbb{F}$ be as in (2). If $ab/2 \leq -cd \leq 2ab$, then

$$ab + cd = P_h + P_\ell.$$

Proof. We will use two basic properties: for $x, y, z, t \in \mathbb{R}$,

(i) if $|x| \leq 2^k$ then $|\operatorname{RN}(x) - x| \leq 2^{-p-1} \cdot 2^k$; (ii) if $|xy| \leq |zt|$ then $\operatorname{ulp}(x)\operatorname{ulp}(y) \leq 2\operatorname{ulp}(z)\operatorname{ulp}(t)$.

By assumption $ab \ge 0$ and $cd \le 0$, so -cd = |cd|. Since ab and cd play a symmetric role, we can assume further that $|cd| \le ab$, so $0 \le ab + cd \le |cd| \le 2^{2p} \text{ulp}(c) \text{ulp}(d)$. Using (i) we deduce that $|ab + cd - P_h| \le 2^{p-1} \text{ulp}(c) \text{ulp}(d)$. On the other hand, $ab + cd \in \mu\mathbb{Z}$ with $\mu :=$ $\min\{\text{ulp}(a) \text{ulp}(b), \text{ulp}(c) \text{ulp}(d)\}$. Since $|cd| \le ab$, it follows from (ii) that $\mu \ge \frac{1}{2} \text{ulp}(c) \text{ulp}(d) =: 2^e$, so that ab + cd and P_h are in $2^e\mathbb{Z}$. Hence $|ab + cd - P_h|$ is an integral multiple of 2^e that is less than or equal to 2^{p+e} . This implies $ab + cd - P_h \in \mathbb{F}$ and thus $P_\ell = \text{RN}(ab + cd - P_h) = ab + cd - P_h$. \Box

We conclude this section by noting that even when the exact sum $P_h + P_\ell$ differs from the exact value ab + cd, its relative error is extremely small, as we always have the following bound, which is asymptotically optimal:

$$P_h + P_\ell = (ab + cd)(1 + \delta), \qquad |\delta| \le \frac{1}{2}u^2.$$

This bound and its asymptotic optimality follow as a special case of the more general property below (which will also be useful in order to establish Property 2 in section IV, and when designing double-word arithmetic algorithms in section IX).

Property 1. Let $x \in \mathbb{R}$, $\hat{x} = \text{RN}(x)$, and $\hat{e} = \text{RN}(x - \hat{x})$. Then the relative error of the exact sum $\hat{x} + \hat{e}$ is at most $\frac{1}{2}u^2$. Furthermore, this bound is asymptotically optimal.

Proof. The bound is clear for x = 0, so let $x \neq 0$. Writing $e = x - \hat{x}$, we have $|e| \leq u \operatorname{ufp}(x) =: B$. This bound B is an integral power of 2, so $|\hat{x} + \hat{e} - x| = |\operatorname{RN}(e) - e| \leq \frac{u}{2}B = \frac{1}{2}u^2\operatorname{ufp}(x)$. Using $|x| \geq \operatorname{ufp}(x)$ then leads to the relative error bound. Asymptotic optimality can be shown by considering $x = 1 + u + \frac{1}{2}u^2$, for which $\hat{x} = 1 + 2u$, $\hat{e} \in \{-u, -u + u^2\}$, and $|(\hat{x} + \hat{e})/x - 1| = \frac{u^2/2}{1 + u + \frac{1}{2}u^2} \sim \frac{1}{2}u^2$ as $u \to 0$.

IV. PRODUCTS OF THREE OR FOUR FP NUMBERS

Note first that given $a, b, c, d \in \mathbb{F}$, one can compute the correctly-rounded value of abc + d very easily with an FD2A: compute $p_h := \text{RN}(ab), p_\ell := \text{RN}(ab - p_h)$, and $q_h := \text{RN}(p_hc + p_\ell c + d)$; since $p_h + p_\ell = ab$, we see immediately that q_h satisfies

$$q_h = \mathbf{RN}(abc + d). \tag{3}$$

This has several interesting properties. A first one (detailed in section V) is to ease the correct computation of the sign of the discriminant of some cubic equations. Evaluating expressions of the form RN(abc + d) can also be useful for elementary function libraries. For example, the cosine of a small argument x is typically approximated by $1 + x^2P(x)$ with P a low-degree polynomial.

Of course, (3) leads in particular to correctly-rounded products of three FP numbers. But what if we want to multiply four FP numbers together? Interestingly, the FD2A can again be used easily in order to get an approximation to *abcd* with relative error at most u, as follows: by applying the method underlying (3), compute $q_h := \text{RN}(abc)$ and $q_\ell := \text{RN}(abc - q_h)$, and then deduce

$$\widehat{r} := \operatorname{RN}(q_h d + q_\ell d). \tag{4}$$

Note that since $abc - q_h$ is not always in \mathbb{F} , the exact sum $q_h + q_\ell$ can differ from abc, and so \hat{r} may not be the correctlyrounded value of abcd. Such a situation occurs for example when (a, b, c, d) = (1 + 2u, 1 + 2u, 1 - u, 1): in this case, one can check that for $u \leq 2^{-3}$, $q_h = 1 + 2u$ and $\hat{r} = 1 + 4u$. Despite this, the relative accuracy of \hat{r} in (4) is essentially best possible, as the next property shows.

Property 2. For \hat{r} as in (4), $|\hat{r} - abcd| \leq u|abcd|$.

Proof. By Property 1, $q_h + q_\ell = abc(1+\delta)$ with $|\delta| \le \frac{1}{2}u^2$; on the other hand, $\hat{r} = (q_h d + q_\ell d)(1+\delta')$ with $|\delta'| \le u/(1+u)$. Hence $\hat{r} = abcd(1+\delta'')$ with $\delta'' = (1+\delta)(1+\delta') - 1$, which satisfies $|\delta''| \le u$.

For comparison, when evaluating *abcd* naively with three FP multiplications, the associated relative error bound is about 3*u*.

Longer products of the form $x_1x_2\cdots x_n$ with $n \ge 5$ will obviously benefit from Property 2 in about the same way, with the usual bound $\approx (n-1)u$ (see for example [29]) being replaced by a smaller one $\approx \frac{n}{4}u + (\frac{n}{4} - 1)u + u = nu/2$.

V. QUADRATIC AND CUBIC EQUATIONS

When solving quadratic or cubic equations in FP arithmetic, a first key step is to get the sign of their discriminant right, as this tells about the nature of the solutions and in particular the number of real solutions.

For quadratic equations $ax^2 + bx + c = 0$ with $a, b, c \in \mathbb{F}$, the discriminant is $\Delta = b^2 - 4ac$ and as already noted in [13] one FD2 instruction suffices to get $\text{RN}(\Delta)$ and thus $\text{sign}(\Delta)$. (In binary arithmetic, $4a \in \mathbb{F}$ is obtained exactly as RN(4a).)

By contrast, other ways to evaluate Δ , such as computing $\Delta_{+\times} := \operatorname{RN}(\operatorname{RN}(b^2) - \operatorname{RN}(4ac)), \Delta_{\operatorname{FMA_l}} := \operatorname{RN}(\operatorname{RN}(b^2) - 4ac)$, and $\Delta_{\operatorname{FMA_2}} := \operatorname{RN}(b^2 - \operatorname{RN}(4ac))$ can be so wrong that even the sign is not obtained correctly [30]. For example, one has $\Delta_{+\times} = 0$ and $\Delta \neq 0$ when (a, b, c) = (1/4 - u/2, 1, 1 + 2u). One can also have $\Delta_{\operatorname{FMA_l}} < 0$ or $\Delta_{\operatorname{FMA_2}} < 0$, while $\Delta = 0$, which leads to conclude wrongly that there is no real solution; an example for $\Delta_{\operatorname{FMA_l}}$ is given by (a, b, c) = (1/4 - u/4, 1 - u, 1 - u).

An FD2 instruction can also be immediately useful when solving so-called depressed cubic equations $x^3 + bx + c = 0$ with $b, c \in \mathbb{F}$. Here the discriminant is $\Delta = -4b^3 - 27c^2$ and its sign can be computed wrongly by direct implementations, with or without FMA. For example, in binary32 arithmetic with b = -14628329 and c = 21534767104, we have $\Delta_{\text{FMA}} < 0 < \Delta$ with $\Delta_{\text{FMA}} := \text{RN}(-\text{RN}(4b \cdot \text{RN}(b^2)) - c \cdot \text{RN}(27c))$.

With an FD2 instruction, however, one can use the solution presented in section IV to easily obtain the correctly-rounded products $\delta_1 = \text{RN}(4b^3)$ and $\delta_2 = \text{RN}(27c^2)$, and then compute $\Delta_{\text{FD2}} = \text{RN}(-\delta_1 - \delta_2)$. By monotonicity of rounding, this approach ensures that $\Delta_{\text{FD2}} > 0$ implies $\Delta > 0$ (and that there are three distinct real roots), and that $\Delta_{\text{FD2}} < 0$ implies $\Delta < 0$ (and that there is one real root). Hence only the case $\Delta_{\text{FD2}} = 0$ will require more work in order to compute sign(Δ) correctly.

VI. CORRECTLY-ROUNDED MULTIPLICATION BY REAL CONSTANTS

In [31], the authors consider the problem of performing the correctly rounded (to nearest) multiplication by a constant $C \notin \mathbb{F}$. Typical useful values of C are π , $1/\pi$, $\ln(2)$, $\sqrt{2}$, etc. Assuming that $C_h = \text{RN}(C)$ and $C_\ell = \text{RN}(C - C_h)$ are precomputed, for an input $x \in \mathbb{F}$ they assume that the exact product $y^* = Cx$ is approximated by

$$y_1 = \operatorname{RN}(C_h x + \operatorname{RN}(C_\ell x)), \tag{5}$$

whose computation requires an FP multiplication followed by an FMA. There are values of C and x for which $y \neq \text{RN}(y^*)$. However, it is shown in [31] that for a given C, a simple algorithm can check if $y_1 = \text{RN}(y^*)$ for all $x \in \mathbb{F}$, and it is observed that this is indeed the case for many useful values of C. The underlying reasoning is as follows:

- First, there are ≤ 4 values of the significand of x for which y₁ and y^{*} may belong to different binades. One easily checks if y₁ = RN(y^{*}) for these values.
- Then, for all other numbers x and with $\epsilon := |C (C_h + C_\ell)|$, one can bound $|y_1 y^*|$ by

$$\frac{1}{2}\mathrm{ulp}(y^*) + \eta_1, \quad \eta_1 := \frac{1}{2}\mathrm{ulp}(C_\ell x) + \epsilon |x|. \tag{6}$$

• Finally, (6) implies that if $y_1 \neq \text{RN}(y^*)$, then Cx is within distance $\leq \eta_1$ from the middle of two consecutive FP numbers. This in turn implies that C is very close to a rational number of the form (2A + 1)/(2B) with A and B two integers between 2^{p-1} and 2^p , which can be checked using the continued-fraction expansion of C.

Method 3 in [31] implements this strategy, and either tells that there is no x such that $y_1 \neq \text{RN}(y^*)$, or gives all values x (up to a multiplication by a power of 2) for which $y_1 \neq \text{RN}(y^*)$.

With an FD2 instruction we can change the way the approximation to y^* is computed and, instead of (5), use

$$y_2 = \mathbf{RN}(C_h x + C_\ell x).$$

We easily obtain that there are at most 2 values of the significand of x for which y and y^* may belong to different binades (as previously, one checks these values separately), and that for all other values we have

$$|y_2 - y^*| \le \frac{1}{2} ulp(y^*) + \eta_2, \quad \eta_2 := \epsilon |x|.$$

Hence, if $y_2 \neq \text{RN}(y^*)$, then Cx is within distance $\leq \eta_2$ from the middle of two consecutive FP numbers. As $\eta_2 \leq \eta_1$, if Method 3 in [31] tells us there is no x such that $y_1 \neq \text{RN}(y^*)$, then there is no x such that $y_2 \neq \text{RN}(y^*)$ either. If Method 3 yields x such that $y_1 \neq \text{RN}(y^*)$, then we must check if $y_2 =$ $\text{RN}(y^*)$. Using this strategy we can show that with π , $1/\pi$, $\ln(2)$, $1/\ln(2)$, $\ln(10)$, $1/\ln(10)$, in the binary32, binary64, and binary128 formats we always have $y_2 = \text{RN}(y^*)$.

VII. INNER PRODUCTS AND SUMS

Another natural application of the FD2A is the computation of inner products with both fewer instructions and fewer rounding errors than when just using an FMA. Given two floating-point vectors $x, y \in \mathbb{F}^n$, recall from [32] that a sequence of *n* FMAs suffices to produce an approximation \hat{r}_{FMA} to the inner product $x^T y$ such that²

$$|\widehat{r}_{\text{FMA}} - x^T y| \le n u |x|^T |y|.$$
(7)

This bound is asymptotically optimal: for fixed n, there exist $x, y \in \mathbb{F}^n$ such that both sides of (7) are asymptotically equivalent as $u \to 0$. For example, with $x^T = [1+2u, 1, \ldots, 1]$ and $y^T = [1-u, u-u^2, \ldots, u-u^2]$, we have $\hat{r}_{\text{FMA}} = 1$ and $x^T y = 1 + nu - (n+1)u^2$.

²Note that instead of nu in (7), a term of the form $(1 + u)^n - 1 = nu + O(u^2)$ can be obtained classically, following [33, Ch. 3]; however, the fact that the term $O(u^2)$ can be removed is a consequence of [32, Lemma 2.1].

With an FD2A this scheme is easily adapted by considering the following recursion: let $\hat{r}_1 := \text{RN}(x_1y_1 + x_2y_2)$ and, for $i = 2, \ldots, \lceil n/2 \rceil$, let

$$\widehat{r}_i := \mathbf{RN}(\widehat{r}_{i-1} + x_{2i-1}y_{2i-1} + x_{2i}y_{2i}),$$

with $x_{n+1} = y_{n+1} = 0$ if *n* is odd. As expected, the output $\hat{r}_{\text{FD2A}} := \hat{r}_{\lceil n/2 \rceil}$ has an error bound about twice smaller than the one in (7). The next theorem makes this claim precise.

Theorem 2. Given $x, y \in \mathbb{F}^n$, a sequence of $\lceil n/2 \rceil$ FD2A instructions suffices to produce an approximation $\hat{r}_{\text{FD2A}} \in \mathbb{F}$ to the inner product $x^T y$ such that

$$|\widehat{r}_{\scriptscriptstyle FD2A} - x^T y| \le m u |x|^T |y|, \quad m := \lceil n/2 \rceil$$

Furthermore, this bound is asymptotically optimal.

 $\begin{array}{l} \textit{Proof. Let the vectors } z, ε, δ, s ∈ ℝ^m \text{ be defined by } z_i = x_{2i-1}y_{2i-1} + x_{2i}y_{2i}, \ s_i = \hat{r_i}, \ s_1 = z_1 + \delta_1 = z_1(1 + ε_1) \\ \textit{and, for } i ≥ 2, \ s_i = z_i + s_{i-1} + \delta_i = (z_i + s_{i-1})(1 + ε_i). \\ \textit{For } i ≥ 2, \textit{ we thus have } s_i = \texttt{RN}(s_{i-1} + z_i) \textit{ and } |\delta_i| = |\texttt{RN}(s_{i-1} + z_i) - (s_{i-1} + z_i)| = \min_{f ∈ 𝔅} |f - (s_{i-1} + z_i)|; \textit{ since } \\ s_{i-1} ∈ 𝔅, \textit{ taking in particular } f = s_{i-1} \textit{ implies that } |\delta_i| ≤ |z_i|. \\ \textit{Hence the assumption needed to apply [32, \textit{Lemma 2.1] to } \\ (z, ε, δ, s) \textit{ is satisfied and since } z_1 + \dots + z_m = x^T y \textit{ and } \\ |\hat{r}_m - x^T y| ≤ mu|x|^T|y|. \\ \textit{The asymptotic optimality of this \\ \textit{bound can be seen by considering } x^T = [1 + 2u, 0, 1, 0, 1, \ldots], \\ \textit{and } y^T = [1 - u, 0, u - u^2, 0, u - u^2, \ldots], \textit{ for which we have } \\ \widehat{r}_m = 1 \textit{ and } x^T y = 1 + mu - (m + 1)u^2. \\ \end{aligned}$

Alternatively, one could approximate $x^T y$ by first computing the *m* values $\hat{z}_i := \operatorname{RN}(x_{2i-1}y_{2i-1} + x_{2i}y_{2i})$ and then, if $m \ge 2$, adding them together by means of a ternary summation tree having $\lfloor m/2 \rfloor$ inner nodes. This would use *m* calls to FD2 followed by $\lfloor m/2 \rfloor$ calls to ADD3, and thus $m + \lfloor m/2 \rfloor \approx 3n/4$ operations. Although this operation count is larger than *m* by about n/4, the error bound associated with this second approach will be smaller than the one in Theorem 2 by at least about n/4, and thus at least about twice smaller. Specifically, classical analysis as in [33, Ch. 4] would show that the constant *mu* obtained so far can be replaced by $(h + 1)u + O(u^2)$, with *h* denoting the height of the underlying ternary tree. Since this tree has $\lfloor m/2 \rfloor$ inner nodes, its height *h* ranges from $\lceil \log_3 \lfloor m/2 \rfloor \rceil$ to $\lfloor m/2 \rfloor$. Hence $(h + 1)u \lesssim m/2 \cdot u \approx n/4 \cdot u$.

Another way to reduce even further the error bound (7) is via *compensation*: besides the FMA-based recursion

$$\widehat{r}_i := \operatorname{RN}(\widehat{r}_{i-1} + x_i y_i), \quad i = 1, \dots, n,$$

which yields $\hat{r}_{\text{FMA}} := \hat{r}_n$, one computes correctly-rounded approximations to the errors $e_i := \hat{r}_{i-1} + x_i y_i - \hat{r}_i$ by means of n calls to FD2A:

$$\widehat{e}_i := \operatorname{RN}(\widehat{r}_{i-1} + x_i y_i - \widehat{r}_i), \quad i = 1, \dots, n$$

(Here we set $\hat{r}_0 := 0$ and so an FMA suffices to produce $e_1 = x_1y_1 - \hat{r}_1 \in \mathbb{F}$.) These *n* values \hat{e}_i are then added together by means of $\lfloor n/2 \rfloor$ calls to ADD3 into a single value

 \hat{e} . Finally, this value, which approximates the exact overall error $e_1 + \cdots + e_n = x^T y - \hat{r}_{\text{FMA}}$, is used to produce the refined approximation $\hat{r}_{\text{COMP}} := \text{RN}(\hat{r}_{\text{FMA}} + \hat{e})$.

Theorem 3. Under the assumptions of the previous theorem, a sequence of n FMA instructions and $\lfloor 3n/2 \rfloor$ FD2A instructions suffices to produce $\hat{r}_{\text{COMP}} \in \mathbb{F}$ such that

$$|\widehat{r}_{{}_{COMP}} - x^T y| \le u |x^T y| + \lambda_{{}_{COMP}} |x|^T |y|$$

with $\lambda_{{}_{COMP}} = (\frac{1}{2}n^2 + n)u^2 + (\frac{1}{4}n^3 + \frac{1}{4}n^2)u^3.$

Proof. For readability define here $r = x^T y$, $\tilde{r} = |x|^T |y|$, $\hat{r} = \hat{r}_{\text{FMA}}$, $e = \sum_{i=1}^n e_i$, and v = u/(1+u). Since $\hat{r}_{\text{COMP}} = \text{RN}(\hat{r} + \hat{e})$ and $\hat{r} + e = r$, we have $|\hat{r}_{\text{COMP}} - r| = |(\hat{r} + \hat{e})(1 + \delta) - r| = |r\delta + (\hat{e} - e)(1 + \delta)|$, $|\delta| \le v$. Hence

$$|\hat{r}_{\text{COMP}} - r| \le v|r| + (1+v)|\hat{e} - e|$$
(8)

and it remains to bound $|\widehat{e}-e|$. First, $|\widehat{e}-e| \leq |\widehat{e}-\sum_{i=1}^{n} \widehat{e}_i| + \sum_{i=2}^{n} |\widehat{e}_i - e_i|$ since $\widehat{e}_1 = e_1$. On the one hand, $|\widehat{e}_i - e_i| \leq v|e_i|$ for all *i*. On the other hand, adding the \widehat{e}_i 's recursively by means of $\lfloor n/2 \rfloor$ ADD3, we deduce from [32, Lemma 2.1] that $|\widehat{e}-\sum_{i=1}^{n} \widehat{e}_i| \leq \lfloor n/2 \rfloor v \sum_{i=1}^{n} |\widehat{e}_i|$. Hence

$$|\widehat{e} - e| \le \left\lfloor \frac{n}{2} \right\rfloor v |e_1| + \left(\left\lfloor \frac{n}{2} \right\rfloor (v + v^2) + v \right) \sum_{i=2}^n |e_i|.$$
(9)

Second, to bound $|e_i|$, let us define $r_i = \sum_{j \le i} x_j y_j$ and $\tilde{r}_i = \sum_{j \le i} |x_j y_j|$. Since the \hat{r}_i 's are produced recursively by the FMA, we deduce from [32, Lemma 2.1] that $|\hat{r}_i - r_i| \le i v \tilde{r}_i$. Consequently, for $i \ge 1$ and with $\hat{r}_0 = 0$, $|e_i| \le v |\hat{r}_{i-1} + x_i y_i| \le v (\tilde{r}_i + (i-1)v \tilde{r}_{i-1})$. Using $\tilde{r}_{i-1} \le \tilde{r}_i \le \tilde{r}_n = \tilde{r}$ for $i \le n$, we obtain $|e_i| \le (v + (i-1)v^2)\tilde{r}$ and, therefore,

$$|e_1| \le v\widetilde{r}$$
 and $\sum_{i=2}^n |e_i| \le (n-1)v(1+\frac{n}{2}v)\widetilde{r}.$ (10)

The claimed error bound then follows from the inequalities (8), (9), (10). Furthermore, it is obtained using n + 1FMA, n - 1 FD2A, and $\lfloor n/2 \rfloor$ ADD3.

What is interesting with Theorem 3 is that $\lambda_{\text{COMP}} \sim \frac{1}{2}n^2u^2$, which is about half of the classical one from [21], and that this smaller bound is obtained using only about 3n/2 FD2A besides the initial FMA-based recursion.

Note also that λ_{COMP} can be reduced even further by using a more balanced ternary tree when adding up the *n* approximate values \hat{e}_i 's (and thus without increasing the operation count): a ternary tree of height *h* leads to $\lambda_{\text{COMP}} \sim (h+1)nu^2$, so that with the minimal height $h = \lceil \log_3 \lfloor n/2 \rfloor \rceil$ we arrive at $\lambda_{\text{COMP}} \sim 0.91n \ln n u^2$.

Note finally that similar gains can of course be obtained for the special case of summation, where $y^T = [1, 1, ..., 1]$, using exclusively the ADD3 instruction.

VIII. POLYNOMIAL EVALUATION

To be able to rapidly evaluate a low-degree polynomial on a specific architecture, taking into account the number of available operators, the depth of the arithmetic pipelines, etc., one can conduct an exhaustive or heuristic-based search among the various possible evaluation schemes [34], [35]. Having an FD2A instruction increases the number of possible schemes, potentially leading to a better optimum. We illustrate this by focusing on the most classical way (Horner's scheme with an FMA) and detailing how it can be adapted to exploit the FD2A when computing $a(x) = \sum_{i=0}^{n} a_i x^i$ given $x, a_i \in \mathbb{F}$.

Recall first that with an FMA, Horner's recursion is given by $\hat{r}_0 := a_n$ and

$$\hat{r}_i := \text{RN}(\hat{r}_{i-1}x + a_{n-i}), \quad i = 1, \dots, n,$$
 (11)

and that this sequence of n FMA instructions yields an approximation $\hat{r}_{\text{FMA}} := \hat{r}_n \in \mathbb{F}$ such that

$$|\widehat{r}_{\text{FMA}} - a(x)| \le \gamma_n \widetilde{a}(|x|), \tag{12}$$

where $\gamma_n := nu/(1 - nu)$ if nu < 1 and $\tilde{a}(x) := \sum_i |a_i|x^i$; see [36, §6.1]. The bound (12) is essentially best possible for fixed n and as $u \to 0$, since taking in particular $x = a_n = 1$ and $a_{n-i} = u - u^2$ for $i \le n$ gives $\hat{r}_{\text{FMA}} = 1$ and $a(x) = \tilde{a}(|x|) = 1 + nu - nu^2$.

With an FD2A, the recursion in (11) can be adapted immediately when $n \ge 2$: first compute $\hat{y} := \text{RN}(x^2)$ and then, with $m = 2\lfloor n/2 \rfloor$, compute

$$\widehat{r}_i := \operatorname{RN}(\widehat{r}_{i-1}\widehat{y} + a_{m-2i+1}x + a_{m-2i}), \quad i = 1 : \lfloor n/2 \rfloor.$$

(Here, $\hat{r}_0 := a_n$ if *n* is even, and $\hat{r}_0 := \text{RN}(a_n x + a_{n-1})$ if *n* is odd.) This scheme uses about n/2 FD2A instructions. In terms of accuracy, if x^2 is computed exactly, then the factor $\gamma_n \approx nu$ in (12) can be decreased down to $\gamma_{\lceil n/2 \rceil} \approx \frac{1}{2}nu$; unfortunately, in the general case where $\hat{y} \neq x^2$, the rounding error due to this initial squaring operation must be taken into account, bringing the factor γ_n back and thus leading to the same a priori bound (12) as for Horner with FMA. This is in contrast with what we have seen for inner products in section VII and Theorem 2.

There is, however, a more interesting way of exploiting the FD2A instruction in the context of Horner evaluation: the ability of the FD2A to recover the correctly-rounded value of the error of an FMA makes it possible to design a very simple *compensated* version of (11), as follows. Set $\hat{r}_0 := a_n$ and $\hat{e} := 0$; then compute for *i* from 1 to *n*

$$\begin{aligned} \widehat{r}_i &:= \operatorname{RN}(\widehat{r}_{i-1}x + a_{n-i}); & // \operatorname{FMA} \\ \widehat{e}_i &:= \operatorname{RN}(\widehat{r}_{i-1}x + a_{n-i} - \widehat{r}_i); & // \operatorname{FD2A} \\ \widehat{e} &:= \operatorname{RN}(\widehat{e}x + \widehat{e}_i); & // \operatorname{FMA} \end{aligned}$$

and finally return $\hat{r}_{\text{COMP}} := \text{RN}(\hat{r}_n + \hat{e}).$

Since \hat{e} is initialized to zero, its final value is obtained using n-1 FMA and is equal to the approximation to $\sum_{i=1}^{n} \hat{e}_i x^{n-i}$ produced by Horner with FMA. The result \hat{r}_{COMP} is thus obtained by means of 2n-1 FMA, n FD2A, and 1 addition, that is, 3n instructions.

An accuracy bound is given by the following theorem. Interestingly, this bound matches the best one from [36, §6.3.2], where it needed 19*n* FP instructions: *n* calls to an EFT for the FMA, n + 1 additions, and n - 1 FMA. (Remark that the EFT used in [36] is ErrFmac [27] without the last Fast2Sum, so its cost is of 17 FP instructions instead of 20.)

Theorem 4. Given $a_0, a_1, \ldots, a_n, x \in \mathbb{F}$, the compensated Horner algorithm described above yields an approximation $\widehat{r}_{COMP} \in \mathbb{F}$ to $a(x) = \sum_{i=0}^{n} a_i x^i$ such that

$$|\widehat{r}_{\text{COMP}} - a(x)| \le u|a(x)| + \gamma_n^2 \,\widetilde{a}(|x|).$$

Proof. Let r = a(x), $\tilde{r} = \tilde{a}(|x|)$, and v = u/(1+u). For $1 \leq i \leq n$, define also $r_i = \sum_{j=0}^{i} a_{n-j} x^{i-j}$ and let e_i be such that $\hat{r}_{i-1}x + a_{n-i} = \hat{r}_i + e_i$. Multiplying both sides of this equality by x^{n-i} and then summing for *i* from 1 to *n* yields after simplification $r = \hat{r}_n + e$, where $e = \sum_{i=1}^n e_i x^{n-i}$. Since $\hat{r}_{\text{COMP}} = (\hat{r}_n + \hat{e})(1+\delta)$ with $|\delta| \leq v$, it follows that

$$|\hat{r}_{\text{COMP}} - r| \le v|r| + (1+v)|\hat{e} - e|.$$
(13)

Let us now bound $|\hat{e} - e|$. Since \hat{e} is obtained by evaluating $\sum_{i=1}^{n} \hat{e}_i x^{n-i}$ by Horner with FMA, it follows from (12) and $|\hat{e}_i - e_i| \le v |e_i|$ that

$$|\widehat{e} - e| \le ((1+v)\gamma_{n-1} + v)\widetilde{e}, \tag{14}$$

where $\tilde{e} = \sum_{i=1}^{n} |e_i| |x|^{n-i}$. Now, $|e_i| \le u |\hat{r}_i|$ with \hat{r}_i obtained by evaluating r_i by Horner with FMA. Hence (12) implies $|\hat{r}_i| \le |r_i| + \gamma_i \sum_{j=0}^{i} |a_{n-j}| |x|^{i-j}$, so that $|e_i| |x|^{n-i} \le u(1 + \gamma_n)\tilde{r}$. Therefore, $\tilde{e} \le nu(1 + \gamma_n)\tilde{r}$ and the conclusion follows from combining this bound with the ones in (13) and (14). \Box

IX. DOUBLE-WORD ARITHMETIC

Double-word arithmetic (often referred to as *double-double* in the literature) deals with real numbers x represented as the unevaluated sums of two FP numbers x_h and x_ℓ such that $x_h = \text{RN}(x_h + x_\ell) = \text{RN}(x)$. Several algorithms for manipulating double-word (DW) numbers have been introduced since the pioneering work of Dekker [22], in particular in [37], [38], [10], [39], [40].

The availability of an FD2A instruction makes it possible to design new algorithms for DW arithmetic, that are cheaper in terms of numbers of instructions, often more accurate, and simpler to state and analyze than the traditional algorithms mentioned above. A key building-block will be the following implementation of the EFT for addition, which appears in [21].

Algorithm 1 : $TwoSum_Add3(a, b)$.	
1: $s \leftarrow \mathbf{RN}(a+b)$	
2: $e \leftarrow \text{RN}(a+b-s)$	// $a + b = e + s$ (EFT)

This algorithm performs only two operations, instead of six for 2Sum. It does not require a condition such as $|a| \ge |b|$ as Fast2Sum does. These features will, of course, benefit to all the DW algorithms described below.

A. Sum of a DW number and an FP number

This algorithm is deduced from Algorithm DWPlusFP in [10], using TwoSum_Add3 instead of 2Sum and Fast2Sum. It returns the same output in 5 FP instructions instead of 10, and one can proceed as in [10, §2] to check that an asymptotically-optimal bound on its relative error is 2u. The analysis is simpler as it avoids having to check the usual FastTwoSum condition $|s| \ge |e|$ used in [10].

Algorithm 2 : DWPlusFP_FD2A (a_h, a_ℓ, b) . Computes a DW approximation c to a + b.

- 1: $(s, f) \leftarrow \mathbf{TwoSum_Add3}(a_h, b)$
- 2: $e \leftarrow \operatorname{RN}(f + a_\ell)$
- 3: $(c_h, c_\ell) \leftarrow \mathbf{TwoSum_Add3}(s, e)$

B. Sum of two DW numbers

The authors of [10] analyze algorithm AccurateDWPlusDW of Hida, Li and Bailey's library [38]. They show that it has an asymptotically-optimal relative error bound of $3u^2/(1-4u)$. If an FD2A operation is available, one can use that algorithm with each 2Sum and Fast2Sum replaced by a TwoSum_Add3, which saves 10 operations without changing the error. However, the availability of an FD2A operation allows one to design an algorithm that is both cheaper and more accurate:

Algorithm 3 : AccDWPlusDW_FD2A $(a_h, a_\ell, b_h, b_\ell)$. Cor
putes a DW approximation z to $a + b$.
1: $(s_h, s_\ell) \leftarrow \mathbf{TwoSum_Add3}(a_h, b_h)$
2: $(t_h, t_\ell) \leftarrow \mathbf{TwoSum_Add3}(a_\ell, b_\ell)$
3: $(c_h, c_\ell) \leftarrow \textbf{TwoSum}_\textbf{Add3}(s_\ell, t_h)$
4: $(v_h, v_\ell) \leftarrow \mathbf{TwoSum_Add3}(s_h, c_h)$
5: $w \leftarrow \mathbf{RN}(t_{\ell} + v_{\ell} + c_{\ell})$
6: $(z_h, z_\ell) \leftarrow \mathbf{TwoSum}_\mathbf{Add3}(v_h, w)$

This algorithm uses 5 FP additions and 6 FD2A operations (instead of 20 additions for AccurateDWPlusDW), and its relative error is bounded by $2u^2$ (the proof is similar to the proof of AccurateDWPlusDW in [10]).

C. Product of a DW number by an FP number

Without an FD2A, multiplying a DW number by an FP number is a rather complex operation (for example, the most accurate of the three algorithms analyzed in [10] takes 10 FP operations and has a tight error bound of $\frac{3}{2}u^2 + 4u^3$). With an FD2A, this becomes much simpler and more accurate:

Algorithm 4 : DWTimesFP_FD2A (a_h, a_ℓ, b) .	Computes	а
DW approximation z to ab .		
1: $s \leftarrow \mathbf{RN}(a_h \cdot b + a_\ell \cdot b)$		

2: $e \leftarrow \text{RN}(a_h \cdot b + a_\ell \cdot b - s)$

3: $(z_h, z_\ell) \leftarrow \mathbf{TwoSum_Add3}(s, e)$

It follows from Property 1 that the relative error of this algorithm is at most $\frac{1}{2}u^2$. This bound is asymptotically optimal: with the values $a_h = 1 + 2u$, $a_\ell = \frac{3}{2}u^2$, and b = 1 - u, we obtain s = 1 and $e = u - u^2$, which leads to a relative error equal to $\frac{\frac{1}{2}u^2 - \frac{3}{2}u^3}{1 + u - \frac{1}{2}u^2 - \frac{3}{2}u^3} \sim \frac{1}{2}u^2$ as $u \to 0$.

As e = RN(ab - s), in most applications the last line of the algorithm can be omitted. That line is however necessary if we wish to always output a DW, as we may have $s \neq RN(s + e)$ (an example is with $a_h = 1 + 2u$, $a_\ell = \frac{5}{2}u^2$, b = 1 - u).

D. Product of two DW numbers

Three algorithms for computing the product of two DW numbers are analyzed in [41]. The most accurate of them has an error bound of $4u^2$ and requires 9 FP operations. The availability of an FD2A operation would improve the situation, both in terms of accuracy and number of operations. Consider the following algorithm, which uses 5 FP operations:

Algorithm	5 :	DWTimesDW_FD2A $(a_h, a_\ell, b_h, b_\ell)$.	Com-
putes a DW	appr	oximation z to ab .	

1: $(c_h, e) \leftarrow \mathbf{TwoProd}(a_h, b_h)$ 2: $c_\ell \leftarrow \mathbf{RN}(a_\ell \cdot b_h + a_h \cdot b_\ell + e)$ 3: $(z_h, z_\ell) \leftarrow \mathbf{TwoSum_Add3}(c_h, c_\ell)$

If $u \leq 2^{-4}$ then this algorithm has relative error $\leq 3u^2$. This can be shown as follows. Here $c_h + e = a_h b_h$ and the absolute error of $z = c_h + c_\ell$ is $|\epsilon_1 + \epsilon_2|$ with $\epsilon_1 = \text{RN}(a_\ell b_h + a_h b_\ell + e) - (a_\ell b_h + a_h b_\ell + e)$ and ϵ_2 the error due to $a_\ell b_\ell$ being ignored. With no loss of generality we assume $1 \leq a_h, b_h \leq 2 - 2u$. This implies that $|a_\ell|, |b_\ell| \leq u$, so that $|\epsilon_2| \leq u^2$.

- If $a_hb_h \leq 2$, then $|e| \leq u$ and, from [41, Lemma 2.11], $a_h+b_h \leq 3-2u$. Thus $|a_\ell b_h + a_h b_\ell + e| \leq u(a_h+b_h) + u < 4u$, so that $|\epsilon_1| \leq 2u^2$. Therefore, $|(c_h+c_\ell)-ab| \leq 3u^2$. As $ab \geq 1$, the relative error is $\leq 3u^2 =: B_1$.
- If $a_hb_h > 2$, then $|e| \le 2u$, so that $|a_\ell b_h + a_h b_\ell + e| \le 2u(2-2u) + 2u < 6u$ and $|\epsilon_1| \le 4u^2$. Hence $|\epsilon_1 + \epsilon_2| \le 5u^2$. Since $ab \ge a_h(1-u) \cdot b_h(1-u)$, the relative error is $\le 5u^2/(2(1-u)^2) =: B_2$.

If $u \leq 2^{-4}$ then $B_2 \leq B_1$, which gives the final bound $3u^2$.

Furthermore, this bound is asymptotically optimal. To see this, let $a_h = b_h = 1 + 2u$ and $a_\ell = b_\ell = u - u^2$. One can check that $c_h = 1 + 4u$, $e = 4u^2$, and $c_\ell = 2u + 4u^2$, so that the relative error of $c_h + c_\ell$ (and thus of $z_h + z_\ell$ as well) is $\frac{3u^2 - 6u^3 + u^4}{1 + 6u + 7u^2 - 6u^3 + u^4} \sim 3u^2$ as $u \to 0$.

A more accurate calculation is possible at a higher cost (8 FP operations), using the following algorithm.

Algorithm 6 : DWTimesDW2_FD2A $(a_h, a_\ell, b_h, b_\ell)$. Computes a DW approximation z to ab.

1: $(c, e) \leftarrow \mathbf{TwoProd}(a_h, b_h)$ 2: $v_h \leftarrow \mathrm{RN}(a_\ell \cdot b_h + a_h \cdot b_\ell)$ 3: $v_\ell \leftarrow \mathrm{RN}(a_\ell \cdot b_h + a_h \cdot b_\ell - v_h)$ 4: $w \leftarrow \mathrm{RN}(v_\ell + a_\ell b_\ell)$ 5: $s \leftarrow \mathrm{RN}(e + v_h + w)$ 6: $(z_h, z_\ell) \leftarrow \mathrm{TwoSum}_A \mathrm{Add3}(c, s)$

The relative error of this algorithm is bounded by $\frac{4u^2+3u^3}{2(1-u)^2} \sim 2u^2$. The proof is similar to the one of the previous algorithm.

E. Division of a DW number by a DW number

This algorithm has the same general structure as DW-DivDW3 in [10], whose relative error bound is $9.8u^2$. Its relative error is $\leq 7.8u^2$ when $p \geq 13$. The proof follows the steps of the proof of [10, Theorem 7.2] (using the error

Algorithm 7 : DWDivDW_FD2A $(a_h, a_\ell, b_h, b_\ell)$. Computes a DW approximation z to a/b.

- 1: $t_h \leftarrow \text{RN}(1/b_h)$
- 2: $r_h \leftarrow \mathbf{RN}(1 t_h \cdot b_h)$
- 3: $r_{\ell} \leftarrow -\mathbf{RN}(t_h \cdot b_{\ell})$
- 4: $(e_h, e_\ell) \leftarrow \mathbf{TwoSum_Add3}(r_h, r_\ell)$
- 5: $(\delta_h, \delta_\ell) \leftarrow \mathbf{DWTimesFP}_FD2A(e_h, e_\ell, t_h)$
- 6: $(m_h, m_\ell) \leftarrow \mathbf{DWPlusFP_FD2A}(\delta_h, \delta_\ell, t_h)$
- 7: $(z_h, z_\ell) \leftarrow \mathbf{DWTimesDW}_\mathbf{FD2A}(a_h, a_\ell, m_h, m_\ell)$

bounds on DWTimesFP_FD2A, DWPlusFP_FD2A, and DW-TimesDW_FD2A given in this paper).

F. Square root of a DW number

The authors of [40] analyze and formally prove a DW square root algorithm (that already appears in [42], without the final renormalization). The availability of an FD2A operator makes it possible to fuse two steps of this algorithm, and to replace its final renormalization by a call to TwoSum Add3. This gives

Algorithm 8 : DWSqrt_FD2A (a_h, a_ℓ) . Computes a DW approximation z to \sqrt{a} .

1: if $a_h = 0$ then $(z_h, z_\ell) \leftarrow (0, 0)$ 2: else 3: $s_h \leftarrow \operatorname{RN}(\sqrt{a_h})$ 4: $\rho \leftarrow \operatorname{RN}(a_h - s_h^2 + a_\ell)$ 5: $s_\ell \leftarrow \operatorname{RN}(\rho/(2 \cdot s_h))$ 6: $(z_h, z_\ell) \leftarrow \operatorname{TwoSum}\operatorname{Add3}(s_h, s_\ell)$

The relative error bound is the same as for the original algorithm, namely, $(25/8)u^2 = 3.125u^2$. The proof is the same as the one given in [40, Theorem 3.6].

REFERENCES

- Y. Tao, G. Deyuan, F. Xiaoya, and J. Nurmi, "Correctly rounded architectures for floating-point multi-operand addition and dot-product computation," in ASAP Proc., 2013.
- [2] N. Brunie, "Modified fused multiply and add for exact low precision product accumulation," in ARITH Proc., 2017.
- [3] L. Bertaccini, G. Paulin, T. Fischer, S. Mach, and L. Benini, "MiniFloat-NN and ExSdotp: An ISA extension and a modular open hardware unit for low-precision training on RISC-V cores," in *ARITH Proc.*, 2022.
- [4] O. Desrentes, B. Dupont de Dinechin, and F. de Dinechin, "Exact fused dot product add operators," in ARITH Proc., 2023.
- [5] L. Bertaccini, G. Paulin, M. Cavalcante, T. Fischer, S. Mach, and L. Benini, "MiniFloats on RISC-V cores: ISA extensions with mixedprecision short dot products," *IEEE Trans. Emerging Topics in Computing*, 2024.
- [6] B. Dupont de Dinechin, J. Hascoet, and O. Desrentes, "In-Place Multicore SIMD Fast Fourier Transforms," in *HPEC Proc.*, 2023.
- [7] R. K. Montoye, E. Hokonek, and S. L. Runyan, "Design of the IBM RISC System/6000 floating-point execution unit," *IBM J. Res. Dev.*, 1990.
- [8] P. Markstein, "Computation of elementary functions on the IBM RISC System/6000 processor," *IBM J. Res. Dev.*, 1990.
- [9] M. A. Cornea-Hasegan, R. A. Golliver, and P. Markstein, "Correctness proofs outline for Newton–Raphson based floating-point divide and square root algorithms," in *ARITH Proc.*, 1999.
- [10] M. Joldes, J.-M. Muller, and V. Popescu, "Tight and rigorous error bounds for basic building blocks of double-word arithmetic," ACM Trans. Math. Soft., 2017.

- [11] C.-P. Jeannerod, N. Louvet, and J.-M. Muller, "Further analysis of Kahan's algorithm for the accurate computation of 2×2 determinants," *Math. Comp.*, 2013.
- [12] O. Desrentes, B. Dupont de Dinechin, and J. Le Maire, "Exact dot product accumulate operators for 8-bit floating-point deep learning," in *DSD Proc.*, 2023.
- [13] C. Lauter, "An efficient software implementation of correctly rounded operations extending FMA: a + b + c and a × b + c × d," in ACSSC Proc., 2017.
- [14] J. Sohn and E. E. Swartzlander, "A fused floating-point four-term dot product unit," *IEEE Trans. Circuits and Systems*, 2016.
- [15] B. Hickmann and D. Bradford, "Experimental analysis of matrix multiplication functional units," in ARITH Proc., 2019.
- B. Hickmann *et al.*, "Intel Nervana Neural Network Processor-T (NNP-T) fused floating point many-term dot product," in *ARITH Proc.*, 2020.
- [17] IEEE, IEEE Standard for Floating-Point Arithmetic (IEEE Std 754-2019), 2019.
- [18] S. Boldo, C.-P. Jeannerod, G. Melquiond, and J.-M. Muller, "Floatingpoint arithmetic," *Acta Numerica*, 2023.
- [19] C.-P. Jeannerod and S. M. Rump, "On relative errors of floating-point operations: optimal bounds and applications," *Math. Comp.*, 2018.
- [20] C.-P. Jeannerod, J.-M. Muller, and A. Plet, "The classical relative error bounds for computing $\sqrt{a^2 + b^2}$ and $c/\sqrt{a^2 + b^2}$ in binary floating-point arithmetic are asymptotically optimal," in *ARITH Proc.*, 2017.
- [21] T. Ogita, S. M. Rump, and S. Oishi, "Accurate sum and dot product," SIAM J. Sci. Comput., 2005.
- [22] T. J. Dekker, "A floating-point technique for extending the available precision," *Numer. Math.*, 1971.
- [23] S. Boldo, "Pitfalls of a full floating-point proof: Example on the formal proof of the Veltkamp/Dekker algorithms," in *IJCAR Proc.*, 2006.
- [24] M. Dukhan, R. Vuduc, and J. Riedy, "Wanted: Floating-point add roundoff error instruction," 2016, preprint (https://arxiv.org/abs/1603.00491) and PMMA16 slides (https://blogs.fau.de/hager/bofs/isc16-workshop).
- [25] W. Arhens, J. Demmel, and H. D. Nguyen, "Algorithms for efficient reproducible floating point summation," ACM Trans. Math. Soft., 2020.
- [26] J. Riedy and J. Demmel, "Augmented arithmetic operations proposed for IEEE-754 2018," in ARITH Proc., 2018.
- [27] S. Boldo and J.-M. Muller, "Some functions computable with a fusedmac," in ARITH Proc., 2005.
- [28] —, "Exact and approximated error of the FMA," *IEEE Trans. Comput.*, 2011.
- [29] S. M. Rump, F. Bünger, and C.-P. Jeannerod, "Improved error bounds for floating-point products and Horner's scheme," *BIT Num. Math.*, 2016.
- [30] W. Kahan, "Lecture notes on the status of IEEE-754," 1997, https:// people.eecs.berkeley.edu/~wkahan/ieee754status/IEEE754.PDF.
- [31] N. Brisebarre and J.-M. Muller, "Correctly rounded multiplication by arbitrary precision constants," *IEEE Trans. Comp.*, 2008.
- [32] M. Lange and S. M. Rump, "Error estimates for the summation of real numbers with application to floating-point summation," *BIT Numer. Math.*, 2017.
- [33] N. J. Higham, Accuracy and Stability of Numerical Algorithms, 2nd ed. SIAM, Philadelphia, PA, 2002.
- [34] G. Revy, "Implementation of binary floating-point arithmetic on embedded integer processors: polynomial evaluation-based algorithms and certified code generation," Ph.D. dissertation, U. Lyon - ÉNSL, 2009.
- [35] M. Cornea, J. Harrison, and P. T. P. Tang, Scientific Computing on Itanium-Based Systems. Intel Press, 2002.
- [36] N. Louvet, "Algorithmes compensés en arithmétique flottante : Précision, validation, performances," Ph.D. dissertation, U. Perpignan, 2007.
- [37] X. Li et al., "Design, implementation and testing of extended and mixed precision BLAS," ACM Trans. Math. Soft., 2002.
- [38] Y. Hida, X. S. Li, and D. H. Bailey, "C++/Fortran-90 double-double and quad-double package, release 2.3.17," 2012, accessible electronically at https://www.davidhbailey.com/dhbsoftware/.
- [39] V. Popescu, "Towards fast and certified multiple-precision librairies," Ph.D. dissertation, U. Lyon - ÉNSL, 2017.
- [40] V. Lefèvre, N. Louvet, J.-M. Muller, J. Picot, and L. Rideau, "Accurate calculation of Euclidean norms using double-word arithmetic," ACM Trans. Math. Soft., 2023.
- [41] J.-M. Muller and L. Rideau, "Formalization of double-word arithmetic, and comments on "Tight and rigorous error bounds for basic building blocks of double-word arithmetic"," ACM Trans. Math. Soft., 2022.
- [42] S. M. Rump and M. Lange, "Faithfully rounded floating-point computation," ACM Trans. Math. Soft., 2020.