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Octree based Voxel Model for representation of spatial conflicts across multiple design domains

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Abstract. This paper discusses use of octree based voxel model for representation of spatial conflicts across multiple design domains. A framework has been developed to create octree based voxel model linked with intended empty spaces in product, which are associated with design requirements, product lifecycle states and connected design domains. Knowledge in System Modelling Language (SysML), is used to select criteria for building octree voxel model. A case study of Coupled cavity travelling wave tube (CCTWT) Slow Wave structure (SWS) design has been taken to showcase the code capabilities. Octree voxels inside CAD platform show and represent spatial conflicts, detected by associativity modelling of Empty space blocks inside CAD along with the Product knowledge in SysML model.

Keywords: Octrees, Voxelization, Spatial Conflicts

1 Introduction

Product designs are carried out with objectives to satisfy multifunctional domain requirements. In every product design, some spaces are intentionally left empty to satisfy specific requirements of different domains. For example, space may have been provided for electrical insulation, thermal isolation, clearance for moving part, port accessibility, Electron beam transmission or Radio frequency signal travel, expansion of material, tool access, sensors, bolts accessibility, welding tools, or for removal / replacement of parts. There may be cases, where the same space is used for different purposes but in different product states. The space, which has been provided for moving parts clearance in operation state also serves the purpose for accessibility in maintenance state. That is the same space, which is used for two different purposes but in different states. Knowledge regarding these space usages for moving part clearance under operation state, may not have been documented or is not available to designer, who is searching for empty space to locate new module into the product. This may create spatial conflict with workspace of other designer in the same domain or with other domain

designers. This also incurs time wastage; if these spatial conflicts are detected and resolved through conventional methods of multi-domain design stakeholder reviews. Electro-Mechanical systems exhibit more examples of these types of conflict. Designers from different domains have spatial conflicts, who claim over same empty space in coupled design iterations at different timelines.

CAD tools only hold the geometry information of the product, while beyond geometry information is not captured in CAD models. Product design information like requirements, product states, and actors involved at different product states, functional model and behavior model, is not available in CAD model. Use of tools like System Modelling Language (SysML) by incorporation of system engineering domain, provides a way to capture and to represent product's non-geometry design knowledge.

In our earlier work for modelling Intended empty spaces for detecting multi-domain spatial conflict, a top down approach was discussed and implemented [1]. A bulk of empty space was detected inside CAD assembly model by Boolean operations and then it was decomposed into smaller empty space (ES) blocks. In subsequent work, the concept of modelling intended empty spaces by parametric primitive blocks was introduced [2]. Subsequent step is modelling inter-dependencies and building associativity among these empty spaces, product structure and other product knowledge in SysML. Further to this, graph based and matrix based approaches were adopted to model these interdependencies and relationships. Design Structure Matrix (DSM)/ Domain Mapping Matrix (DMM)/ Multi-Domain Matrix (MDM) were used to detect the spatial conflicts among multiple design domains in different product lifecycles states. Upon identifying these conflicts, knowledge based spatial suggestions can be made for efficient assembly and layout planning in product design. In this paper, focus is on to build an octree based voxel model for representation and computation purpose, which uses CAD model and ES Blocks with spatial conflicts.

2 Octree based Voxel model to represent CAD model

Voxel based method is an extension of the concept of pixels into 3D. Space is considered as a regular array of cells (usually cubes). Each cell is called voxel. Voxelization provides an alternative to geometric representation for volume modelling outside CAD. It provides a uniform, simple and robust description to synthetic and measured objects and gives the basis of volume graphics [3]. Voxelization of 3D model is done to represent the volumetric space of geometric objects using the primitives of array of voxels in the 3D discrete space. Voxel model approximates the shape of the model as closely as possible. The concept of voxelization was first introduced for volume graphics in 1993 [3]. Voxelization has application in different fields as volume modelling [4], virtual medicine [5], hepatic rendering [6], visualization of geometric model [7], CSG Modelling [8], Collision detection [9] [10] [11] and spatial analysis [12]. Voxel models are used for geospatial applications as computational methods for voxelating spatial datasets of 3D city models containing 3D surfaces, curve and point cloud data models [13]. OctoMap is also an Octree based Voxel model, used as 3D mapping framework for various robotic applications [14].

Goal of voxelization here is to devise a method to discretize the whole product assembly and empty spaces into smaller pieces and computationally model the empty spaces available free of spatial conflicts, to make valuable knowledge based spatial suggestions. Voxelization of 3D Solid models in CAD is one way to model volume and have empty space representation. Voxel information can be represented in form of 3D array data structure in cases where the CAD model space is small. Voxel enumeration at different voxel sizes is shown in Fig 1. A moderate 3-dimensional array 1000X1000X1000 will result in 1 billion voxels. Handling associated computations and rendering of such big number of voxel information is difficult task for any CAD software. Once the 3D model is voxelized, Boolean operations such as union and subtraction are computationally efficient.

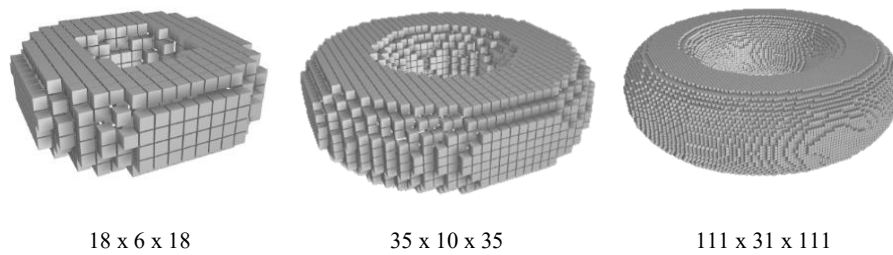


Fig 1. Voxel enumeration at different voxel sizes

Voxelization of 3D model needs efficient conversion of geometry in CAD to Voxels. Octree as data structure, enable efficient storage of 3D voxel data. Octree is a 3D generalization of a Quadtree (for 2D). Each node in an Octree has eight children as shown in Fig 2.

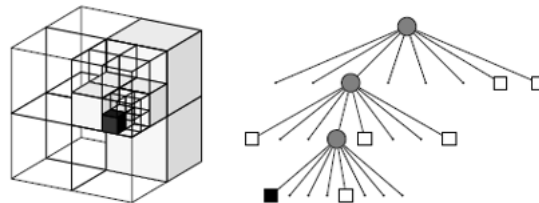


Fig 2. Octree containing empty and occupied nodes

Modelling of intended Empty space blocks as geometry items in CAD model has been carried out in continued work earlier. Further division of these blocks in voxels provide a way to efficiently represent and compute independent of CAD Software for meaningful space suggestions to users. Fig 3 shows a UML class diagram of octree based voxel model implemented. UML class diagram has been used for easy representation of the code structure. Any Octree object contains a root Octree node, which can be further divided into eight octree nodes. Node can contain an Octree leaf object, which are used as a condition to divide, the octree upto a user given voxel resolution. CAD model is ray intersected using a standard ray intersection method with CAD APIs.

This results into a point cloud of CAD geometry surfaces. Individual point is added as Octree Leaf to Octree node and based on the location of Octree leaf in root octree object, the octree nodes are further sub-divided.

Every octree node is having variables for Inner node, Empty Node and associated solid body in CAD model. Every Octree Node is having an object for octree bounds to define its boundaries modelled as class in Fig 3. It has been implemented using C# language and the code interacts with CAD model using CAD APIs. Octree Voxel model visualization is done using OpenGL libraries outside CAD environment.

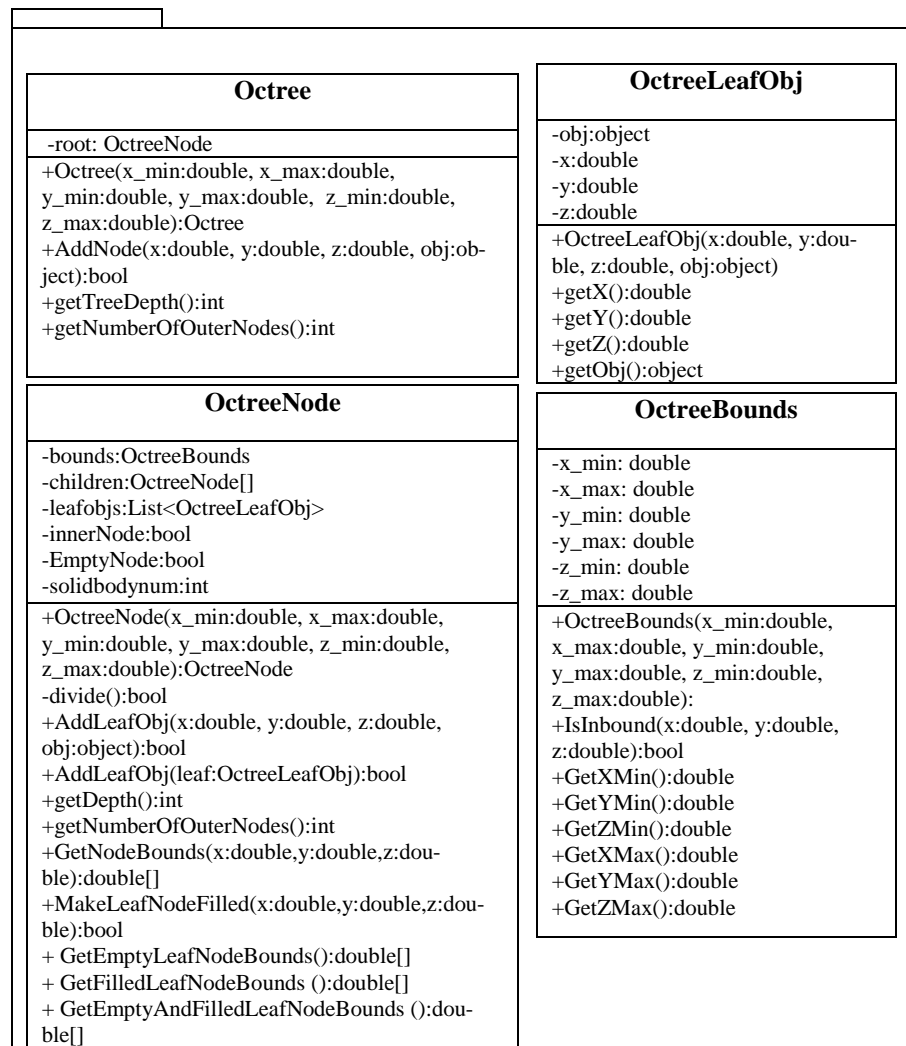


Fig 3. UML Class Diagram for Octree based voxel model representation of CAD model and Empty Spaces

Purpose of this representation and modeling, is to embed the knowledge available in System Modelling language (SysML) with the CAD model. SysML is the extension of

UML, which is developed to provide standard system modelling tools [15]. SysML supports capturing requirements of system and capturing structural & behavioral models. Block Definition Diagrams (BDD) and Internal Block Diagrams (IBD) are used to define classes and instances. Use of SysML tools, provides the way to capture, represent and associate the product's non-geometry information. This knowledge is stored in SysML compatible XML file for further processing along with the CAD model. Octree based voxel model is created on the fly instead of saving it in XML or other formats, whenever a representation is needed of CAD model with ES blocks associated with knowledge elements like Design requirements, Design domains and Product Lifecycle states. Octree based voxel models are compact and have an edge over array data structure for representation purpose, only when ratio of modeled space to total space, is up to 30 percent. Hence Knowledge available in the SysML, is used to choose only those Empty space blocks, which are connected to Design requirements under specific Product lifecycle state. In this particular use, where octree voxel model also needs to do computation for spatial suggestions, computational methods using Octree data structure, are preferred over arrays.

3 Case Study – CCTWT Slow Wave Structure (SWS) Design

Coupled Cavity Travelling wave tubes (CCTWT) are used to amplify RF signals in RADAR. Design of travelling wave tube involves multiple design domains like electromagnetics, thermal, structural, manufacturing and electrical. CCTWTs have five main sub-assemblies - Electron Gun, Slow Wave Structure (SWS), Input RF Window, Output RF window and Collector. Electron beam gets generated in Electron Gun, Which then travels through inner feature of Slow Wave Structure (SWS). RF signals enter SWS through Input RF Window and interact with Electron Beam, while travelling through SWS cavities.

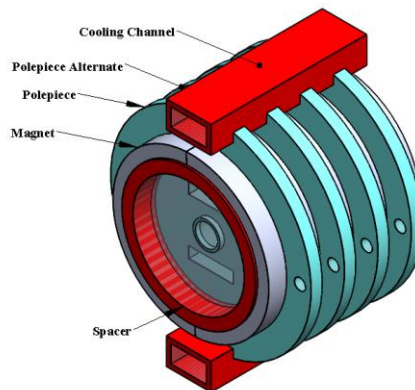


Fig. 4. Slow wave structure (SWS) solid model

SWS is an important sub-assembly of TWT and is a perfect example, where multiple design domain teams need collaboration to resolve spatial conflicts to have an efficient product design. SWS consists of alternate pattern of pole-pieces and spacers. Solid

model of typical SWS is show in Fig. 4. SWS CAD model has been represented in Octree Voxel model in Fig. 5. Complete process of getting point cloud of representing surfaces to full voxel model not differentiating filled and empty nodes to separate Empty space leaf nodes and SWS model filled leaf nodes have been shown in Fig. 5.

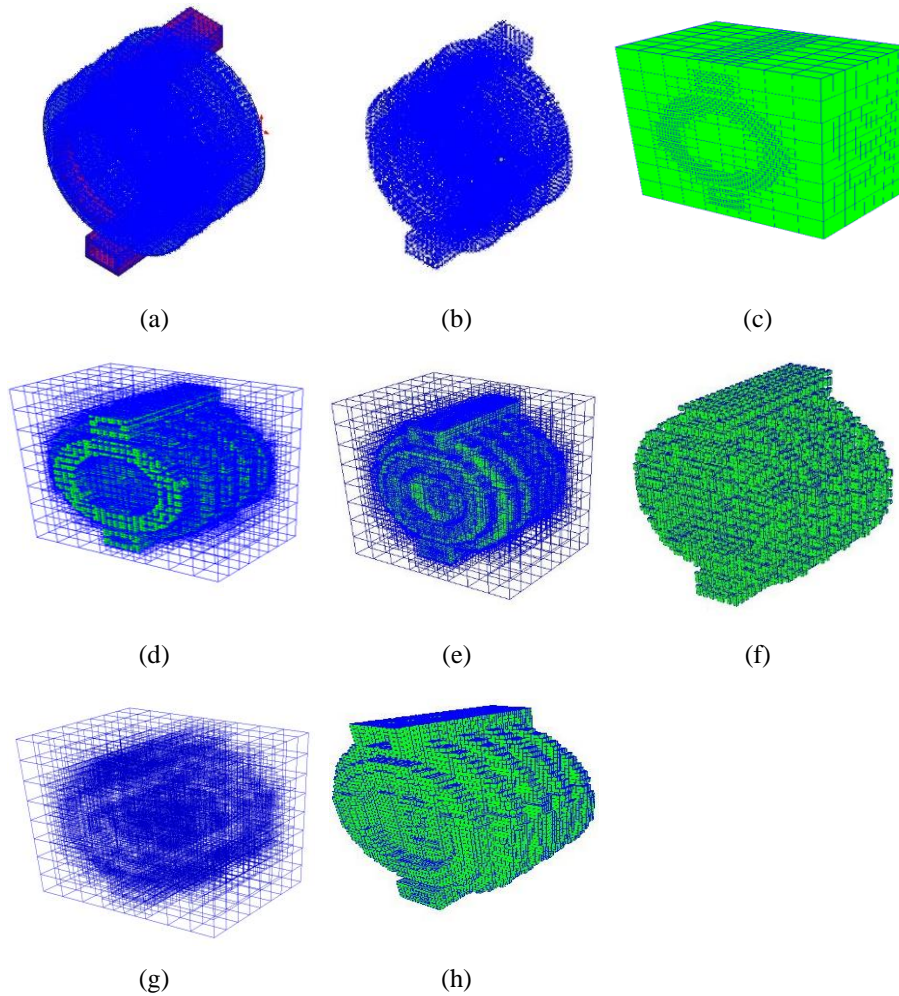


Fig. 5. Octree Based Voxel model generated using ray intersection on CAD Model. (a) Ray Intersecting CAD model from x, y and Z direction with equal resolution. (b) Point cloud generated by standard Ray intersection method with CAD APIs. (c) Voxel model showing complete SWS with its bounding box (CAD model with Empty Spaces). (d) Empty Spaces shown with wireframe and filled octree nodes with their faces modelled. (Empty nodes visible between filled SWS). (e) Filled Nodes between surface nodes to model it as a volume (f) Surface Octree node SWS Model. (g) Octree Voxel model of Empty space (h) Octree Voxel Model of only SWS Geometry

Design requirements for SWS with connected design domain and meant to be for particular product lifecycle states, are listed in table 1. Design mapping matrix (DMM) of ES blocks and requirements in different product lifecycle state is shown in Fig. 6. It

also shows the spatial conflicts in red color, depicting multiple usage of single ES Block in multiple design domains. This information is used to select the ES blocks shown in DMM rows, for creating the octree voxel model. Design requirements listed in table 1, are allocated to different ES blocks. SWS modelled with intended empty space blocks has been shown in Fig. 7. Empty spaces can be associated with the design requirement, which are satisfied by them.

Table 1. Design Requirements table

ID	Requirement	Design Domain	Product Lifecycle States
R6	Electron Beam Transmission	Electromagnetic	Testing, Operation
R7	RF Signal and Electron Beam Interaction	Electromagnetic	Testing, Operation
R8	Space for Magnet Placements in assembly	Electromagnetic	Assembly, Testing
R9	Cooling channel for thermal management	Thermal	All States
R10	Holes to align pole-pieces of SWS stack in Assembly and Brazing	Manufacturing	Manufacturing, Assembly
R11	Brazing of Cooling Channels	Manufacturing	Manufacturing, Assembly
R12	Magnetic Tuning for increasing electron beam transmission	Electromagnetic	Testing
R13	Coolant flow inside pole-piece cooling channels	Thermal	Testing, Operation
R14	Cooling of Pole-piece Cavities to avoid degassing	Thermal	Testing, Operation
R15	Magnetic field to compress electron beam	Electromagnetic	Testing, Operation
R16	Structure to sustain Ultra High Vacuum level inside TWT	Structural	All States
R17	Materials to sustain brazing temperatures	Structural	Assembly
R18	Materials to sustain temperatures under processing	Structural	Assembly
R19	RF Power Gain requirement	Electromagnetic	Testing, Operation
R20	RF Frequency Bandwidth requirement	Electromagnetic	Testing, Operation

3D solid model of SWS empty space blocks and Cut-sectional model views of intended empty spaces of SWS for different requirements, have been shown in Fig. 8(a) and (b). This method enables us to correctly differentiate and demarcate spaces inside SWS like for RF Signal and Electron beam. ES Blocks depicting multi-domain spatial conflicts in CAD model are shown in Fig. 8c. ES block list is computed from DMM shown in Fig. 6 for different product lifecycle states. This list in addition with Product structure, is the criteria, used to model octree Voxel model shown in Fig. 8(d).

Manufacturing		Assembly		Testing					Operation			
	R10		R8 R10		R6 R7 R8 R12 R14		R6 R7 R7 R14					
ESBlock5	0	ESBlock5	0 0	ESBlock5	0 1 0 0 0	ESBlock5	0 1 0					
ESBlock8	0	ESBlock8	0 0	ESBlock8	0 1 0 0 0	ESBlock8	0 1 0					
ESBlock9	0	ESBlock9	0 0	ESBlock9	0 0 0 0 0	ESBlock9	0 0 0					
ESBlock11	0	ESBlock11	0 0	ESBlock11	0 1 0 0 0	ESBlock11	0 1 0					
ESBlock14	0	ESBlock14	0 0	ESBlock14	0 1 0 0 0	ESBlock14	0 1 0					
ESBlock17	0	ESBlock17	0 0	ESBlock17	1 0 0 0 0	ESBlock17	1 0 0					
ESBlock18	0	ESBlock18	0 0	ESBlock18	0 1 0 0 0	ESBlock18	0 1 0					
ESBlock19	0	ESBlock19	0 0	ESBlock19	1 0 0 0 0	ESBlock19	1 0 0					
ESBlock20	0	ESBlock20	0 0	ESBlock20	1 1 0 0 1	ESBlock20	1 1 1					
ESBlock21	0	ESBlock21	0 0	ESBlock21	1 1 0 0 1	ESBlock21	1 1 1					
ESBlock22	0	ESBlock22	0 0	ESBlock22	0 1 0 0 0	ESBlock22	0 1 0					
ESBlock23	0	ESBlock23	0 0	ESBlock23	1 0 0 0 0	ESBlock23	1 0 0					
ESBlock24	0	ESBlock24	0 0	ESBlock24	1 0 0 0 0	ESBlock24	1 0 0					
ESBlock25	0	ESBlock25	0 0	ESBlock25	1 0 0 0 0	ESBlock25	1 0 0					
ESBlock26	0	ESBlock26	0 0	ESBlock26	0 1 0 0 0	ESBlock26	0 1 0					
ESBlock27	0	ESBlock27	0 0	ESBlock27	1 0 0 0 0	ESBlock27	1 0 0					
ESBlock28	0	ESBlock28	0 0	ESBlock28	1 1 0 0 1	ESBlock28	1 1 1					
ESBlock29	0	ESBlock29	0 0	ESBlock29	1 1 0 0 1	ESBlock29	1 1 1					
ESBlock30	0	ESBlock30	0 0	ESBlock30	1 0 0 0 0	ESBlock30	1 0 0					
ESBlock31	0	ESBlock31	0 0	ESBlock31	1 1 0 0 1	ESBlock31	1 1 1					
ESBlock32	0	ESBlock32	0 0	ESBlock32	1 0 0 0 0	ESBlock32	1 0 0					
ESBlock33	0	ESBlock33	0 0	ESBlock33	0 1 0 0 0	ESBlock33	0 1 0					
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ESBlock41	0	ESBlock41	1 0	ESBlock41	0 0 1 1 0	ESBlock41	0 0 0					
ESBlock42	0	ESBlock42	0 0	ESBlock42	0 0 0 1 0	ESBlock42	0 0 0					
ESBlock43	0	ESBlock43	0 0	ESBlock43	0 0 0 1 0	ESBlock43	0 0 0					
ESBlock44	0	ESBlock44	0 0	ESBlock44	0 0 0 1 0	ESBlock44	0 0 0					
ESBlock45	0	ESBlock45	0 0	ESBlock45	0 0 0 1 0	ESBlock45	0 0 0					
ESBlock46	1	ESBlock46	0 1	ESBlock46	0 0 0 0 0	ESBlock46	0 0 0					
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ESBlock52	1	ESBlock52	0 1	ESBlock52	0 0 0 0 0	ESBlock52	0 0 0					
ESBlock53	1	ESBlock53	0 1	ESBlock53	0 0 0 0 0	ESBlock53	0 0 0					
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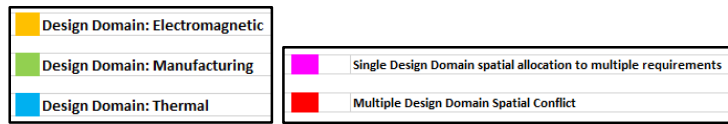


Fig. 6. DMM of ES Blocks and Design Requirements for Slow Wave structure for each product lifecycle state showing multiple usage of single ES Block within Single domain and in multiple design domains depicting spatial conflict

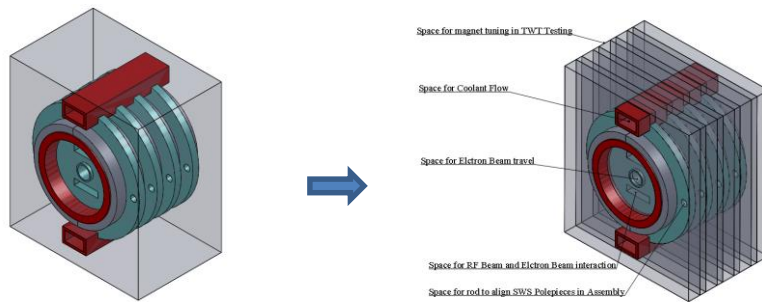


Fig. 7. SWS Empty spaces decomposed into intended empty space blocks showing allocated design requirements

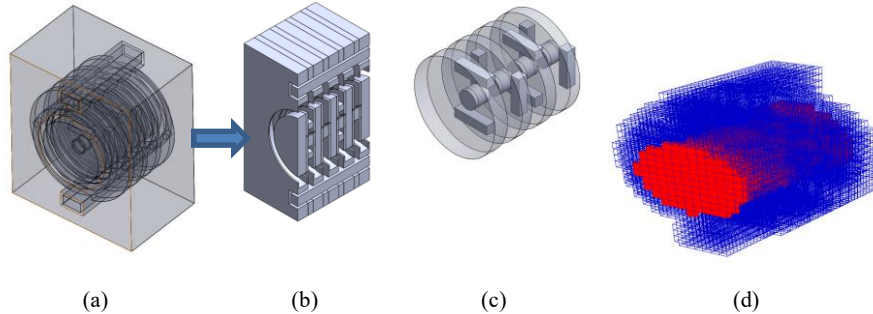


Fig. 8. a) 3D solid model of SWS Empty spaces b) Cut sectional model of SWS's bulk empty space decomposed into intended Empty space blocks to be allocated to different design requirements. c) ES Blocks depicting multi-domain spatial conflicts in CAD model d) Multi-domain spatial conflict representation using Octree Voxel model by rendering conflicting ES blocks in red with wireframe SWS octree voxel model outside CAD environment using OpenGL.

4 Conclusion

This paper discusses use of octree based voxel model for representation of spatial conflicts across multiple design domains. A framework has been developed to create octree based voxel model linked with intended empty spaces in product, which are associated with design requirements, product lifecycle states and are also connected with functional design domains. Knowledge in SysML, which was elaborated in our earlier work [1], is used to select criteria for building octree voxel model. A case study of Coupled cavity travelling wave tube (CCTWT) Slow Wave structure (SWS) design has been taken to showcase the code capabilities and showcase Octree voxels outside CAD platform representing spatial conflicts detected by associativity modelling of Empty space blocks along with the Product knowledge in SysML model.

In future work, it has been planned to use Octree based voxel model for knowledge based spatial suggestions in CAD for assembly and layout planning. Spatial conflict across multiple design domains are detected using different approaches in our earlier work. Subsequently in this paper, an octree based voxel model was generated using CAD model and selected Empty space blocks, which have requirements allocated from multiple design domains / states and showcases spatial conflicts. This gives us spaces available free from conflicts, which are planned to be used for locating new parts in assembly and carry out layout planning.

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