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Towards the separate compilation of Modelica: modularity and interfaces for the index reduction of incomplete DAE systems

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Abstract

A key feature of the Modelica language is its object-oriented nature: components are instances of classes and they can aggregate other components, so that extremely large models can be efficiently designed as “trees of components”. However, the structural analysis of Modelica models, a necessary step for generating simulation code, often relies on the flattening of this hierarchical structure, which undermines the scalability of the language and results in widely-used Modelica tools not being able to compile and simulate such large models.

In this paper, we propose a novel method for the modular structural analysis of Modelica models. An adaptation of Pryce’s Sigma-method for non-square DAE systems, along with a carefully crafted notion of component interface, make it possible to fully exploit the object tree structure of a model. The structural analysis of a component class can be performed once and for all, only requiring the information provided by the interface of its child components. The resulting method alleviates the exponential computation costs that can be yielded by model flattening: hence, its scalability makes it ideally suited for the modeling and simulation of large cyber-physical systems.

Keywords: DAE, Modelica, object-oriented modeling, index reduction, structural analysis, linear programming, interface theory, difference bound matrices

1 Introduction

System modeling tools are key to the engineering of safe and efficient Cyber-Physical Systems (CPS). Although ODE-based languages and tools, such as Simulink (MathWorks, Inc. 1994–2023), are widely used in industry, there are two main reasons why DAE-based modeling is best suited to the modeling of such systems: it enables a modeling based on first principles of the physics; it is physics-agnostic, and consequently accommodates arbitrary combinations of physics (mechanics, electrokinetics, hydraulics, thermodynamics, chemical reactions, etc.).

The pioneering work by Hilding Elmqvist (Elmqvist 1978) led to the emergence of the Modellica community in the 1990s, and the DAE-based modeling language of the same name (Modelica Association 2023) has become a de facto standard, with its object-oriented nature enabling a component-based modeling style. Its combined use with the port-Hamiltonian paradigm (Rashad et al. 2020) results in a methodology that is instrumental to the scalable modeling of large systems, additionally ensuring that the model architecture preserves the system architecture, in stark contrast to ODE-based modeling (Benveniste, Caillaud, Elmqvist, et al. 2019; Benveniste, Caillaud, and Malandain 2022).

Consequently, DAE-based modeling requires that Modelica tools properly scale up to very large models. However, although Modelica enables the modeling of extremely large systems, its implementations (Dassault Systèmes 2002–2023; Fritzson et al. 2020) are often not capable of compiling and simulating such large models. Scaling has been and still is a subject for sustained effort by the Modelica community (Casella and Guironnet 2021), and although HPC issues belong to the landscape (Braun, Casella, and Bachmann 2017), a more specific issue is of uttermost importance for the Modelica language.

In the first steps of the compilation of a Modelica model, its hierarchical structure is flattened, thanks to a recursive syntactic inlining of the objects composing it.¹ The result of this flattening process is an unstructured DAE that can be exponentially larger than the source model. The structural analyses that are required for the generation of simulation code (namely, the index reduction of the DAE system, followed by a block-triangular form transformation of the reduced-index system) are then performed on this monolithic DAE model. As the compilation process does not fully take advantage of the hierarchical nature of the models it has to handle, the modeling capabilities offered by the Modelica language are undermined by performance issues on the structural analysis itself (Höger 2015; Höger 2019). Additionally, model flattening poses a challenge when attempting to extend DAE-based modeling to higher-order modeling or dynamically changing systems (Broman and Fritzson 2008; Broman 2010; Broman 2021).

In this paper, a new modular structural analysis algorithm is proposed that takes full advantage of the object tree structure of a DAE model. The bedrock of this method is a novel concept of structural analysis-aware interface for components. The essence of a component interface is to capture the necessary information about a Modelica class that needs to be exposed, in order to perform the structural

¹See (Modelica Association 2023), Section 5.6 for a complete definition of the flattening process.
analysis of a component comprizing instances of the former class, while hiding away useless information regarding the equations and all protected features it may contain.

In order to compute a component interface, one has to be able to perform the structural analysis of the possibly non-square DAE system that this component encapsulates, and to use the interfaces of the components it aggregates in this analysis. We base our algorithm on Pryce’s Σ-method for index reduction (Pryce 2001), which essentially consists in the successive solving of two dual linear integer programs. The striking difference with Pryce’s algorithm is that these problems are solved by parts, in a scalable manner.

Putting all of this together, it is then possible to perform a modular structural analysis, in which structural analysis is performed at the class level, and the results can then be instantiated for each component of the system model, knowing its context. Hence, structural information at the system level is derived from composing the result of component-level analysis. Modular structural analysis yields huge gains in terms of memory usage and computational costs, as the analysis of a single large-scale DAE is replaced with that of multiple smaller subsystems. Moreover, the analysis is performed at the class level, meaning that a single structural analysis is needed for all system components that are instances of the same class.

To the best of our knowledge, only (Höger 2015) addresses the specific issue of performing the structural analysis of a hierarchical model; Section 2.1 of this paper shows the approach proposed by Höger and explains why we regard it as an efficient but still partial solution. On the related subject of sorting equations, attention is paid in (Zimmermann, Fernández, and Kofman 2019) to methods that avoid unrolling loops and expanding arrays when sorting equations, an issue targeting the same overall objective as both (Höger 2015; Höger 2019) and the present work.

Section 2 introduces two simple examples of DAE systems, to be used for illustrative purposes, and explains the modular approach from (Höger 2015) on one of them. In Section 3, we provide background information on structural analysis, which includes the Σ-method used to perform index reduction.

In Section 4, we introduce Σ-systems as an abstraction of DAE systems suitable for structural analysis. Σ-systems include the linear programming problems associated with the Σ-method and the structural description of the BTF. They also provide a notion of composition that abstracts the composition of DAE systems. This formalization leads us to the main contribution of this paper, presented in Section 5, where we propose the notion of Σ-interface.

Finally, Section 6 introduces our prototype implementation of the resulting modular method, then presents numerical applications to both examples presented in Section 2.

2 Examples

In this section, we develop two illustrative examples: a slightly modified version of the chained circuit proposed by C. Höger in (Höger 2015), and a homemade chained mass-and-spring system. Both will be used to illustrate how our approach changes and improves the structural analysis process of models where several instances of a same class are connected, while also highlighting different features of our algorithm.

2.1 A chained circuit

2.1.1 The circuit model

This example is a minor modification (with a second inductance added) of the one developed in (Höger 2015). The following text is borrowed verbatim from this reference:

The Modelica representation of the circuit [...] consists of a name (Circuit), declarations of parameters (n), unknowns (u and i) and sub-components (e). The physical behavior is defined directly by multiple equations, including the description of sub-circuit interconnection. Without knowledge about the internal structure of SubCircuit it is possible to use it inside the larger circuit, as long as it provides the corresponding interface (i.e. variables u and i). This composition of models is an easy and safe way to create more complex models out of simpler building blocks and is the very foundation of object-oriented modeling.

The Circuit and SubCircuit Modelica codes and schematics are given in Fig. 1 and 2 respectively.

2.1.2 The approach by C. Höger

While the flattening of the Circuit model is linear with n, the cost of the structural analysis of the resulting model is super-linear, thus preventing the classical approach from properly scaling up. In (Höger 2015), important contributions are proposed to cope with this problem. As far as we know, this paper is the first one pointing this issue very clearly. A faster method is proposed to perform the structural analysis of a hierarchical model, involving scoping and hiding.

This method is based on Pryce’s Σ-method (Pryce 2001), which is explained in Section 3. The Σ-method involves solving a pair of dual Linear Programming problems (primal and dual), using a specific iterative algorithm for the dual. In Theorem 1 of (Höger 2015), the author provides a complexity argument to support the claim that the dual problem is not a bottleneck on its own. Hence, the author focuses on the primal problem, for which a decomposition method is proposed.

2 Our own numerical experiments confirm this observation.
model Circuit
    parameter Integer n = 10;
    SubCircuit[n] c;
    Real u,i;
    equation
        c[1].i = i;
        u = sin(time);
    for j in 2:n loop
        c[j].i = c[j-1].i;
    end for;
    sum(c.u) = u;
end Circuit;

class SubCircuit
    parameter Real R1, R2, L, C;
    Real i, u;
protected
    Real u1,i1, u2,i2, uC,iC, uL1,iL1, uL2,iL2;
    equation
        iC=C*der(uC);
        uL1=L1*der(iL1);
        uL2=L2*der(iL2);
        u1 = R1 * i1;
        u2 = R2 * i2;
        u2 = uL1;
        uC=u1+u2;
        i1=i2+iL1;
        u=u1+uL1+uL2;
        i=i1+iC;
        i=iL2;
end SubCircuit;

Figure 1. Modelica code and electrical schematics of the chained circuit.

Figure 2. Modelica code of the SubCircuit class, and schematics of the corresponding electrical circuit.

2.2 A recursive mass-spring-damper system

Figure 3 shows the model, consisting of a chain of mass-spring-damper elements, defined with the 1D translational components of the Modelica Standard Library (MSL). Although recursive classes are not allowed in Modelica, we use a recursive definition of a chain of elements. This gives a binary-tree structure to the model, that is best suited for the modular structural analysis method presented in the sequel of the paper. Our prototype implementation of the method supports recursive classes, with conditional statements evaluated statically, at compile time.

2.3 Our contribution for these examples

Despite its important contribution, we believe that (Höger 2015) does not provide the ultimate answer. While a hierarchical algorithm for solving the primal problem is a great contribution in terms of computational costs, it still does not fully take advantage of object-oriented modeling. We would like instead to advance towards the separate compilation of components and systems, which consists in:

1. Proposing a notion of interface for model components, that is rich enough to subsequently perform system-level structural analysis; and
2. Proposing a modular structural analysis method, consisting of the needed algorithms performing hierarchi-
cal structural analysis based on interface information. In doing so, both primal and dual problems, as well as the construction of a Block-Triangular Form (BTF) for the Jacobian, need to be addressed—this is also in contrast to the approach of (Höger 2015), where only the primal problem is considered.

To simplify our presentation, we skip the discussion of the BTF; it will be developed in an extended version of this paper, that is currently in the works.

3 The $\Sigma$-method for DAE

The index reduction method proposed by J. Pryce (Pryce 2001), called the $\Sigma$-method, is an interesting alternative to the classical method originally proposed by C. Pantelides (Pantelides 1988). Its elegant and compact formulation as a pair of dual Linear Programming problems (LP) makes it particularly valuable for an extension to DAE components and architectures. The $\Sigma$-method can be summarized as follows:

The $\Sigma$-matrix

For $S = (F,X)$ a square DAE system involving equations $f=0$, where $f \in F$, and variables $x \in X$ and their derivatives, form the $\Sigma$-matrix $\Sigma = (\sigma_{f,x})_{(f,x) \in F \times X}$ of the DAE system, where $\sigma_{f,x}$ is the highest differentiation order of variable $x$ in $f$, or $-\infty$ if $x$ does not appear in $f$. 
**Primal problem**

The primal LP encodes the search for a maximum weight transverse of $\Sigma$, that will be described as $(f, x_f) \in F$ or, equivalently, $(f, x) \in X$ in what follows. The existence of a solution to the primal LP is a success check for the $\Sigma$-method.

**Dual problem**

The variables of the associated dual LP are variable offsets $(d_x)_{x \in X}$ and equation offsets $(c_f)_{f \in F}$, and we search for the minimal non-negative solution to this LP. (Pryce 2001) proves the uniqueness of this solution and proposes a relaxation method for finding it, given a solution to the primal problem.

The dual problem can be rewritten as the following constraint system, involving only the variable offsets $(d_x)_{x \in X}$:

$$
\begin{align*}
\forall x & \in X: \\
&(d_x \geq \sigma_{f,s} \\
\forall (f,x) & \in E: \\
&(d_x - d_{xf}) \geq \sigma_{f,s} - \sigma_{f,sf})
\end{align*}
$$

and the equation offsets are then given by

$$c_f = d_{xf} - \sigma_{f,sf} .$$

It is proved in (Pryce 2001) that the set of solutions of (1) does not depend on the particular choice of a solution to the primal problem.

**Use of the offsets**

Equation offset $c_f$ indicates how many times equation $f=0$ needs to be differentiated to get the index-reduced system, whereas $d_i$ indicates the maximum differentiation order of $x$ in this index-reduced system. In addition, the solution of the primal problem is useful for computing a BTF for the Jacobian of this system.

C. Höger (Höger 2015) explains that the primal problem is the main bottleneck, hence it focuses on solving it efficiently, by decomposing it into smaller subproblems. In contrast, we extend and adapt the $\Sigma$-method for *open DAE systems*, which are DAE systems that can possess more variables than equations, and that can be composed with other open DAE systems by unifying their common variables.

### 4 Structural analysis of open DAE

The set of variables of an open DAE system can be decomposed as

$$X = X^s \cup X^l$$

where $X^s$ and $X^l$ are its sets of *shared* and *local* variables, respectively. For example, the `SubCircuit` of Fig. 2 possesses 2 shared variables and 10 local variables (declared as `protected` in Modelica).

#### 4.1 Selectors

Since `SubCircuit` possesses 11 equations, we can regard it as a square system by assuming that one among the shared variables $i, u$ is dependent and the other one is free (determined by the yet unspecified environment of this circuit):

$$X^s = X^{free} \cup Y$$

where $Y \subseteq X^s$ is a subset of the shared variables. The set of dependent variables of the system is then $X^{dep} = Y \cup X^l$.

If we compose open DAE system $S$ with an environment $S'$ (another open DAE system), then the two selectors $Y$...
and $Y'$ for $S$ and $S'$ must satisfy the condition
\[ Y \cap Y' = \emptyset, \tag{3} \]
expressing that $S'$ cannot claim determining variable $x$ if the latter is already claimed by $S$, i.e., belongs to $Y$. We say that selectors $Y$ and $Y'$ are compatible if (3) holds.

For SubCircuit, two possible choices for selectors are
\[ Y = \{i\} \quad \text{or} \quad Y = \{u\}. \tag{4} \]

\section{Matching selectors in compositions}

If $\{i\}$ is selected, then $u$ is free, i.e., it must be determined by the (yet unspecified) environment. Thus, we expect this environment to allow for a selector containing $u$ but not $i$. This means that information (4) has to be exposed by $S$ as part of its interface for structural analysis.

Then, by exposing (4), $S$ sets structural constraints on the interface of any environment for it. We will show that information (4) is sufficient for characterizing the environments that are compatible with $S$.

This information, however, is not sufficient to perform the structural analysis in a modular way. In the sequel, we will identify the information that is missing for this purpose.

\section{Interfaces for the modular structural analysis of DAE systems}

This section introduces the main contribution of this paper, which is the notion of component interface needed for the structural analysis of DAE systems. This notion is called $\Sigma$-interface as a reference to the $\Sigma$-method itself. Possible ways to perform the modular structural analysis of the Circuit example are also shown, as a way to illustrate the benefits of the modular approach.

\subsection{The $\Sigma$-method for open DAE systems}

We first show the result of the $\Sigma$-method, applied to the open DAE system SubCircuit (see Fig. 2), for the two possible choices of selectors given by (4).

\subsection{Primal problem}

The solutions of the primal problem for both selectors are given in Fig. 4. For each case, the free variable for the considered selector is written in blue. The chosen maximum weight transverse is indicated by highlighting in red the dependent variable associated to each equation, and we give in the third column the differentiation order of this variable in this equation. Two important observations will guide us in defining the primal $\Sigma$-interface:

1. The choice of the transverse, which fixes the assignment of variables to equations, does not depend on the (yet unspecified) environment of SubCircuit;

2. The total weight of the maximal transverse of SubCircuit, for a given selector, is added to the total weight of a maximal transverse of the environment (for a compatible selector), yielding the total weight of the overall transverse (that covers both the component and its environment).

\subsection{Dual problem}

The dual problem is the constraint system (1), whose dependent variables are the offsets $(d_i)_x$. For $S = \text{def SubCircuit}$, both selectors have to be considered:

- $S$ has selector $\{i\}$. A maximal weight transverse is shown in red on Fig. 4-left. With this transverse, the dual problem (1) is shown in Fig. 5-left.

- $S$ has selector $\{u\}$. A maximal weight transverse is shown in red on Fig. 4-right. With this transverse, the dual problem (1) is shown in Fig. 5-right.

Note that, while edges $(f,x)$ are local (since all equations are local), variables can be shared between components; hence, dual problems only interact via the offsets of their shared variables $i$ and $u$ for SubCircuit. This observation will guide us in defining the notion of dual $\Sigma$-interface.

\subsection{$\Sigma$-interfaces}

\subsubsection{Primal $\Sigma$-interfaces}

We say that a selector for a component is consistent if the primal problem has at least one solution for this selector.

Primal $\Sigma$-interface of a component Based on the discussion at the end of Section 5.1.1, it suffices to expose, for each consistent selector, the set of all pairs (selector, maximal transverse weight)
\[ (\text{selector }, \text{maximal transverse weight}) \tag{6} \]

at the interface of the considered component, for the primal problem. Thus, (6) defines the primal $\Sigma$-interface of an open DAE system. For the SubCircuit example, the primal $\Sigma$-interface is the set
\[ \left\{ \{Y = \{i\}, J_Y = \{i\} = 3\}, \{Y = \{u\}, J_Y = \{u\} = 2\} \right\} \tag{7} \]

Composing primal $\Sigma$-interfaces Let us consider two open DAE systems $S_1$ and $S_2$ whose composition $S = S_1 \cup S_2$ is another open DAE system. Then, solving the primal problem for $S$ for a given selector $Y$ is equivalent to:

1. Solving the primal problems for $S_i, i=1,2$, for each selector $Y_i$, thus producing optimal weights $J_{Y_i}^*$, then

2. Selecting an optimizing compatible pair $(Y_1, Y_2)$ of selectors, i.e., solve
\[ \max \{J_{Y_1}^* + J_{Y_2}^* \mid Y_1 \cap Y_2 = \emptyset \text{ and } Y_1 \cup Y_2 = Y\} \tag{8} \]
class SubCircuit
  protected
  parameter Real R1, R2, L, C;
  Real i, u;
  Real u1, i1, u2, i2, uC, iC, uL1, iL1, uL2, iL2;
  equation
    iC = C * der(uC); // 1
    uL1 = L1 * der(iL1); // 1
    uL2 = L2 * der(iL2); // 1
    u1 = R1 * i1; // 0
    u2 = R2 * i2; // 0
    uL2 = L2 * iL2; // 0
    i1 = i2 + iL1; // 0
    iL2 = i1 + iC; // 0
    u2 = uL1; // 0
    iC = C * iL2; // 0
  end SubCircuit;

class SubCircuit
  protected
  parameter Real R1, R2, L, C;
  Real i, u;
  Real u1, i1, u2, i2, uC, iC, uL1, iL1, uL2, iL2;
  equation
    iC = C * der(uC); // 1
    uL1 = L1 * der(iL1); // 1
    uL2 = L2 * der(iL2); // 1
    u1 = R1 * i1; // 0
    u2 = R2 * i2; // 0
    uL2 = L2 * iL2; // 0
    i1 = i2 + iL1; // 0
    iL2 = i1 + iC; // 0
    i = iL2; // 0
  end SubCircuit;

Figure 4. The primal problem for SubCircuit, seen as an open DAE system, for two possible choices for the selector. Left: selector \( Y = \{i\} \) yields a maximal transverse (in red) of weight 3; right: selector \( Y = \{u\} \) yields a maximal transverse (in red) of weight 2. In each case, the contribution of each equation to the weight of the transverse is provided on the right of the equation, and the free variable (to be determined by the environment) is highlighted in blue.

Figure 5. The dual problem when \( S \) has selector \( \{i\} \) (left) and \( \{u\} \) (right).

The primal \( \Sigma \)-interface of \( S \) is then obtained by collecting the pairs \( (Y, Y_i) \) for every consistent selector of \( S \). Given a consistent selector \( Y \) for \( S \), we denote by
\[
(\pi_1(Y), \pi_2(Y))
\]
an optimizing selector pair. Remark that this pair may not be unique. However, the end result of the structural analysis (the offsets \( d_i \) and \( c_j \)) does not depend upon the choice of \( (\pi_1(Y), \pi_2(Y)) \).

5.2.2 Dual \( \Sigma \)-interfaces

5.2.2.1 Dual \( \Sigma \)-interface of a component Based on the discussion at the end of Section 5.1.2, we can now define the dual \( \Sigma \)-interface as collecting,
\[
\text{for each consistent selector } Y, \text{ the projection } \mathcal{D}^Y \text{ of system (1) on the subset of offsets } (d_i)_{i \in \mathcal{Y}}. \quad (9)
\]

For the SubCircuit example, the dual \( \Sigma \)-interface is the following set (collecting two elements):
\[
\begin{cases}
  (Y = \{i\}, \mathcal{D}^Y = \{i\}) : \{ d_i, d_a \geq 0 \} & \{ d_i \leq d_a+1 \} \\
  (Y = \{u\}, \mathcal{D}^Y = \{u\}) : \{ d_i, d_a \geq 0 \} & \{ d_a \leq d_i+1 \}
\end{cases}
\]
\quad (10)

In (10), the two constraint systems are obtained by projecting, over the offsets \( d_i \) and \( d_a \), the constraint systems given in Fig. 5.

5.3 Using \( \Sigma \)-interfaces in DAE systems

In this section, we illustrate the use of \( \Sigma \)-interfaces for the modular structural analysis of DAE systems. To each open DAE system, we associate its \( \Sigma \)-interface, by fusing the primal and dual \( \Sigma \)-interfaces defined above: it is a set of triples whose elements are a consistent selector, the weight of a solution of the corresponding primal problem, and the projection of the dual problem on the offsets of the shared variables. We shall then use (8) and (11) to perform the structural analysis of a DAE system in a modular way.
In what follows, successive instances of SubCircuit are chained, as shown in Fig. 1. The structural analysis will be performed by induction on the length \( n \) of the chain.

Call \( S_n \) the chain of length \( n \); in particular, \( S_1 = \text{SubCircuit} \), denoted by \( SC \) in what follows. Call \( i. u_n \) the shared variables of \( S_n \). Seen as a chain, \( S_n \) is obtained by composing \( S_{n-1} \) with \( SC \) and adding a Kirchhoff equation for voltages. We regard this last equation as a component with no local variables, denoted by \( Y \) below.

Note that we also have to rename \( u \) as \( v \) in \( SC \) in order to avoid name clashes, which we write \([u/v]\), and that variable hiding has to be used on the result of the composition, as variables \( u_{n-1} \) and \( v \) have to be made local in \( S_n \).

As a result, for all \( n \geq 2 \), \( S_n \) is defined as the following composition:

\[
S_n = \text{hide } u_{n-1}, v \text{ in } SC[u/v] \quad (13)
\]

For every \( n \), \( S_n \) has exactly one more variable than it has equations. Thus, there are two possible selectors for \( S_n \): \( Y_{S_n} = \{i\} \) and \( Y_{S_n} = \{u_n\} \).

**Case** \( Y_{S_n} = \{u_n\} \). Then, \( i \) is free and there is only one triple of compatible selectors in the composition occurring in the right-hand side of (13):

\[
(Y_{S_{n-1}} = \{u_{n-1}\}, Y_{SC} = \{v\}, Y_{eq} = \{u_n\}) .
\]

An immediate induction argument shows that the optimal weight for \( S_n \) is \( 2(n-1) + 0 + 2 = 2n \).

We prove by induction that the dual \( \Sigma \)-interface is

\[
\begin{cases}
  d_1, d_{u_n} \geq 0 \\
  d_{u_{n-1}} \leq d_1 + 1
\end{cases}
\]

for every \( n \). This holds for \( n = 1 \), as (14) then yields the dual \( \Sigma \)-interface of class SubCircuit for selector \( \{u\} \) (see (10)). Assuming that (14) holds for \( n-1 \), the dual \( \Sigma \)-interfaces compose as follows:

\[
\text{hide } u_{n-1}, v \text{ in } S_{n-1}, SC \quad (14)
\]

which yields (14) when projected on \( u_n, i \).

Consequently, the variable offsets of a SubCircuit in a Circuit are independent of the number of chained instances, and the equation offset of an equation in the \( k \)-th component of the chain is equal to that of the same equation in the original class.

**Case** \( Y_{S_n} = \{i\} \). Then, \( u_n \) is free and there are two triples of compatible selectors:

\[
\begin{align*}
(Y_{S_{n-1}} = \{u_{n-1}\}, Y_{SC} = \{i\}, Y_{eq} = \{v\}) & \quad (15) \\
(Y_{S_{n-1}} = \{i\}, Y_{SC} = \{v\}, Y_{eq} = \{u_{n-1}\}) & \quad (16)
\end{align*}
\]

By adding the contributions of the components, in the order they are written above, in each case, we get:

- In case (15): \( 2(n-1) + 0 + 3 = 2n + 1 \)
- In case (16): \( 2(n-1) + 1 + 0 + 2 = 2n + 1 \)

Thus, each of the two triples (15) and (16) is optimizing, which brings an important question: Does the dual \( \Sigma \)-interface depend on the choice of a tuple of selectors?

If **triple (15) is used**: System (14) for \( n-1 \) provides us with the dual \( \Sigma \)-interface of \( S_{n-1} \) for selector \( u_{n-1} \). For \( S_n \), the dual \( \Sigma \)-interfaces then compose as follows:

\[
\left\{ \begin{array}{l}
  d_1, d_{u_{n-1}}, d_v, d_{u_n} \geq 0 \\
  d_{u_{n-1}} \leq d_1 + 1 \\
  d_v \leq d_{u_n} + 1 \\
  d_v \leq d_{u_{n-1}} \\
  d_{u_n} \leq d_v
\end{array} \right. 
\]

which yields

\[
\left\{ \begin{array}{l}
  d_1, d_{u_n} \geq 0 \\
  d_1 \leq d_{u_n} + 1
\end{array} \right. 
\]

If **triple (16) is used**: We will prove by induction that (17) still holds. Assuming that (17) holds for \( n-1 \), the dual \( \Sigma \)-interfaces compose as follows:

\[
\left\{ \begin{array}{l}
  d_1, d_{u_{n-1}}, d_v, d_{u_n} \geq 0 \\
  d_1 \leq d_{u_{n-1}} + 1 \\
  d_v \leq d_{u_n} + 1 \\
  d_{u_{n-1}} \leq d_v \\
  d_{u_n} \leq d_v
\end{array} \right. 
\]

which yields again (17).

Recall that solutions to the \( \Sigma \)-method’s dual problem are independent of the particular choice of a solution for the primal problem. This property generalizes to the composition of dual interfaces:

the dual \( \Sigma \)-interface of a composition does not depend on the choice of a particular optimizing (18) tuple.

**5.3.2 The Circuit, seen as a tree**

Instead of a chain, the architecture of the Circuit can be regarded as a binary tree:

\[
T_m \quad \text{def} \quad T_{m-1} \quad \text{if } m > 1 \quad (19)
\]

where:
We reuse the $\Sigma$-interface of class SubCircuit, the optimal weights $2\ell_m$ and $2\ell_{m+1}$ for selectors $Y_{\Sigma_m}=$\{u$_m$\} and $Y_{\Sigma_n}=$\{\bar{i}\} respectively, and we focus on the dual interface. It is computed by the recursion shown in Fig. 6.

![Figure 6](image-url)

The dual $\Sigma$-interface of tree shaped architecture (19).

### 5.3.3 Discussion

It is worth comparing the chain-based and tree-based approaches above. At first glance, since recursion arguments were used in both cases, the two approaches may seem equivalent in terms of computational costs. However, this impression shall not last once we detail how implementations should proceed.

- For the chain architecture of Section 5.3.1, each induction step consists in the computation of the $\Sigma$-interface of prefix $S_k$ as a function of the $\Sigma$-interface of $S_{k-1}$, for $k$ increasing from 2 to $n$.

- For the tree-shaped architecture of Section 5.3.2, the induction step expresses the $\Sigma$-interface of the root of each subtree $T_k$ as a function of the $\Sigma$-interface of each subtree $T_{k-1}$. Remark that the $\Sigma$-interface of each subtree $T_k$ is only computed once, as all leaf components are instances of the same class, and all subtrees of height $k$ have, by construction, identical interfaces.

The number of steps is proportional to $n$ in the first case, and $m \sim \log n$ in the second case. Since the computational complexity of each induction step is roughly the same, treating the Circuit model as a tree-shaped architecture can dramatically improve performance.

### 6 Implementation and experimental results

#### 6.1 Implementation considerations

A significant difficulty in our modular structural analysis is the consideration of selector-dependent structural analyses. Both the primal and dual problems are functions of the selectors, which can be numerous for components with a large number of public variables. Although Modelica models are usually sparse, with components exposing only a few variables, a mere enumeration of selectors can result in an exponential growth of the handled data structures.

To deal with this issue, we advocate the approach proposed in (Benveniste, Caillaud, Malandain, and Thibault 2022; Caillaud, Malandain, and Thibault 2020) for multimode DAE systems. In these works, a dual representation is introduced, where equations are labeled with a predicate on mode variables, characterizing the modes under which they are active. Using this representation (instead of a direct one, that lists the active equations for each mode of the model) provides a compact representation of the structure of multimode systems. Reduced Ordered Binary Decision Diagrams, or ROBDD (Bryant 1986), provide not only an adapted data structure, but also efficient computation algorithms that can be used for performing the whole structural analysis in an “all-modes-at-once” fashion.

For modular structural analysis, a similar representation can be used, but with selectors instead of modes. The whole structural analysis chain can then be performed in an “all-selectors-at-once” fashion: (Benveniste, Caillaud, Malandain, and Thibault 2022) provides all building blocks for the computation of primal interfaces and their compositions, as presented in Section 5.2.1. The computation and composition of dual interfaces reduce to projecting parametrized constraint systems such as the one illustrated Fig. 5. Such systems are called Difference Bound Matrices (DBM) and come equipped with a rich calculus (Dill 1989; Miné 2001) with a polynomial computational complexity. Although there are excellent implementations of DBM, parametric DBM remain to be implemented, possibly using tuples of ROBDD for the representation of matrix elements.

#### 6.2 Benchmarks and measured performance

Experimental results have been obtained on the modular structural analysis method, using two benchmarks: the
In this paper, we present a modular index-reduction method for (possibly incomplete) DAE systems, based on John Pryce’s \( \Sigma \)-method (Pryce 2001) and extending the seminal work of Christoph Höger on scalable algorithms for the compilation of Modelica (Höger 2015; Höger 2019). Our method is built upon three key contributions: (i) a concept of selector that allows to characterize, from a structural analysis point of view, the possible effects of unknown environments on an incomplete DAE system; (ii) a concept of interface for the primal and dual problems of the \( \Sigma \)-method, that encapsulates the minimal information regarding a subsystem that needs to be exposed to its environment in order to perform the structural analysis; and (iii) interface composition and transformation operators that allow to compute the interface of a system from the interfaces of its parts.

Our modular structural analysis method is well-suited to the Modelica language, since the interface of a class can be computed inductively from the interfaces of the objects contained in the class. Modelica models are often sparse, meaning that each component shares only a few variables with its environment. This guarantees that the interface of a component remains small, independently of the number of components, variables and equations it may contain.

We believe that this concept of interface, and the modular structural analysis method it yields, pave the way towards a genuine separate compilation of Modelica, that can scale up to extremely large models.

The benchmarks performed with our prototype implementation demonstrate that extremely large models, organized in a component tree with sufficient regularity, can be analyzed in a few milliseconds.

Future work shall focus on a more robust implementation of the method, based on our IsamDAE multimode DAE structural analysis software (Benveniste, Caillaud, Malandain, and Thibault 2022; Caillaud, Malandain, and Thibault 2020). We plan to use a functional, BDD-based, representation of interfaces, in order to curb the combinatorics of selectors that is expected when computing interfaces of Modelica classes with a large number of public variables. This functional representation is also a promising approach to the extension of the notion of interface to multimode models.

The structural analysis of Modelica models comprising for loops could be done by (i) computing a tree decomposition of the component graph obtained from the evaluation of the loops, followed by (ii) the modular structural analysis of the resulting component tree. Benchmarking this approach on the scalable test suite (Casella and Guironnet 2021) would be of great interest.

Several features of the Modelica language, such as innerouter declarations, expandable connectors and overconstrained connections, may turn out to be difficult to deal with. In the Modelica Language Specification (Modelica Association 2023), the semantics of these features is expressed in terms of an elaboration phase, that is not modular. Devising a modular transformation of these features into the Modelica kernel language is a challenge that needs to be addressed before the modular structural analysis method can be applied to the full Modelica language.
There are also a few fundamental issues, related to the modular structural analysis, that ought to be investigated: (i) Is it possible to decide, by inductive reasoning, whether a parametric model is structurally nonsingular for every valuation of its parameters? (ii) Is it possible to perform the structural analysis of DAE systems that have a large treewidth, such as systems organized as a grid of dimension 2 or higher?

Answers to these questions are key to the design of scalable separate compilation methods for the Modelica language. One could envision the standardization of interfaces, possibly as an extension of the FMI standard for model exchange, so that precompiled Modelica libraries, equipped with their $\Sigma$-interfaces, could be built and reused.

References


