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Analytical and Experimental Evaluation of Wake-up Receivers based protocols

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Abstract—Achieving energy efficient wireless communication is the most pursued goal in Wireless Sensor Networks (WSNs), as energy consumption is typically a major barrier to long term applications. In recent years, ultra-low power Wake-up Receivers (WuRx) have emerged, enabling pure asynchronous wireless communication that eliminates energy waste due to idle listening. However, to achieve a significant increase of energy efficiency compared to traditional duty-cycling approaches, Medium Access Control (MAC) protocols exploiting WuRx must be carefully designed. Therefore, we propose an analytical framework to model MAC protocols, leveraging WuRx or not, which gives an important evaluation of power consumption, latency and reliability. This framework was used to both model a WuRx-based MAC protocol, and to model two other state-of-the art MAC protocols for WSNs not using WuRx. Experimental power consumption and latency measurements were conducted to validate the proposed framework and the MAC protocol leveraging WuRx. Analytical results show the convenience of using WuRx and quantify the benefits of this emerging technology. These results demonstrate that using WuRx achieves up to 135 times lower power consumption and up to 23 times lower latency compared to traditional approaches in typical low throughput WSNs applications.

I. INTRODUCTION

WSNs are characterized by limited resources such as memory, computation capabilities and especially available energy. Energy is typically the limiting factor of long-term applications, therefore maximizing the lifetime of WSNs is an aggressively pursued goal. Communication is usually one of the most energy consuming tasks, thus several techniques were proposed to increase the energy efficiency of communication [1]. The most popular one is duty-cycling, which consists of switching on and off the transceiver according to a predefined schedule. However, duty-cycling does not eliminate energy waste due to idle listening and incurs high latencies. Many approaches based on duty-cycling were proposed in literature and in commercial solutions [2], which typically face a hard trade-off, reducing either idle listening or latency.

Recent progress in microelectronics has made possible the design of Ultra-Low Power (ULP) WuRx which allow a continuous channel monitoring while consuming orders of magnitude less power than traditional transceivers [3]. These devices wake up the node microcontroller (MCU) or other sleeping subsystems using interrupts when a specific signal, called Wake-up Beacon (WuB), is detected. One of the main benefits of WuRx is to enable “pure” asynchronous communication that can significantly increase the energy efficiency of communications and reduce the latency [4]–[8]. Moreover, when WuRx provide computational capabilities [9], [10], they

are able to process the received data autonomously in an energy efficient manner. For instance, this feature makes possible for a WuB to wake up only a specific node by performing address matching on the WuRx, but it also allows the WuRx to take actions without waking-up the main MCU, e.g. activating a node sub-system or changing the sensor sampling rate, with a significant amount of energy saved.

To ensure ultra-low power consumption, state-of-the-art WuRx are usually characterized by their lower sensitivity and their lower bitrate compared with traditional WSN transceivers [3], [9], [10]. Due to this drawback, WuBs must be sent at a higher transmission power and a lower bitrate than the other frames received by the main radio to achieve the same transmission range, making the sending of WuBs energetically expensive. Therefore, MAC protocols leveraging WuRx must be carefully designed to really achieve more energy efficient communication than traditional approaches relying on duty-cycling. To identify the application scenarios for which WuRx are advantageous compared to traditional approaches and to quantify the benefits brought by WuRx, an analytical model is required.

While many dedicated MAC models were proposed for the standard MAC IEEE 802.15.4 (e.g. [11]) and IEEE 802.11 (e.g. [12]), or for specific applications such as cooperative retransmission [13], only a few generic frameworks were proposed to model MAC protocols. In [14], the authors analyzed the performance of a set of MAC protocols in the context of low datarate WSNs. If the proposed traffic and radio models are generic, the latency and energy models are specific to each MAC, making this approach hard to extend to new protocols. *Asudeh et al.* [15] proposed a selection framework to choose the appropriate protocol that satisfies the requirements for a given context defined by a set of input parameters. Three categories of protocols (preamble sampling, common active period and scheduled) are defined and it is assumed that the protocols in the same category have similar performance characteristics. However, the new paradigm brought by WuRx cannot fit in any of these categories. Focusing on WuRx, *Lont et al.* [16] compared analytically the energy consumption of a WuRx-based MAC to two other traditional approaches. The authors focus on energy consumption, while in this work latency and reliability are also considered. Moreover, a separate analytical model was used for each MAC scheme, while we proposed in this work a generic model. In [17], an analytical comparison between WuRx-based and preamble sampling schemes was proposed. The authors show that WuRx enable lower latency if the transmit power can be scaled without limitation and reduce the energy consumption in the context of low traffic. The

authors also suggested that the low sensitivity of WuRx can be compensated by the deployment of more nodes. However, the authors did not consider any real MAC approach, and the used analytical model was not designed to model specific MAC schemes. In this paper, we propose a framework combining both analytical results and experimental measurements to model accurately MAC protocols leveraging WuRx or using traditional duty-cycling approach. The proposed framework is based on Absorbing Markov Chains (AMCs) [18], and focuses on energy consumption, latency and reliability. Markov chains have already proved to be useful for modeling communications protocols, especially to study specific MAC protocols [19] and cross-layer designs [20]. Using experimental measurements, we have proven that the model provides accurate estimations in the context of low throughput applications, typical for WSNs [21].

To evaluate pure asynchronous WuRx-based approach, we used the transmitter initiated MAC protocol TI-WuR from [4], to which we added the Automatic Repeat reQuest (ARQ) error control scheme. Using the proposed analytical framework, we modeled the new version of TI-WuR. In addition, to evaluate both the proposed framework and the benefits of WuRx, two other state-of-the-art protocols have been modeled: (i) PW-MAC [22], which focuses on low energy consumption and is based on the duty-cycling approach; (ii) IEEE 802.15.4, the most commonly employed standardized protocol in WSNs, which specifies two different modes, one that requires a controller node that coordinates the other nodes using beacon frames, and one without such a controller (beaconless mode). In this work, the beaconless mode was chosen, which requires the nodes to be “always awake” and uses the Carrier Sense Multiple Access with Collision Avoidance (CSMA/CA) approach to communicate. This protocol provides a benchmark value for latency. The average power consumption, latency and the reliability of these protocols were compared, analytically and experimentally. The principal contributions of this work are:

- A novel framework for modeling MAC protocols in the context of low throughput WSN applications.
- An analytical comparison of three protocols that were modeled using this new framework.
- Experimental measurements realized to set the model parameters using realistic and accurate energy consumption and latencies values, and to validate the accuracy of the framework.
- Experimental comparisons of the energy consumption and latency of the WuRx in low datarate scenarios using the TI-WuR protocol were conducted. The results of these experimentations, combined with those of the analytical comparisons, are used to discuss and quantify the benefits enabled by WuRx.

II. GENERIC FRAMEWORK FOR MODELING MAC

The proposed analytical framework for modeling the energy consumption, the latency and the reliability of MAC protocols is introduced in this section. For a given protocol, two AMCs are defined, one to model a packet reception operation and the other to model a packet transmission operation. Three typologies of states are defined to build an AMC:

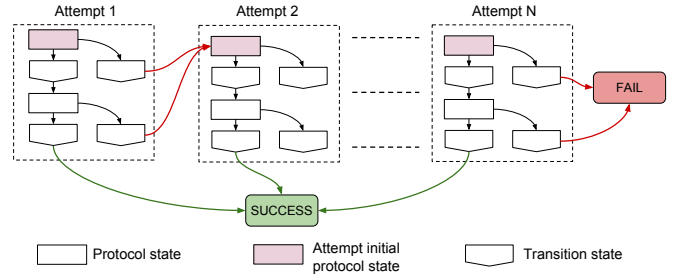


Fig. 1: Global model of an AMC modeling a packet transmission or reception with multiple attempts allowed.

protocol states, *transition states* and *final states*. Protocol states are defined by the protocol itself. To correctly evaluate the energy consumption and latency incurred by the protocol, transition states, which do not affect the behavior of the protocol, are needed to model the energy and latency cost of state transitions. Final states represent the possible outcomes of a packet transmission or reception: “Success” or “Fail”, similarly to [20]. The two final states are the absorbing states of the AMCs, *i.e.* the states which are impossible to leave, while all the other states are transient. Fig. 1 illustrates the global structure of an AMC modeling a packet transmission or reception when multiple attempts are allowed. Each attempt has an initial state, and the initial state of the first attempt is the initial state of the AMC. On Fig. 1, transition probabilities are not shown for clarity. If an attempt fails, then a new attempt is initiated if the failed attempt was not the last one. Otherwise, the operation is a failure. In the rest of this paper, mathematical objects (AMC, matrix, vector or scalar) associated to the packet transmission operation are denoted with a “t” subscript, while mathematical objects associated to the packet reception operation are denoted with a “r” subscript. When referring indifferently to both operations, the “b” subscript is used.

Let C_b be an AMC modeling a packet transmission or reception, and \mathbf{P}_b be its associated transition matrix defined by:

$$\mathbf{P}_b = \begin{matrix} & e_1 & \cdots & e_{M_b} & s & f \\ \begin{matrix} e_1 \\ \vdots \\ e_{M_b} \\ s \\ f \end{matrix} & \begin{pmatrix} p_{1,1} & \cdots & p_{1,M_b} & p_{1,s} & p_{1,f} \\ \vdots & \vdots & \vdots & \vdots & \vdots \\ p_{M_b,1} & \cdots & p_{M_b,M_b} & p_{M_b,s} & p_{M_b,f} \\ 0 & \cdots & 0 & 1 & 0 \\ 0 & \cdots & 0 & 0 & 1 \end{pmatrix} \end{matrix}, \quad (1)$$

where M_b is the number of transient states $e_i, i \in \{1, \dots, M_b\}$, s is the final “Success” state, f is the final “Fail” state, and $p_{i,j} \in [0, 1]$ is the transition probability from the state e_i to the state e_j . As s and f are absorbing states, $p_{s,s} = p_{f,f} = 1$. We denote $p_f \in [0, 1)$ the *frame failure probability*, *i.e.*, the probability that a frame transmission/reception fails. Various events can lead to a frame transmission/reception failure, such as collisions and interferences. In the proposed model, p_f is an input parameter, allowing the evaluation of MAC protocols under various channel conditions. Hence, when constructing an AMC modeling a packet transmission or reception operation, the states corresponding to a frame transmission or reception lead to a failure with a probability p_f , and to a success with a probability $1 - p_f$.

Applying classical AMC results, \mathbf{P}_b can be written without loss of generality as follows [18]:

$$\mathbf{P}_b = \begin{pmatrix} \mathbf{Q}_b & \mathbf{R}_b \\ \mathbf{0} & \mathbf{I}_2 \end{pmatrix}, \quad (2)$$

where \mathbf{Q}_b is a M_b -by- M_b matrix, \mathbf{R}_b is a M_b -by-2 matrix, \mathbf{I}_2 is the identity matrix of size 2 and $\mathbf{0}$ is the 2-by- M_b null matrix. The *fundamental matrix* of C_b is [18]:

$$\mathbf{N}_b = (\mathbf{I}_{M_b} - \mathbf{Q}_b)^{-1} = \sum_{k=0}^{\infty} \mathbf{Q}_b^k, \quad (3)$$

where $\mathbf{Q}_b^0 = \mathbf{I}_{M_b}$. The ij -entry of \mathbf{N}_b , denoted n_{ij} , is the expected number of times the chain was in the state e_j if it started in the state e_i before being absorbed. It is assumed without loss of generality that the initial state of C_b is e_{i_0} with $i_0 \in \{1, \dots, M_b\}$. Thus, only the i_0^{th} row of the \mathbf{N}_b matrix is considered, and the vector of size M_b corresponding to this row is denoted \mathbf{n}_b .

A. Probability of packet operation success

To evaluate the reliability of a protocol, the probability that a packet transmission or reception succeed, *i.e.* that C_b is absorbed by the state s , is considered. The *absorption probability matrix* denoted \mathbf{B}_b is a M_b -by-2 matrix in which the ij -entry denoted b_{ij} is the probability that the matrix will be absorbed by the j^{th} absorbing state if it starts in the i^{th} transient state. We have [18]:

$$\mathbf{B}_b = \mathbf{N}_b \mathbf{R}_b. \quad (4)$$

As the initial state is e_{i_0} , only the i_0^{th} row is considered and the vector of size 2 corresponding to this row is denoted \mathbf{b}_b . We denote $b_{b,s}$ the entry of this vector corresponding to the probability that the chain C_b is absorbed by the state s .

B. Energy cost of a packet transmission/reception operation

Let \mathbf{e}_b be the *energy costs vector*, *i.e.* the vector of size M_b in which the i^{th} entry is the energy cost incurred by the MAC protocol when traversing the state e_i . Hence, the average energy cost of a packet transmission or reception modeled by C_b is the scalar product of \mathbf{n}_b and \mathbf{e}_b :

$$\bar{e}_b = \mathbf{n}_b \cdot \mathbf{e}_b. \quad (5)$$

In (5), \mathbf{n}_b is related to the protocol functioning, while \mathbf{e}_b is related to application and hardware specifics, *i.e.* size of frames and transceiver power consumption.

C. Average power consumption

Knowing the expected costs of a packet transmission and a packet reception, respectively denoted \bar{e}_t and \bar{e}_r and computed using (5), the average power consumption incurred by communications can be computed as follows:

$$\bar{P}_C = \lambda_r \bar{e}_r + (\lambda_g + b_{r,s} \lambda_r) \bar{e}_t + P_o, \quad (6)$$

if it is assumed that a node forwards all the packets that are successfully received. In (6), λ_r and λ_g are respectively the average packet reception and local packet generation rate, $b_{r,s}$ is the probability that a packet reception succeed and is computed as described in Section II-A, and P_o is the power consumption overhead incurred by the MAC protocol and due to other activities than packet transmissions or receptions (e.g. use of WuRx, periodic transceiver switch-on...).

D. Latency of a packet transmission operation

We define the *latency costs vector* denoted \mathbf{l}_b as the vector of size M_b in which the i^{th} entry is the latency incurred by the MAC protocol when traversing the state e_i . However, the same reasoning as in (5) can not be applied for the latency, as the scalar product of \mathbf{n}_b and \mathbf{l}_b gives the average duration of a packet transmission or reception operation without regard to its outcome. However, when the latency is evaluated, we are interested in the packet transmission or reception duration when that succeeds. Therefore, the *conditional fundamental matrix* denoted $\mathbf{N}_{|a,b}$ is introduced as the matrix in which the ij -entry, denoted $n_{|a,ij}$, is the expected number of times the chain was in the state e_j if it started in the state e_i and knowing that the chain was absorbed by the state $a \in \{s, f\}$. Moreover, we have:

$$n_{|a,ij} = \frac{b_{ja}}{b_{ia}} n_{ij}. \quad (7)$$

Proof: Let $X_{ij}^{(m)}$ be the random variable that takes the value 1 if the chain is at the transient state e_j at the step m if it started at the transient state e_i , and 0 otherwise. Then:

$$\Pr(X_{ij}^{(m)} = 1) = q_{ij}^{(m)}, \quad (8)$$

where $q_{ij}^{(m)}$ is the ij -entry of the matrix \mathbf{Q}_b^m . Suppose A_i is the random variable corresponding to the state which will absorb the chain if it started at the transient state e_i . According to Bayes' theorem:

$$\begin{aligned} \Pr(X_{ij}^{(m)} = 1 | A_i = a) \\ = \frac{\Pr(A_i = a | X_{ij}^{(m)} = 1) \Pr(X_{ij}^{(m)} = 1)}{\Pr(A_i = a)}. \end{aligned} \quad (9)$$

Because of the Markov property:

$$\Pr(A_i = a | X_{ij}^{(m)} = 1) = \Pr(A_j = a), \quad (10)$$

hence, we have:

$$\begin{aligned} \Pr(X_{ij}^{(m)} = 1 | A_i = a) &= \frac{\Pr(A_j = a)}{\Pr(A_i = a)} \Pr(X_{ij}^{(m)} = 1) \\ &= \frac{b_{ja}}{b_{ia}} q_{ij}^{(m)}, \end{aligned} \quad (11)$$

using the definition of the absorption probabilities matrix. The expected number of times the chain was in the transient state e_j if it started in the transient state e_i and if it was absorbed by the state a after m steps is:

$$\begin{aligned} \mathbb{E}(X_{ij}^{(0)} + \dots + X_{ij}^{(m)} | A_i = a) \\ = \frac{b_{ja}}{b_{ia}} (q_{ij}^{(0)} + \dots + q_{ij}^{(m)}), \end{aligned} \quad (12)$$

because of the linearity of the expectancy. Hence, when m goes to infinity:

$$\begin{aligned} n_{|a,ij} &= \lim_{m \rightarrow +\infty} \mathbb{E}(X_{ij}^{(0)} + \dots + X_{ij}^{(m)} | A_i = a) \\ &= \lim_{m \rightarrow +\infty} \frac{b_{ja}}{b_{ia}} (q_{ij}^{(0)} + \dots + q_{ij}^{(m)}) = \frac{b_{ja}}{b_{ia}} n_{ij}. \end{aligned} \quad (13)$$

where the last equality is because of (3). \blacksquare

The conditional fundamental matrix $\mathbf{N}_{|s,t}$ is such that the ij -entry is the expected number of times the chain was in the state e_j if it started in the state e_i knowing that the packet transmission succeed. As the initial state is assumed to be e_{i_0} , the vector corresponding to the i_0^{th} row and denoted $\mathbf{n}_{|s,t}$ is considered. Hence, the expected latency incurred by the transmission of a packet is:

$$\bar{l}_t = \mathbf{n}_{|s,t} \cdot \mathbf{l}_t. \quad (14)$$

E. Average number of attempts

Let \mathbf{c} be the vector of size M_b in which the i^{th} entry takes the value 1 if e_i corresponds to the initial state of an attempt, 0 otherwise. Then, the expected number of attempts denoted a_b , without considering the outcome of the transmission or reception operation, is given by:

$$a_b = \mathbf{n}_b \cdot \mathbf{c}. \quad (15)$$

III. MODELING THREE MAC PROTOCOLS

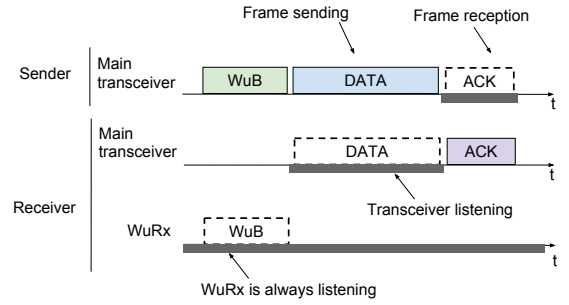
In this section, three MAC protocols are modeled using the framework previously introduced to compare their power consumption, latency and reliability. The three evaluated protocols are TI-WuR, a transmitter initiated MAC leveraging WuRx extended in this paper, PW-MAC, and IEEE 802.15.4 MAC beaconless mode, which relies on CSMA/CA. For each of these protocols, both the AMC describing a packet transmission and the AMC describing a packet reception were established, and the value of P_o was calculated. Although the same variable names are used for the three protocols (P_o, e_i, \dots) for clarity, they take different values for different protocols.

A. TI-WuR

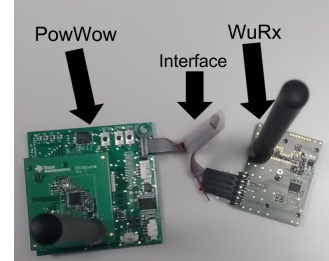
TI-WuR achieves a pure-asynchronous communication with WuRx. A packet transmission using the extended TI-WuR is shown by Fig. 2a. The sender first sends a WuB to the WuRx of the receiving node. When it receives the WuB, the receiving node listens to the channel using its main transceiver to receive the data frame. Afterward, the sender sends the data frame, and waits for an acknowledgment (ACK). Finally, the receiver sends the ACK when it has successfully received the data frame. Each frame sending or reception corresponds to a transient protocol state of the AMCs used to model packet transmission or reception.

In this work, the WuRx designed and developed in [9] is employed. This WuRx works in the 868MHz frequency bands, and receives data with On-Off Keying modulation (OOK) at a bitrate of 1 kbps. The sensitivity in these conditions was measured to be -55dBm , with a range around 25m using 3dBi gain antennas. The WuRx provides computational capabilities due to the ULP MCU, the PIC12LF1552 from Microchip, that it embeds. The power consumption of the whole WuRx was measured to be $1.83 \mu\text{W}$ in always-on listening mode and $284 \mu\text{W}$ when receiving and processing data with the ULP MCU active. The ULP MCU was programmed to partially incorporate TI-WuR, and in particular to perform address matching, allowing nodes to wake up only a specific node and not all their neighbors.

Due to the presence of the WuRx, the main transceiver is only used for sending and receiving data, and therefore



(a) Packet transmission using TI-WuR.



(b) A PowWow node equipped with a WuRx.

Fig. 2: Packet transmission using TI-WuR and platform setup.

the energy consumption it incurs is accounted by the two first terms of (6). However, P_o must account for the power consumption due to the always on WuRx, although it is very low. At each WuB reception, the WuRx ULP MCU wakes up to process the data embedded in the WuB. As this is part of the packet reception operation, it is accounted in the first term of (6). The expected number of attempts per packet reception a_r corresponds to the expected number of WuB received per packet reception when accounting retransmissions. Hence, we have:

$$P_o = \left(1 - a_r \lambda_r \frac{L_w}{R_w}\right) P_w^l, \quad (16)$$

where L_w is the size of a WuB, R_w is the bitrate at which the WuBs are transmitted and P_w^l is the power consumption of the WuRx when only the analog front-end is active for listening to the medium.

B. PW-MAC

PW-MAC is a receiver initiated protocol based on the traditional duty-cycling approach that focuses on low energy consumption for both the sender and the receiver [22]. Each node periodically sends a beacon to inform the neighbors that it is ready to receive, and then listen to the channel. If no data frame is detected after a short delay, the node goes back to the sleep mode. Each node “learns” the wake-up schedule of its forwarders, and if it has a packet to send to one of them, it wakes up just before the next scheduled wake-up of the forwarder to receive the periodic beacon that initiates the communication. This approach significantly reduces the idle listening on the transmitting node side, but not on the received node side as periodic beacon sending and channel listening are still required.

During a packet reception, the beacon transmission is considered as being part of the packet reception operation. The expected number of attempts per packet reception operation a_r corresponds to the expected number of beacons transmitted per packet reception operation when counting retransmissions. Hence, the power consumption overhead incurred by the periodic switch-on of the transceiver is:

$$P_o = \left(\frac{1}{T_{WI}} - a_r \lambda_r \right) e_w, \quad (17)$$

where e_w is the energy cost of a scheduled wake-up, and T_{WI} is the wake-up interval, *i.e.* the average duration between two scheduled wake-ups.

C. Unslotted CSMA/CA

Unslotted CSMA/CA, which is used by IEEE 802.15.4 MAC layer beaconless mode, requires the transceiver to be always listening to the channel. As the cost of packets transmissions and receptions are accounted by the two first terms of (6), the power overhead incurred by CSMA/CA is:

$$P_o = \left(1 - \lambda_r (\mathbf{n}_r \cdot \mathbf{l}_r) - (\lambda_g + b_{r,s} \lambda_r) (\mathbf{n}_t \cdot \mathbf{l}_t) \right) P_{rx}, \quad (18)$$

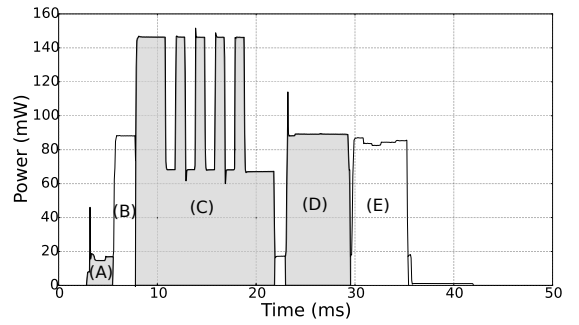
where P_{rx} is the power consumption of the transceiver when it is listening the channel. In (18), $(\mathbf{n}_r \cdot \mathbf{l}_r)$ and $(\mathbf{n}_t \cdot \mathbf{l}_t)$ correspond to the expected duration of a packet reception and packet transmission operation respectively, without considering operation outcomes.

IV. ANALYTICAL AND EXPERIMENTAL RESULTS

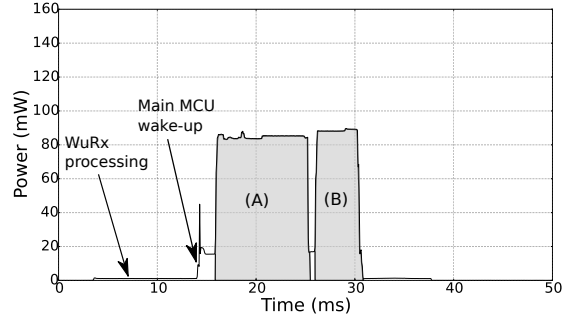
In this section, analytical results and experimental measurements are presented to compare the power consumption, reliability and latency of TI-WuR, PW-MAC and CSMA/CA. Microbenchmarks of these protocols were conducted to obtain accurate values of the energy consumption and latency incurred by each state of these protocols. These values were used to set the energy costs and latency costs vectors, as detailed in Section IV-A. Section IV-B exposes analytical results along with results of experimental measurements realized to evaluate the accuracy of the proposed models. All the experimentations were realized using the PowWow platform [23] and the WuRx from [9]. However, the WuRx were connected to the nodes only when evaluating TI-WuR, and the obtained setup is shown by Fig. 2b.

A. Microbenchmarks

The proposed analytical model requires the energy cost vectors \mathbf{e}_t and \mathbf{e}_r and the latency cost vectors \mathbf{l}_t and \mathbf{l}_r to be carefully set to provide realistic power consumption and latency values. Fig. 3 shows the traces obtained for the extended TI-WuR, which were acquired using a Keysight N6705B DC power analyzer. Fig. 3a exposes a packet transmission operation, which includes among others the sending of the WuB (C), the sending of the data frame (D) and the reception of the ACK (E). The sending of the WuB is preceded by a radio setup phase (B) and a MCU processing phase (A). Moreover, MCU processing stages are also present between frames receptions/transmissions. Fig. 3b shows in detail the different stages of a packet reception using TI-WuR. The processing of the WuB by the WuRx can be



(a) Sending a packet using TI-WuR.



(b) Receiving a packet using TI-WuR.

Fig. 3: Microbenchmarks of TI-WuR.

seen, as well as the data frame reception (A) and the ACK transmission (B). Moreover, similarly to what was observed for the packet transmission, phases of MCU processing are present just after the main MCU wake-up and between frames transmission/reception. These measurements were used to set the energy costs and latency costs vectors needed by the TI-WuR model. Similarly, microbenchmarks were conducted for PW-MAC and CSMA/CA, and the energy costs and latency costs vectors associated to these protocols were set using the same procedure.

Moreover, when transmitting a packet in PW-MAC, a node has to wait in average $\frac{T_{WI}}{2}$ s for the receiving node to wake up, and, if the attempt fails, T_{WI} s for the next attempt. In TI-WuR or CSMA/CA, the node can start the transmission immediately. However, if the attempt fails, a backoff is set before starting a new attempt. These latency costs were accounted in the \mathbf{l}_t and \mathbf{l}_r vectors, and therefore are considered by (14).

B. Results and Discussion

Analytical results are presented and discussed in this section, along with experimental measurements that were carried out to evaluate the accuracy of the proposed model. The average power consumption and the packet transmission latency of a testbed node that locally generates packets at a rate λ_g and receives packets at a rate λ_r were measured. The node forwards all the packets that it successfully receives. Measurements were realized for various values of λ_g , λ_r , the wake-up interval T_{WI} when PW-MAC was evaluated, and the WuB size L_w when TI-WuR was evaluated. The analytical and experimental results are presented by Fig. 4, on which the red markers represent the experimentally measured values. It can be observed that the

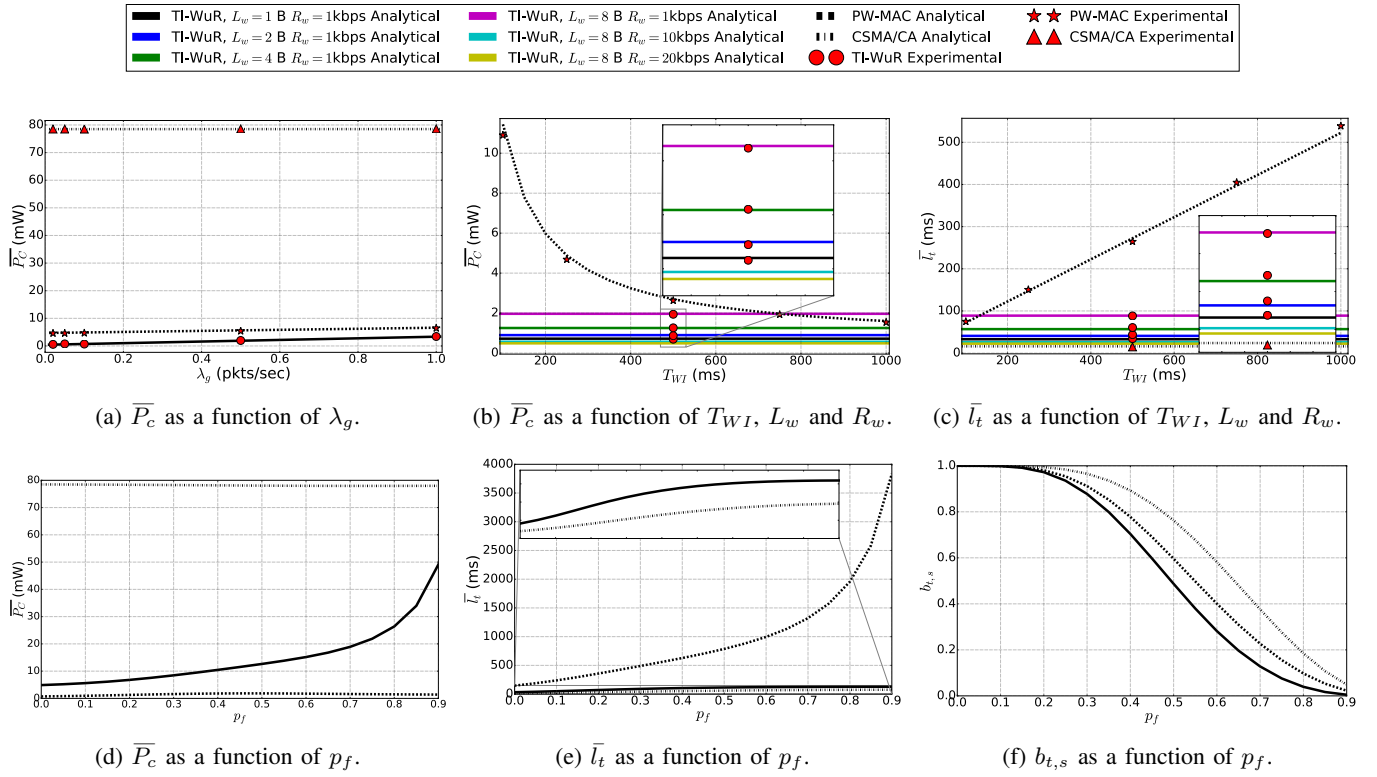


Fig. 4: Analytical and experimental results.

analytical models fit the experimental measurements well for the three protocols, therefore these results show the accuracy of the proposed framework.

Fig. 4a shows the impact of λ_g on the average consumed power \bar{P}_c when $T_{WI} = 250$ ms, $R_w = 1$ kbps and $L_w = 1$ byte. As expected from (6), \bar{P}_c increases linearly with λ_g and λ_r , and therefore similar results were obtained for λ_r , which are not shown in this paper. The average power consumption of the node with TI-WuR is at least twice lower than with PW-MAC, which can be explained by the fact that TI-WuR does not require periodic switch-on of the transceiver. As CSMA/CA requires the transceiver to be constantly listening to the channel, it consumes between 135 and 23 times more than TI-WuR and between 17 and 12 times more than PW-MAC depending of λ_g . These results show the energy saving enabled by WuRx in low throughput applications.

Fig. 4b exposes \bar{P}_c when T_{WI} , L_w and R_w vary. The power consumption of CSMA/CA is not shown, as it is up to 135 times higher than for the two other evaluated protocols. Higher values of T_{WI} leads to lower power consumption of PW-MAC, as the duty-cycle of the transceiver decreases with T_{WI} . For values of T_{WI} higher than 750 ms, the power consumption of PW-MAC becomes lower than the one of TI-WuR when $L_w = 8$ bytes and $R_w = 1$ kbps. However, this is at the cost of higher latency as shown by Fig. 4c. These results show the convenience of using WuRx in low throughput applications, as in the case of many WSNs scenarios, when low latency and low power consumption is required. Indeed, as PW-MAC relies on the duty-cycling approach, a node has to wait for its forwarder to turn its transceiver on before being able to

engage a communication with it, and the average waiting time is $\frac{T_{WI}}{2}$. Therefore, a compromise between power consumption and latency must be made using the traditional duty-cycling approach. When high bitrates are used for sending the WuB (10 kbps or 20 kbps), TI-WuR significantly outperforms PW-MAC regarding both the power consumption and the latency, even when the WuB size is 8 bytes as it can be seen on Fig. 4b and Fig. 4c. Because the WuRx from [9] is optimized for a bitrate of 1 kbps, using higher bitrate was not realized experimentally. The Fig. 4c also shows that the latency incurred by CSMA/CA is lower than both TI-WuR and PW-MAC, as it does not require any beacon sending or rendez-vous scheme. However, this is at the cost of letting the main transceiver always listening, incurring a power consumption of one order of magnitude higher than with TI-WuR. Moreover, using TI-WuR with $R_w = 10$ kbps permits to achieve a latency of 25.9 ms (22.0 ms if $R_w = 20$ kbps), while the latency of CSMA/CA is 16.2 ms in the same conditions.

Fig. 4d shows the impact of the frame failure probability p_f on \bar{P}_c when $T_{WI} = 250$ ms, $R_w = 1$ kbps and $L_w = 1$ byte. It can be seen that while p_f has a low impact on the average power consumption incurred by TI-WuR and PW-MAC, it has a strong impact on the average power consumption incurred by PW-MAC. Indeed, when a node sends a packet in PW-MAC to a forwarder node, the sending node first wakes up just before the forwarder sends its scheduled beacon. If no beacon is received, then the sending node assumes that the synchronization with the forwarder node was lost, keeping its transceiver active until a beacon from the forwarder node is received to resynchronize. Therefore, a high frame failure

probability (e.g. due to collisions or interferences) causes frequent resynchronization activities and therefore significantly increases the power consumption. This unwanted effect also leads to high packet transmission latency when p_f becomes high as shown by Fig. 4e, as the sending node waits for a valid beacon from the forwarder node. On the other hand, when an attempt to transmit a packet fails with TI-WuR of CSMA/CA, the sending node makes a new attempt until the transmission succeed or exceed a predefined number of re-transmission (set to 4 in this work), each re-transmission being preceded by a random backoff. Nonetheless, the functioning of PW-MAC leads to higher probability of successfully delivering the packet compared to TI-WuR as shown by Fig. 4f. However, the difference becomes significant when the probability of frame failure is higher than 20 %. CSMA/CA is the more reliable protocol, as only two frames must successfully be transmitted for the communication to be successful (the data frame and the ACK), while three frames must be successfully transmitted for TI-WuR and PW-MAC.

V. CONCLUSION

In this paper, a new framework to model MAC protocols that focuses on power consumption, latency and reliability was introduced. This framework has been employed to model PW-MAC, IEEE 802.15.4 beaconless mode, and TI-WuR, a protocol leveraging wake-up extended in this work. Experimental measurements demonstrated the validity of the model. Moreover, experimental and analytical results shown that WuRx enables significant power saving compared to PW-MAC, while incurring a latency close to the one of CSMA/CA. However, CSMA/CA requires the transceiver to be always on to listen the medium. Moreover, as for CSMA/CA, the latency and power consumption of TI-WuR are not strongly impacted by the frame failure probability. On the contrary, PW-MAC incurs high power consumption and high latency in bad channel condition. These results shown that using WuRx in low throughput WSNs applications allows both low power consumption and low latency, while using traditional approaches based on duty-cycling requires a compromise between these two performance metrics.

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