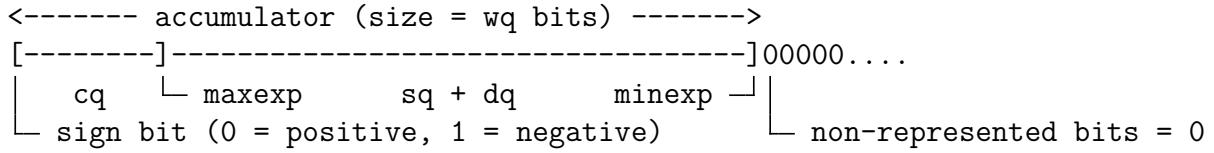
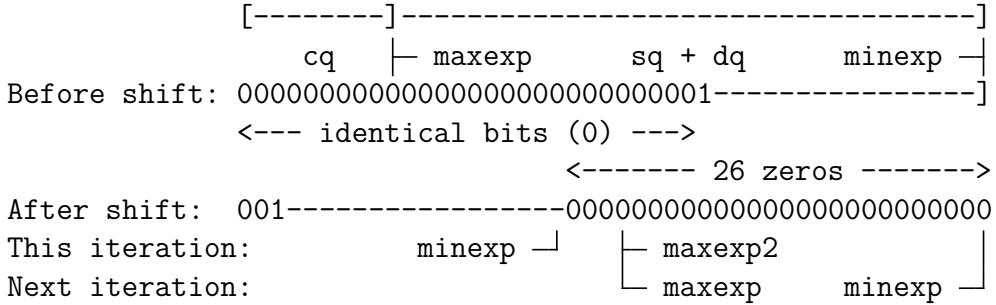


Figures

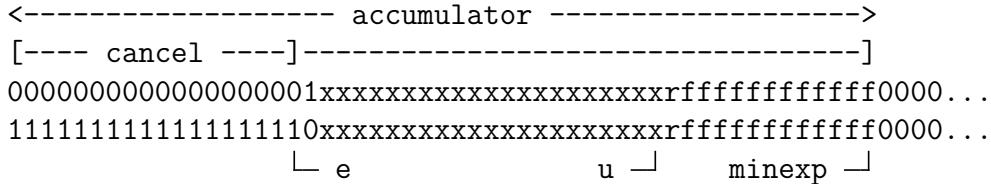
The accumulator:



Example of first iteration and after a cancellation with shiftq = 26:



After the first sum_raw invocation, case u > minexp:



x: bits of the uncorrected sq-bit significand (in two's complement),
after the first bit.

r: rounding bit (in two's complement).

f: represented bits that follow the rounding bit (in two's complement).