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# Addressed Term Rewriting Systems: Application to a Typed Object Calculus

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We present a formalism called *Addressed Term Rewriting Systems*, which can be used to model implementations of theorem proving, symbolic computation, and programming languages, especially aspects of sharing, recursive computations and cyclic data structures. Addressed Term Rewriting Systems are therefore well suited for describing object-based languages, and as an example we present a language called  $\lambda Obj^a$ , incorporating both functional and object-based features. As a case study in how reasoning about languages is supported in the ATRS formalism a type system for  $\lambda Obj^a$  is defined and a type soundness result is proved.

## 1. Introduction

### 1.1. *Addressed Calculi and Semantics of Sharing*

Efficient implementations of functional programming languages, computer algebra systems, theorem provers, and similar systems require some sharing mechanism to avoid multiple computations of a single argument. A natural way to model this sharing in a symbolic calculus is to pass from a tree representation of terms to directed *graphs*. Such term-graphs can be considered as a representation of program-expressions intermediate between abstract syntax trees and concrete representations in memory, and term-graph rewriting provides a formal operational semantics of functional programming sensitive to sharing. There is a wealth of research on the theory and applications of term-graphs; see for example (Barendregt, et al. 1987, Sleep, et al. 1993, Plump 1999, Blom 2001) for general treatments, and (Wadsworth 1971, Turner 1979, Ariola & Klop 1994, Ariola & Klop 1996, Ariola, et al. 1995, Ariola & Arvind 1995) for applications to  $\lambda$ -calculus and implementations.

This paper bridges two approaches to the implementation of functionally or declarative programming language, namely *graph rewriting* and *term rewriting*. The former provides an abstract approach for an efficient implementation whereas the latter is closer

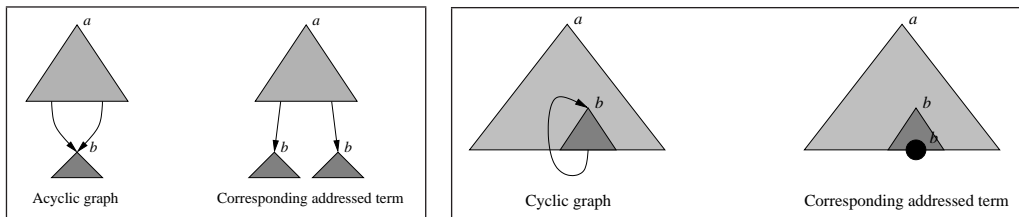


Figure 1. Sharing and Cycles Using Addresses

to ordinary mathematical notation and so stays closer to our intuition. The theory here applies to languages such as pure ML (Milner, et al. 1997, Leroy, et al. 2004), Haskell (Peyton Jones, et al. 2003), Clean (Plasmeijer & van Eekelen 2001), logic languages like Prolog (Kowalski 1979) or rule-based languages like Maude (Clavel, et al. 2003), Elan (Borovansky, et al. 1998) or Cafe-OBJ (Diaconescu & Futatsugi 1998).

In this paper we annotate terms, as trees, with *global addresses* in the spirit of (Felleisen & Friedman 1989, Rose 1996, Benaissa, et al. 1996). The approach to labeled term rewriting systems of Ohlebusch (Ohlebusch 2001) is similar to our formalism (in the acyclic case); Lévy (Lévy 1980) and Maranget (Maranget 1992) previously introduced *local addresses*. From the point of view of the operational semantics, global addresses describe better what is going on in a computer or an abstract machine.

Labeling intermediate terms with addresses is a novel technique for the analysis of the implementation of functional languages. A standard treatment would consist of translating a big-step rewriting semantics into an abstract machine (Diehl, et al. 2000). In our formalism addresses are part of the evaluation-terms and they are *de facto* decorated with locations. As a consequence the small-step semantics given by rewriting and the abstract machines are strictly coupled: the thesis is that machine implementation can be accurately modeled by term rewriting.

The formalisms of term-graph rewriting and addressed-term rewriting share much similarity but we feel that the addressed-term setting has several advantages. Our intention is to define a calculus that is as close to actual implementations as possible with the view of terms as tree-like structure, and the addresses in our terms really do correspond to memory references. To the extent that we are trying to build a bridge between theory and implementation we prefer this directness to the implicit coding inherent in a term-graph treatment.

With explicit global addresses we can keep track of the sharing that can be used in the implementation of a calculus. Sub-terms that share a common address represent the same sub-graphs, as suggested in Figure 1 (left), where  $a$  and  $b$  denote addresses. In (Dougherty, et al. 2002), addressed terms were studied in the context of *addressed term rewriting*, as an extension of classical first-order term rewriting. In addressed term rewriting we rewrite simultaneously all sub-terms sharing a same address, mimicking what would happen in an implementation.

We also enrich the sharing with a special *back-pointer* to handle *cyclic graphs* (Rose 1996). Cycles are used in the functional language setting to represent infinite data-structures and (in some implementations) to represent recursive code; they are also

interesting in the context of imperative object-oriented languages where *loops in the store* may be created by imperative updates through the use of `self` (or `this`). The idea of the representation of cycles via addressed terms is rather natural: a cyclic path in a finite graph is fully determined by a prefix path ended by a “jump” to some node of the prefix path (represented with a back-pointer), as suggested in Figure 1 (right).

The inclusion of explicit *indirection nodes* is a crucial innovation here. Indirection nodes allow us to give a more realistic treatment of the so-called collapsing rules of term-graph rewriting (rules that rewrite a term to one of its proper sub-terms). More detailed discussion will be found in Section 2.

### 1.2. Suitability of Addressed TRS for describing an Object-based Formalism

Recent years have seen a great deal of research aimed at providing a rigorous foundation for object-oriented programming languages. In many cases, this work has taken the form of “object-calculi” (Fisher, et al. 1994, Abadi & Cardelli 1996, Gordon & Hankin 2000, Igarashi, et al. 2001).

Such calculi can be understood in two ways. On the one hand, the formal system is a specification of the semantics of the language, and can be used as a formalism for classifying language design choices, to provide a setting for investigating type systems, or to support a denotational semantics. Alternatively, we may treat an object-calculus as an intermediate language into which user code (in a high-level object-oriented language) may be translated, and from which an implementation (in machine language) may be derived.

Several treatments of functional operational semantics exist in the literature (Landin 1964, Augustson 1984, Kahn 1987, Milner, et al. 1990). Addressed Term Rewriting Systems (originally motivated by implementations of lazy functional programming languages (Peyton-Jones 1987, Plasmeijer & van Eekelen 1993)) are the foundation of the  $\lambda\mathit{Obj}^a$  formalism (Lang, et al. 1999) for modeling object-oriented languages. The results in (Lang et al. 1999) showed how to model  $\lambda\mathit{Obj}^a$  using Addressed Term Rewriting Systems, but with no formal presentation of those systems. Here we expose the graph-based machinery underneath the rewriting semantics of  $\lambda\mathit{Obj}^a$ . To our knowledge, term-graph rewriting has been little explored in the context of the analysis of object-based programming.

The novelty of  $\lambda\mathit{Obj}^a$  is that it provides a *homogeneous* approach to both functional and object-oriented aspects of programming languages, in the sense the two semantics are treated in the same way using addressed terms, with only a minimal sacrifice in the permitted algebraic structures. Indeed, the addressed terms used were originally introduced to describe sharing behavior for functional programming languages (Rose 1996, Benaissa et al. 1996).

A useful way to understand the  $\lambda\mathit{Obj}^a$  formalism is by analogy with graph-reduction as an implementation-calculus for functional programming. Comparing  $\lambda\mathit{Obj}^a$  with the implementation techniques of functional programming (FP) and object oriented programming (OOP) gives the following correspondence. The  $\lambda\mathit{Obj}^a$  “modules” L, C, and F are defined in section 3.

Paradigm	$\lambda\mathcal{Obj}^a$ fragment	Powered by
Pure FP	$\lambda\mathcal{Obj}^a$ (L)	ATRS
Pure FP+OOP	$\lambda\mathcal{Obj}^a$ (L+C+F)	ATRS

### 1.3. Outline of the Paper

The paper is organized as follows: Section 2 details the formalism of addressed term rewriting systems and establishes a general relation between addressed term rewriting systems and first-order term rewriting systems. Section 3 puts addressed term rewriting systems to work by presenting the three modules of rewriting rules that form the core of  $\lambda\mathcal{Obj}^a$ . For pedagogical convenience we proceed in two steps: first we present the calculus  $\lambda\mathcal{Obj}^\sigma$ , intermediate between the calculus  $\lambda\mathcal{Obj}$  of Fisher, Honsell and Mitchell (Fisher et al. 1994) and our full calculus  $\lambda\mathcal{Obj}^a$ , then we scale up to  $\lambda\mathcal{Obj}^a$  itself. Section 4 presents a running object-based example in the  $\lambda\mathcal{Obj}^a$  formalism. Section 5 addresses the relationship between  $\lambda\mathcal{Obj}$  and  $\lambda\mathcal{Obj}^a$ . Section 6 proposes a type system for  $\lambda\mathcal{Obj}^a$ . Section 7 concludes. In appendix A we give a connection between a kind of ATRS's and TRS's.

This paper is an extended version of (Dougherty, et al. 2005) and originated in research presented in (Lang et al. 1999).

## 2. Addressed Term Rewriting Systems

In this section we introduce *addressed term rewriting systems* or ATRS. Classical term rewriting (Dershowitz & Jouannaud 1990, Klop 1990, Baader & Nipkow 1998) cannot easily express issues of sharing and mutation. Calculi that give an account of memory management often introduce some *ad-hoc* data-structure to model the memory, called *heap*, or *store*, together with access and update operations. However, the use of these structures necessitates restricting the calculus to a particular strategy. The aim of addressed term rewriting (and that of term-graph rewriting) is to provide a mathematical model of computation that reflects memory usage and is robust enough to be independent of the rewriting strategy.

*Sharing of computation.* Consider the reduction  $\text{square}(x) \rightarrow \text{times}(x, x)$ . In order to share subterms, addresses are inserted in terms making them *addressed terms*. For instance if we are to compute  $\text{square}(\text{square}(2))$ , we attach addresses  $a, b, c$  to the individual subterms. This yields  $\text{square}^a(\text{square}^b(2^c))$  which can then be reduced as follows:

$$\begin{aligned} \text{square}^a(\text{square}^b(2^c)) &\rightsquigarrow \text{times}^a(\text{square}^b(2^c), \text{square}^b(2^c)) \\ \text{times}^a(\text{times}^b(2^c, 2^c), \text{times}^b(2^c, 2^c)) &\rightsquigarrow \text{times}^a(4^b, 4^b) \rightsquigarrow 16^a, \end{aligned}$$

where “ $\rightsquigarrow$ ” designates a one step reduction with sharing. The key point of a shared computation is that *all* terms that share a common address are reduced *simultaneously*.

*Sharing of Object Structures.* It is important not only to share *computations*, but also to share *structures*. Indeed, objects are typically structures that receive multiple pointers. As an example, if we “zoom” on Figure 7, we can observe that the objects  $p$  and  $q$  share

a common structure addressed by  $b$ . This can be very easily formalized in the formalism, since addresses are first-class citizens. See Section 4.

*Cycles.* Cycles are essential in functional programming when one deals with infinite data-structures, as in lazy functional programming languages. Cycles are also used to save space in the code of recursive functions. Moreover in the context of object programming languages, cycles can be used to express *loops* which can be introduced in memory via lazy evaluation of recursive code.

### 2.1. Addressed Terms

*Addressed terms* are first order terms whose occurrences of operator symbols are decorated with addresses. They satisfy well-formedness constraints ensuring that every addressed term represents a connected piece of a store. Moreover, the label of each node sets the number of its successors. Addresses intuitively denote *node locations* in memory. Identical subtrees occurring at different paths can thus have the same address corresponding to the fact that the two occurrences are *shared*.

The definition is in two stages: the first stage defines the basic inductive term structure, called *preterms*, while the second stage just restricts preterms to well-formed preterms, or addressed terms.

#### Definition 2.1 (Preterms).

- 1 Let  $\Sigma$  be a term signature, and  $\bullet$  a special symbol of arity zero (a constant). Let  $\mathcal{A}$  be an enumerable set of *addresses* denoted by  $a, b, c, \dots$ , and  $\mathcal{X}$  an enumerable set of *variables*, denoted by  $X, Y, Z, \dots$ . An *addressed preterm*  $t$  over  $\Sigma$  is either a variable  $X$ , or  $\bullet^a$  where  $a$  is an address, or an expression of the form  $F^a(t_1, \dots, t_n)$  where  $F \in \Sigma$  (the label) has arity  $n \geq 0$ ,  $a$  is an address, and each  $t_i$  is an addressed preterm (inductively).
- 2 The location of an addressed preterm  $t$ , denoted by  $loc(t)$ , is defined by

$$loc(F^a(t_1, \dots, t_n)) \triangleq loc(\bullet^a) \triangleq a,$$

- 3 The sets of variables and addresses occurring within a preterm  $t$  are denoted by  $var(t)$  and  $addr(t)$ , respectively, and defined in the obvious way.

**Example 2.1.** The following are preterms

- $\text{times}^a(\text{times}^b(2^c, 2^c), \text{times}^b(2^d, 2^c))$
- $\text{times}^a(\text{times}^b(2^c, 2^c), \text{times}^b(2^c, 2^c))$
- $\text{times}^a(\text{times}^b(\bullet^c, \bullet^c), \text{times}^b(\bullet^c, \bullet^c))$

The first is “ill-formed” in the sense that the two subterms labeled with  $b$  are not the same; the latter two do not suffer from this anomaly, and are examples of what we call an *admissible* term: see Definition 2.4. For the first preterm above we have

- $loc(\text{times}^b(2^d, 2^c)) = loc(\text{times}^b(2^c, 2^c)) = b$ ,
- $var(\text{times}^a(\text{times}^b(x, x), \text{times}^b(x, x))) = \{x\}$ ,
- $addr(\text{times}^a(\text{times}^b(2^c, 2^c), \text{times}^b(2^c, 2^c))) = \{a, b, c\}$ ,

Addresses signify locations used to store a concrete term in memory; an open preterm  $t$  (a preterm with variables) is a notation to capture the family of concrete instantiations of  $t$ , and so the variables in  $t$  itself are never realized themselves. For this reason we do not assign addresses to variables in a term; they are meant to be replaced by addressed terms lying at any address.

The definition of a preterm makes use of a special symbol  $\bullet$  called a *back-pointer* and used to denote *cycles* (Rose 1996). A back-pointer  $\bullet^a$  in an addressed term must be such that  $a$  is an address occurring on the path from the root of the addressed term to the back-pointer node. It simply indicates at which address one has to branch (or point back) to go on along an infinite path.

**Example 2.2.** The infinite list made of 0's will be represented as  $\text{cons}^a(0^b, \bullet^a)$  and we can look for the  $5^{\text{th}}$  element, by applying the function  $\text{nth}$ , as follows:  $\text{nth}^c(5^b, \text{cons}^a(0^b, \bullet^a))$ .

An essential operation that we must have on addressed (pre)terms is the *unfolding* that allows seeing, on demand, what is beyond a back-pointer. Unfolding can therefore be seen as a *lazy operator* that traverses one step deeper in a cyclic graph. It is accompanied with its dual, called *folding*, that allows giving a minimal representation of cycles. Note however that folding and unfolding operations have *no operational meaning* in an actual implementation (hence *no operational cost*) but they are essential in order to represent correctly transformations between addressed terms.

### Definition 2.2 (Folding and Unfolding).

**Folding.** Let  $t$  be a preterm, and  $a$  be an address. We define  $\text{fold}(a)(t)$  as the *folding of preterms located at  $a$*  in  $t$  as follows:

$$\begin{aligned} \text{fold}(a)(X) &\triangleq X \\ \text{fold}(a)(\bullet^b) &\triangleq \bullet^b \quad (a \equiv b \text{ allowed here}) \\ \text{fold}(a)(F^a(t_1, \dots, t_n)) &\triangleq \bullet^a \\ \text{fold}(a)(F^b(t_1, \dots, t_n)) &\triangleq F^b(\text{fold}(a)(t_1), \dots, \text{fold}(a)(t_n)) \quad \text{if } a \not\equiv b \end{aligned}$$

**Unfolding.** Let  $s$  and  $t$  be preterms, such that  $\text{loc}(s) \equiv a$  (therefore defined), and  $a$  does not occur in  $t$  except as the address of  $\bullet^a$ . We define  $\text{unfold}(s)(t)$  as the *unfolding of  $\bullet^a$  by  $s$*  in  $t$  as follows:

$$\begin{aligned} \text{unfold}(s)(X) &\triangleq X \\ \text{unfold}(s)(\bullet^b) &\triangleq \begin{cases} s & \text{if } a \equiv b \\ \bullet^b & \text{otherwise} \end{cases} \\ \text{unfold}(s)(F^b(t_1, \dots, t_m)) &\triangleq F^b(t'_1, \dots, t'_m) \quad \text{where} \quad \begin{aligned} s' &\triangleq \text{fold}(b)(s) \\ t'_1 &\triangleq \text{unfold}(s')(t_1) \\ &\dots \\ t'_m &\triangleq \text{unfold}(s')(t_m) \end{aligned} \end{aligned}$$

Given an address  $a$ , the intended meaning of *folding* is to replace all the subterm at

address  $a$  by  $\bullet^a$ , when *unfolding* does somewhat the opposite as it replaces  $\bullet^a$  by the term it corresponds to (*i.e.* at the same address) above it.

**Example 2.3.**  $fold(a)(nth^c(5^b, cons^a(0^b, \bullet^a))) = nth^c(5^b, \bullet^a)$ .  
 $unfold(cons^a(0^b, \bullet^a))(nth^c(5^b, cons^a(0^b, \bullet^a))) = nth^c(5^b, cons^a(0^b, \bullet^a))$

We now proceed with the formal definition of *addressed terms* also called *admissible* preterms, or simply *terms*, for short, when there is no ambiguity. As already mentioned, addressed terms are preterms that denote term-graphs. From the point of view of addresses, terms are preterms where at each address one finds basically the same term, *i.e.* the same term modulo an appropriate treatment of  $\bullet$  or back pointers.

The notion of in-term helps to define addressed terms. The definition of addressed terms takes two steps: the first step is the definition of *dangling terms*, that are the sub-terms, in the usual sense, of actual addressed terms. Simultaneously, we define the notion of a dangling term, say  $s$ , at a given address, say  $a$ , in a dangling term, say  $t$ . When the dangling term  $t$  (*i.e.* the “out”-term) is known, we just call  $s$  an in-term. For a dangling term  $t$ , its in-terms are denoted by the function  $t @ \_$ , read “ $t$  at address  $\_$ ”, which returns a minimal and consistent representation of terms at each address, using the unfolding.

Therefore, there are two notions to be distinguished: on the one hand the usual well-founded notion of “sub-term”, and on the other hand the (no longer well-founded) notion of “term in another term”, or “in-term”. In other words, although it is not the case that a term is a proper sub-term of itself, it may be the case that a term is a proper in-term of itself or that a term is an in-term of one of its in-terms, due to cycles. The functions  $t_i @ \_$  are also used during the construction to check that all parts of the same term are consistent, mainly that all in-terms that share a same address are all the same dangling terms.

Dangling terms may have back-pointers that do not point anywhere because there is no node with the same address “above” in the term. The latter are called *dangling back-pointers*. For instance,  $times^a(\bullet^c, \bullet^c)$  has a dangling back-pointer, while  $cons^a(0^b, \bullet^a)$  has none. The second step of the definition restricts the addressed terms to the dangling terms that do not have dangling back-pointers. The following defines dangling terms and the function  $t @ \_$  from  $addr(t)$  to dangling in-terms. Intuitively,  $t @ a$  returns the in-term of  $t$  at address  $a$ .

### Definition 2.3 (Dangling Addressed Terms).

We define the set  $DT(\Sigma)$  of *dangling addressed terms* inductively and simultaneously define the function  $t @ \_$ :

Variables.

- Each variable  $X$  is in  $DT(\Sigma)$
- $X @ \_$  is nowhere defined.

Back-pointers.

- $\bullet^a \in DT(\Sigma)$
- $\bullet^a @ a \equiv \bullet^a$



Expressions.

- $t \equiv F^a(t_1, \dots, t_n) \in DT(\Sigma)$  provided that
  - $F \in \Sigma$  of arity  $n$ ,
  - $t_1 \in DT(\Sigma), \dots, t_n \in DT(\Sigma)$  satisfy:  $b \in \text{addr}(t_i) \cap \text{addr}(t_j) \Rightarrow t_i @ b \equiv t_j @ b$ , and
  - $a$  is an address satisfying  $a \in \text{addr}(t_i) \Rightarrow t_i @ a \equiv \bullet^a$
- $t @ a \equiv t$ ; and if  $b \in \text{addr}(t_i)$ ,  $b \neq a$ ,  $t @ b \equiv \text{unfold}(t)(t_i @ b)$ .

**Example 2.4.** Let  $t = \text{cons}^a(0^c, \text{cons}^b(1^d, \bullet^a))$ . Then  $t @ b = \text{cons}^b(1^d, \text{cons}^a(0^c, \text{cons}^b(1^d, \bullet^a)))$ . We say that  $\text{cons}^b(1^d, \bullet^a)$  is a dangling term.

*Admissible addressed terms* are those where all  $\bullet^a$  do point back to something in  $t$  such that a complete (possibly infinite) unfolding of the term exists. The only way we can observe this with the  $t @ \_$  function is through checking that no  $\bullet^a$  can “escape” because this cannot happen when it points back to something.

**Definition 2.4 (Addressed Term).**

A dangling addressed term  $t$  is *admissible* if  $a \in \text{addr}(t) \Rightarrow t @ a \neq \bullet^a$ . An admissible dangling addressed term will be simply denoted an *addressed term*.

**Example 2.5.**

- The following are admissible terms:
  - $\text{times}^a(\text{times}^b(2^c, 2^c), \text{times}^b(2^c, 2^c))$ ,
  - $\text{nth}^c(5^b, \text{cons}^a(0^b, \bullet^a))$ ,
  - $\text{cons}^a(0^c, \text{cons}^b(1^d, \bullet^a))$ .

It is easy to see that an in-term of an admissible term is admissible.

## 2.2. Addressed Term Rewriting

The reduction of an addressed term must return an addressed term (not just a preterm). In other words, the computation model (here addressed term rewriting) must take into account the sharing information given by the addresses, and must be defined as the *smallest rewriting relation preserving admissibility between addressed terms*. Hence, a computation has to take place simultaneously at several places in the addressed term, namely at the places located at the same address. This simultaneous update of terms corresponds to the update of a location in the memory in a real implementation.

In an ATRS, a rewriting rule is a *pair of open addressed terms* (i.e., containing variables) at the same location. The way addressed term rewriting proceeds on an addressed term  $t$  is not so different from the way usual term rewriting does; conceptually there are four steps.

- 1 *Find a redex in  $t$ , i.e.* an in-term *matching* the left-hand side of a rule. Intuitively, an addressed term matching is the same as a classical term matching, except there is a new kind of variables, called addresses, which can only be substituted by addresses.

- 2 Create *fresh addresses*, *i.e.* addresses not used in the current addressed term  $t$ , which will correspond to the locations occurring in the right-hand side, but not in the left-hand side (*i.e.* the new locations).
- 3 *Substitute the variables and addresses* of the right-hand side of the rule by their new values, as assigned by the matching of the left-hand side or created as fresh addresses. Let us call this new addressed term  $u$ .
- 4 For all  $a$  that occur both in  $t$  and  $u$ , the result of the rewriting step, say  $t'$ , will have  $t' @ a \equiv u @ a$ , otherwise  $t'$  will be equal to  $t$ .

We give the formal definition of matching and replacement, and then we define rewriting precisely. The reader may find it useful to look ahead to section 2.3 where we provide examples of rewriting with ATRSs: this will help motivate the following technical definitions.

**Definition 2.5 (Substitution, Matching, Unification).**

- 1 Mappings from addresses to addresses are called *address substitutions*. Mappings from variables to addressed terms are called *variable substitutions*. A pair of an address substitution  $\alpha$  and a variable substitution  $\sigma$  is called a *substitution*, and it is denoted by  $\langle \alpha; \sigma \rangle$ .
- 2 Let  $\langle \alpha; \sigma \rangle$  be a substitution and  $p$  a term such that  $\text{addr}(p) \subseteq \text{dom}(\alpha)$  and  $\text{var}(p) \subseteq \text{dom}(\sigma)$ . The application of  $\langle \alpha; \sigma \rangle$  to  $p$ , denoted by  $\langle \alpha; \sigma \rangle(p)$ , is defined inductively as follows:

$$\begin{aligned} \langle \alpha; \sigma \rangle(\bullet^a) &\triangleq \bullet^{\alpha(a)} \\ \langle \alpha; \sigma \rangle(X) &\triangleq \sigma(X) \\ \langle \alpha; \sigma \rangle(F^a(p_1, \dots, p_m)) &\triangleq F^{\alpha(a)}(q_1, \dots, q_m) \text{ and } q_i \triangleq \text{fold}(\alpha(a))(\langle \alpha; \sigma \rangle(p_i)) \end{aligned}$$

- 3 We say that a term  $t$  *matches* a term  $p$  if there exists a substitution  $\langle \alpha; \sigma \rangle$  such that  $\langle \alpha; \sigma \rangle(p) \equiv t$ .
- 4 We say that two terms  $t$  and  $u$  *unify* if there exists a substitution  $\langle \alpha; \sigma \rangle$  and an addressed term  $v$  such that  $v \equiv \langle \alpha; \sigma \rangle(t) \equiv \langle \alpha; \sigma \rangle(u)$ .

We now define *replacement*. The replacement function operates on terms. Given a term, it changes some of its in-terms at given locations by other terms with the same address. Unlike classical term rewriting (see for instance (Dershowitz & Jouannaud 1990) pp. 252) the places where replacement is performed are simply given by addresses instead of paths in the term.

**Definition 2.6 (Replacement).**

Let  $t, u$  be addressed terms. The replacement generated by  $u$  in  $t$ , denoted by  $\text{repl}(u)(t)$

is defined as follows:

$$\begin{aligned} \text{repl}(u)(X) &\triangleq X \\ \text{repl}(u)(\bullet^a) &\triangleq \begin{cases} u @ a & \text{if } a \in \text{addr}(u) \\ \bullet^a & \text{otherwise,} \end{cases} \\ \text{repl}(u)(F^a(t_1, \dots, t_m)) &\triangleq \begin{cases} u @ a & \text{if } a \in \text{addr}(u) \\ F^a(\text{repl}(u)(t_1), \dots, \text{repl}(u)(t_m)) & \text{otherwise} \end{cases} \end{aligned}$$

The replacement of addressed term is the generalization of the usual notion of replacement for terms that takes account of addresses in two respects: terms at same address must be replaced simultaneously bullets must be unfolded appropriately to avoid dangling pointers.

An easy induction on the structure of dangling terms shows show

**Proposition 2.1 (Replacement Admissibility).**

If  $t$  and  $u$  are addressed terms, then  $\text{repl}(u)(t)$  is an addressed term.

We now define the notions of redex and rewriting.

**Definition 2.7 (Addressed Rewriting Rule).**

An addressed rewriting rule over  $\Sigma$  is a pair of addressed terms  $(l, r)$  over  $\Sigma$ , written  $l \rightsquigarrow r$ , such that  $\text{loc}(l) \equiv \text{loc}(r)$  and  $\text{var}(r) \subseteq \text{var}(l)$ . Moreover, if there are addresses  $a, b$  in  $\text{addr}(l) \cap \text{addr}(r)$  such that  $l @ a$  and  $l @ b$  are unifiable, then  $r @ a$  and  $r @ b$  must be unifiable with the same unifier.

The square redex, given at the beginning of this section, is an example of addressed rewriting rule. This example is somewhat simple, since rules may in general contain addresses, but Figure 6 contains plenty of examples of rules with addresses.

The condition  $\text{var}(r) \subseteq \text{var}(l)$  ensures that there is no creation of variables. The condition  $\text{loc}(l) \equiv \text{loc}(r)$  says that  $l$  and  $r$  have the same top address, therefore  $l$  and  $r$  are not variables.

**Remark 2.1 (The right-hand side restriction).** In the definition of rewriting rule we forbid the right-hand side to be a variable. It may appear that this limits the expressive power of ATRS's. On the contrary this is an interesting feature of ATRS's which highlights our attention to implementation concerns. Suppose for example that we were to have a rule  $F^a(x) \rightarrow x$ , to be applied in an in-term  $F^a(t^b)$  of a term. (Remember that variables have no addresses.) Then  $F^a(t^b)$  would be rewritten to  $t^b$ . Do we intend that  $t^b$  is also at address  $a$ ? If yes, this means we have to have a means to redirect all pointers to  $a$  toward  $b$ . Surely such acrobatics should not be part of a term-rewriting system – nor an implementation of rewriting! So we forbid such situations. We can still encode the rule capturing the equation  $F(x) = x$ , by using indirection nodes (see the paragraph on evaluation contexts in Section 3.2). In the case of the above rule  $F^a(x) \rightarrow x$ , the trick would be to introduce a unary operator  $[\_ ]^a$  (the indirection) and replace the rule by  $F^a(x) \rightarrow [x]^a$  which then fulfills the restriction. Therefore other rules have to be

introduced to remove the indirection, for instance a rule  $G^b(\lceil x \rceil^a) \rightarrow G^b(x)$ , which also fulfills the restriction.

**Definition 2.8 (Redex).**

A term  $t$  is a *redex* for a rule  $l \rightsquigarrow r$ , if  $t$  matches  $l$ . A term  $t$  has a *redex*, if there exists an address  $a \in \text{addr}(t)$  such that  $t@_a$  is a redex.

In the `square` example at the beginning of this section, both  $\text{square}^a(\text{square}^b(2^c))$  and  $\text{square}^b(2^c)$  are redexes. Note that, in general, we do not impose restrictions as linearity in addresses (*i.e.* the same address may occur twice), or acyclicity of  $l$  and  $r$ . Besides redirecting pointers, ATRS create *new* nodes. *Fresh renaming* insures that these new node addresses are not already used.

**Definition 2.9 (Fresh Renaming).**

- 1 We denote by  $\text{dom}(\varphi)$  and  $\text{rng}(\varphi)$  the usual *domain* and *range* of a function  $\varphi$ .
- 2 A *renaming* is an injective address substitution.
- 3 Let  $t$  be a term having a redex for the addressed rewriting rule  $l \rightsquigarrow r$ . A renaming  $\alpha_{\text{fresh}}$  is *fresh* for  $l \rightsquigarrow r$  with respect to  $t$  if  $\text{dom}(\alpha_{\text{fresh}}) = \text{addr}(r) \setminus \text{addr}(l)$  *i.e.* the renaming renames each newly introduced address to avoid capture, and  $\text{rng}(\alpha_{\text{fresh}}) \cap \text{addr}(t) = \emptyset$ , *i.e.* the chosen addresses are not present in  $t$ .

**Proposition 2.2 (Substitution Admissibility).**

Given an admissible term  $t$  that has a redex for the addressed rewriting rule  $l \rightsquigarrow r$ . Then

- 1 A fresh renaming  $\alpha_{\text{fresh}}$  exists for  $l \rightsquigarrow r$  with respect to  $t$ .
- 2  $\langle \alpha \cup \alpha_{\text{fresh}}; \sigma \rangle(r)$  is admissible.

*Proof.* The admissibility of  $t$  and  $l$  ensures that the substitution  $\langle \alpha; \sigma \rangle$  satisfies some well-formedness property, in particular the set  $\text{rng}(\sigma)$  is a set of mutually admissible terms in the sense that the parts they share together are consistent (or in other words, the preterm obtained by giving these terms a common root, with a fresh address, is an addressed term).

The use of  $\alpha_{\text{fresh}}$  both ensures that all addresses of  $r$  are in the domain of the substitution, and that their images by  $\alpha$  will not clash with existing addresses.

The definition of substitution takes care in maintaining admissibility for such substitutions, in particular the management of back-pointers. These properties are sufficient to ensure the admissibility of  $\langle \alpha \cup \alpha_{\text{fresh}}; \sigma \rangle(r)$ .  $\square$

At this point, we have given all the definitions needed to specify rewriting.

**Definition 2.10 (Rewriting).**

Let  $t$  be a term that we want to reduce at address  $a$  by rule  $l \rightsquigarrow r$ . Proceed as follows:

- 1 Ensure  $t@_a$  is a redex. Let  $\langle \alpha; \sigma \rangle(l) \hat{=} t@_a$ .
- 2 Compute  $\alpha_{\text{fresh}}$ , a fresh renaming for  $l \rightsquigarrow r$  with respect to  $t$ .
- 3 Compute  $u \equiv \langle \alpha \cup \alpha_{\text{fresh}}; \sigma \rangle(r)$ .
- 4 The result  $s$  of rewriting  $t$  by rule  $l \rightsquigarrow r$  at address  $a$  is  $\text{repl}(u)(t)$ . We write the reduction  $t \rightsquigarrow s$ , defining “ $\rightsquigarrow$ ” as the relation of all such rewritings.

**Theorem 2.1 (Closure under Rewriting).**

Let  $R$  be an addressed term rewriting system and  $t$  be an addressed term. If  $t \rightsquigarrow u$  in  $R$  then  $u$  is also an addressed term.

*Proof.* The proof essentially walks through the steps of Definition 2.10, showing that each step preserves admissibility.

Let  $\mathcal{R}$  be an addressed term rewriting system and  $t$  be an addressed term rewritten by the rule  $l \rightsquigarrow r$ . All of  $t$ ,  $l$ , and  $r$ , are admissible. For the rewrite to be defined, we furthermore know that  $t$  has a redex

$$t' \triangleq t @ a \equiv \langle \alpha; \sigma \rangle (l)$$

The term  $t'$  is admissible and by Proposition 2.2, we can find a renaming  $\alpha_{\text{fresh}}$  that is fresh for  $l \rightsquigarrow r$  with respect to  $t'$  and we know that  $u \equiv \langle \alpha \cup \alpha_{\text{fresh}}; \sigma \rangle (r)$  is admissible. Proposition 2.1 finally ensures that  $\text{repl}(u)(t)$  is admissible.  $\square$

Actually ATRS generalize TRS and we show in appendix A that acyclic ATRS which do not mutate terms, i.e., which do not replace a subterm by another subterm in place actually simulate TRS's.

**2.3. Rewriting examples**

**Example 2.6.** As an easy application of the definition of rewriting we can revisit our square example, and see that  $\text{times}^a(\text{square}^b(2^c), \text{square}^b(2^c))$  rewrites to  $\text{times}^a(\text{times}^b(2^c, 2^c), \text{times}^b(2^c, 2^c))$ .

**Example 2.7 (Cycles).** Consider the term  $t = H^c(F^b(G^a(\bullet^b)), G^a(F^b(\bullet^a)))$ , and  $l \rightarrow r = G^a(x) \rightarrow F^a(x)$ . We want to reduce  $t$  at address  $a$ . We first compute the matching  $\langle \alpha; \sigma \rangle = \langle \{a \mapsto a\}; \{x \mapsto F^b(G^a(\bullet^b))\} \rangle$ . Since  $r$  does not contain fresh variables, then  $\sigma_{\text{fresh}} = \emptyset$ .  $s = \langle \alpha; \sigma \rangle (F^a(x)) = F^a(F^b(\bullet^a))$ . The result of rewriting is:

$$\text{repl}(\{a \mapsto F^a(F^b(\bullet^a)), b \mapsto F^b(F^a(\bullet^b))\})(t) = H^c(F^b(F^a(\bullet^b)), F^a(F^b(\bullet^a)))$$

The corresponding transformation on graphs is shown in Figure 2.

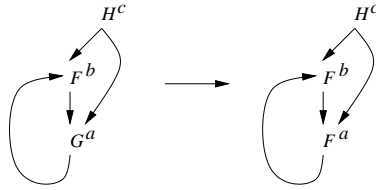


Figure 2. Graph Rewrite equivalent to Addressed Term Rewrite of Example 2.7.

**Example 2.8.** Consider the term  $t = G^a(H^b(\bullet^a))$ , and the rule

$$l \rightarrow r = H^a(G^b(x)) \rightarrow H^a(x).$$

We want to reduce  $t$  at address  $b$ . Note that there is a redex at address  $b$  even though it does not appear syntactically. Indeed,  $t @ b = H^b(G^a(\bullet^b))$ , and

$$\mathit{match}(H^a(G^b(x)))(H^b(G^a(\bullet^b))) = \langle \{a \mapsto b, b \mapsto a\}; \{x \mapsto H^b(G^a(\bullet^b))\} \rangle.$$

We denote this matching by  $\langle \alpha; \sigma \rangle$ . Note that  $\sigma_{\text{fresh}} = \emptyset$  and  $s = \langle \alpha; \sigma \rangle(H^a(x)) = H^b(\langle \alpha; \{x \mapsto \bullet^b\} \rangle(x)) = H^b(\bullet^b)$ . The result of rewriting  $t$  is

$$\mathit{repl}(\{b \mapsto H^b(\bullet^b)\})(t) = G(H^b(\bullet^b))$$

**Example 2.9 (Mutation).** The following shows how side effects are performed, using the well-known example of `setcar`.

Consider the symbols `nil` of arity 0, and `cons` of arity 2, the constructors of lists. `car`, of arity 1 is the operator which returns the first element of a list, and `setcar`, of arity 2, the operator which affects a new value to the first element of the list, performing a side effect. We also have an infinite number of symbols denoted by  $1, 2, \dots, n, \dots$ , and the operation `plus` of arity 2. At last, we have the special symbol  $\lceil \rceil$  of arity 1 denoting an indirection node. The rules for the definition of `car` and `setcar` are the following:

$$\begin{aligned} \mathit{setcar}^a(\mathit{cons}^b(y, z), x) &\rightarrow \lceil \mathit{cons}^b(x, z) \rceil^a && \text{(Set-Car)} \\ \mathit{setcar}^a(\lceil z \rceil^b, x) &\rightarrow \mathit{setcar}^a(z, x) && \text{(Elim-Ind)} \\ \mathit{car}^a(\mathit{cons}^b(n^c, z)) &\rightarrow \lceil n^c \rceil^a && \text{(Car-}n\text{)} \end{aligned}$$

We omit a definition of `plus` here. Consider the term

$$t = \mathit{plus}^e(\mathit{car}^c(\mathit{setcar}^a(\mathit{cons}^b(1^f, \mathit{nil}^h), 2^g)), \mathit{car}^d(\mathit{cons}^b(1^f, \mathit{nil}^h)))$$

in which the list  $\mathit{cons}^b(1^f, \mathit{nil}^h)$  appears twice, which means that it is shared. Starting by applying the rule for `setcar`, the reduction proceeds as follows:

$$\begin{aligned} t &\rightarrow \mathit{plus}^e(\mathit{car}^c(\lceil \mathit{cons}^b(2^g, \mathit{nil}^h) \rceil^a), \mathit{car}^d(\mathit{cons}^b(2^g, \mathit{nil}^h))) && \text{(Set-Car)} \\ &\rightarrow \mathit{plus}^e(\mathit{car}^c(\mathit{cons}^b(2^g, \mathit{nil}^h)), \mathit{car}^d(\mathit{cons}^b(2^g, \mathit{nil}^h))) && \text{(Elim-Ind)} \\ &\rightarrow \mathit{plus}^e(2^c, \mathit{car}^d(\mathit{cons}^b(2^g, \mathit{nil}^h))) && \text{(Car)} \\ &\rightarrow \mathit{plus}^e(2^c, 2^d) && \text{(Car)} \\ &\rightarrow 4^e && \text{(Plus)} \end{aligned}$$

On the other hand, starting by applying the rule for `car`, the computation proceeds as follows:

$$\begin{aligned} t &\rightarrow \mathit{plus}^e(\mathit{car}^c(\mathit{setcar}^a(\mathit{cons}^b(1^f, \mathit{nil}^h), 2^g)), 1^d) && \text{(Car)} \\ &\rightarrow \mathit{plus}^e(\mathit{car}^c(\lceil \mathit{cons}^b(2^g, \mathit{nil}^h) \rceil^a), 1^d) && \text{(Set-Car)} \\ &\rightarrow \mathit{plus}^e(\mathit{car}^c(\mathit{cons}^b(2^g, \mathit{nil}^h)), 1^d) && \text{(Elim-Ind)} \\ &\rightarrow \mathit{plus}^e(2^c, 1^d) && \text{(Car)} \\ &\rightarrow 3^e && \text{(Plus)} \end{aligned}$$

The above example shows that ATRS may produce side effects. It is essential to be

able to determine easily whether a system has side effects or not. For instance, a side effect appears clearly in the rule (Set-Car), since the term  $\text{cons}^b(y, z)$  becomes  $\text{cons}^b(x, z)$  whereas the rewriting takes place at address  $a$  in a term of shape  $\text{setcar}^a(\square, \square)$ . Rules which do not host such transformations have no side effect.

### 3. Modeling an Object-based Formalism via ATRS: $\lambda\text{Obj}^a$

The purpose of this section is to describe the top level rules of the  $\lambda\text{Obj}^a$ , a formalism strongly based on acyclic ATRS introduced in the previous section.

The formalism is described by a set of rules arranged in *modules*. The three modules are called respectively **L**, **C**, and **F**, and they will be introduced in Subsection 3.3.

**L** is the *functional* module, and is essentially the calculus  $\lambda\sigma_w^a$  of (Benaissa et al. 1996).

This module alone defines the core of a purely functional programming language based on  $\lambda$ -calculus and weak reduction.

**C** is the *common object* module, and contains all the rules common to all instances of object calculi defined from  $\lambda\text{Obj}^a$ . It contains rules for instantiation of objects and invocation of methods.

**F** is the module of *functional update*, containing the rules needed to implement object update.

The set of rules **L** + **C** + **F** is the instance of  $\lambda\text{Obj}^a$  for functional object calculi.

We do this in two steps:

- 1 first we present the functional calculus  $\lambda\text{Obj}^\sigma$ , intermediate between the calculus  $\lambda\text{Obj}$  of Fisher, Honsell and Mitchell (Fisher et al. 1994) and our  $\lambda\text{Obj}^a$ ,
- 2 then we scale up over the full  $\lambda\text{Obj}^a$ .

By a slight abuse of terminology we may say that  $\lambda\text{Obj}^a$  is a *conservative extension* of  $\lambda\text{Obj}^\sigma$ , in the sense that for computations in  $\lambda\text{Obj}^a$  and computations in  $\lambda\text{Obj}^\sigma$  return the same normal form (Theorems 5.1 and 5.2). Since a  $\lambda\text{Obj}^a$ -term yields a  $\lambda\text{Obj}^\sigma$ -term by erasing addresses and indirections, one corollary of this conservativeness is *address-irrelevance*, *i.e.* the observation that the program layout in memory cannot affect the eventual result of the computation. This is an example of how an informal reasoning about implementations can be translated in  $\lambda\text{Obj}^a$  and formally justified.

#### 3.1. Syntax and operational semantics of $\lambda\text{Obj}^\sigma$

$\lambda\text{Obj}^\sigma$  is a language without addresses (see the syntax in Figure 3). The rules of  $\lambda\text{Obj}^\sigma$  are presented in Figure 4. As noted earlier, terms of this calculus are terms of  $\lambda\text{Obj}^a$  without addresses and without indirection nodes. The rules of  $\lambda\text{Obj}^\sigma$  are properly contained in those of modules **L** + **C** + **F** of  $\lambda\text{Obj}^a$ . One can also say that  $\lambda\text{Obj}^\sigma$  is the *lambda calculus of objects* proposed by Fisher, Honsell and Mitchell in (Fisher et al. 1994) enriched with explicit substitutions. The first category of expressions is the *code* of programs. Terms that define the code have no addresses, because code contains no environment and is not subject to any change during the computation (remember that addresses are meant to tell the computing engine which parts of the computation structure can or have to

$M, N ::= \lambda x.M \mid MN \mid x \mid c \mid$	
$\langle \rangle \mid \langle M \leftarrow \mathbf{m} = N \rangle \mid M \leftarrow \mathbf{m}$	(Code)
$U, V ::= M[s] \mid UV \mid$	
$U \leftarrow \mathbf{m} \mid \langle U \leftarrow \mathbf{m} = V \rangle \mid Sel(O, \mathbf{m}, U)$	(Eval. Contexts)
$O ::= \langle \rangle \mid \langle O \leftarrow \mathbf{m} = V \rangle$	(Object Structures)
$s ::= U/x ; s \mid id$	(Substitutions)

Figure 3. The Syntax of  $\lambda Obj^\sigma$ 

Basics for Substitutions			
$(MN)[s]$	$\rightsquigarrow$	$(M[s] N[s])$	(App)
$((\lambda x.M)[s] U)$	$\rightsquigarrow$	$M [U/x ; s]$	(Bw)
$x[U/y ; s]$	$\rightsquigarrow$	$x[s]$	$x \neq y$ (RVar)
$x[U/x ; s]$	$\rightsquigarrow$	$U$	(FVar)
$\langle M \leftarrow \mathbf{m} = N \rangle [s]$	$\rightsquigarrow$	$\langle M[s] \leftarrow \mathbf{m} = N[s] \rangle$	(P)
Method Invocation			
$(M \leftarrow \mathbf{m})[s]$	$\rightsquigarrow$	$(M[s] \leftarrow \mathbf{m})$	(SP)
$(O \leftarrow \mathbf{m})$	$\rightsquigarrow$	$Sel(O, \mathbf{m}, O)$	(SA)
$Sel(\langle O \leftarrow \mathbf{m} = U \rangle, \mathbf{m}, V)$	$\rightsquigarrow$	$(UV)$	(SU)
$Sel(\langle O \leftarrow \mathbf{n} = U \rangle, \mathbf{m}, V)$	$\rightsquigarrow$	$Sel(O, \mathbf{m}, V)$	$\mathbf{m} \neq \mathbf{n}$ (NE)

Figure 4. The Rules of  $\lambda Obj^\sigma$ 

change simultaneously). The second and third categories define dynamic entities, or inner structures: the *evaluation contexts*, and the *internal structure of objects* (or simply *object structures*). The last category defines *substitutions* also called *environments*, *i.e.*, lists of terms bound to variables, that are to be distributed and augmented over the code.

*Notation.* The “;” operator acts as a “cons” constructor for lists, with the environment  $id$  acting as the empty, or identity, environment. By analogy with traditional notation



for lists we adopt the following aliases:

$$\begin{aligned} M[\ ]^a &\triangleq M[\text{id}]^a \\ M[U_1/x_1; \dots; U_n/x_n]^a &\triangleq M[U_1/x_1; \dots; U_n/x_n; \text{id}]^a \end{aligned}$$

The code category is essentially the Lambda Calculus of Objects of (Fisher et al. 1994), and its operational semantics is the one presented in (Gianantonio, et al. 1998) and briefly recalled below:

**(Gianantonio et al. 1998) Operational Semantics.**

$$\begin{aligned} (\lambda x.M) N &\rightsquigarrow M\{N/x\} && \text{(Beta)} \\ M \leftarrow \mathfrak{m} &\rightsquigarrow \text{Sel}(M, \mathfrak{m}, M) && \text{(Select)} \\ \text{Sel}(\langle M \leftarrow \mathfrak{m} = N \rangle, \mathfrak{m}, P) &\rightsquigarrow NP && \text{(Success)} \\ \text{Sel}(\langle M \leftarrow \mathfrak{n} = N \rangle, \mathfrak{m}, P) &\rightsquigarrow \text{Sel}(M, \mathfrak{m}, P) \quad \mathfrak{m} \neq \mathfrak{n} && \text{(Next)} \end{aligned}$$

The main difference between  $\lambda\text{Obj}^\sigma$  and  $\lambda\text{Obj}$  (Fisher et al. 1994) lies in the use of a single operator  $\leftarrow$  for building an object from an existing prototype. If the object  $M$  contains  $\mathfrak{m}$ , then  $\leftarrow$  denotes an object override, otherwise  $\leftarrow$  denotes an object extension. The principal operation on objects is method invocation, whose reduction is defined by the (Select) rule. Sending a message  $\mathfrak{m}$  to an object  $M$  containing a method  $\mathfrak{m}$  reduces to  $\text{Sel}(M, \mathfrak{m}, M)$ . The arguments of  $\text{Sel}$  in  $\text{Sel}(M, \mathfrak{m}, P)$  have the following intuitive meaning (in reverse order)

- $P$  is the receiver (or recipient) of the message;
- $\mathfrak{m}$  is the message we want to send to the receiver of the message;
- $M$  is (or reduces to) a proper sub-object of the receiver of the message.

We note that the (Beta) rule above is given using the *meta substitution* (denoted by  $\{N/x\}$ ), as opposed to the *explicit substitution* used in  $\lambda\text{Obj}^a$ . Finally, the semantics presented in (Gianantonio et al. 1998) is not restricted to weak reduction (“no reduction under lambdas”) as is that in  $\lambda\text{Obj}^a$ .

By looking at the last two rewrite rules, one may note that the  $\text{Sel}$  function “scans” the recipient of the message until it finds the definition of the method we want to use. When it finds the body of the method, it applies this body to the recipient of the message. Notice that  $\text{Sel}$  somewhat destroys the object. In order to keep track of the objects on which the method when found has to be applied, a third parameter is added to  $\text{Sel}$ .

### 3.2. Syntax of $\lambda\text{Obj}^a$

The syntax of  $\lambda\text{Obj}^a$  is summarized in Figure 5. As for  $\lambda\text{Obj}^\sigma$  terms that define the code have no addresses (the same for substitutions). In contrast, terms in evaluation contexts have explicit addresses.

“Fresh” addresses are often provided to evaluation contexts, when distributing the environment (a fresh address is an address unused in a global term). Intuitively, the address of an evaluation context is the address where the result of the computation will be

$M, N$	$::= \lambda x.M \mid MN \mid x \mid c \mid \langle \rangle \mid \langle M \leftarrow m = N \rangle \mid M \leftarrow m$	(Code)
$U, V, W$	$::= M[s]^a \mid (UV)^a \mid [U]^a \mid$ $\langle U \leftarrow m = V \rangle^a \mid (U \leftarrow m)^a \mid Sel^a(O, m, V) \mid$	(Eval. Contexts)
$O$	$::= \langle \rangle \mid \langle O \leftarrow m = V \rangle$	Object Structures
$s$	$::= U/x ; s \mid id$	(Substitutions)

Figure 5. The Syntax of  $\lambda Obj^a$ 

<b>The Module L</b>		
$(MN)[s]^a$	$\rightsquigarrow (M[s]^b N[s]^c)^a$	(App)
$((\lambda x.M)[s]^b U)^a$	$\rightsquigarrow M[U/x ; s]^a$	(Bw)
$x[U/x ; s]^a$	$\rightsquigarrow [U]^a$	(FVar)
$x[U/y ; s]^a$	$\rightsquigarrow x[s]^a \quad x \neq y$	(RVar)
$([U]^b V)^a$	$\rightsquigarrow (UV)^a$	(AppRed)
<b>The Module C</b>		
$(M \leftarrow m)[s]^a$	$\rightsquigarrow (M[s]^b \leftarrow m)^a$	(SP)
$(U \leftarrow m)^a$	$\rightsquigarrow Sel^a(U, m, U)$	(SA)
$([U]^b \leftarrow m)^a$	$\rightsquigarrow (U \leftarrow m)^a$	(SRed)
$Sel^a(\langle U \leftarrow m = V \rangle^b, m, W)$	$\rightsquigarrow (VW)^a$	(SU)
$Sel^a(\langle U \leftarrow n = V \rangle^b, m, W)$	$\rightsquigarrow Sel^a(U, m, W) \quad m \neq n$	(NE)
$Sel^a([U]^b, m, V)$	$\rightsquigarrow Sel^a(U, m, V)$	(SelRed)
<b>The Module F</b>		
$\langle M \leftarrow m = N \rangle [s]^a$	$\rightsquigarrow \langle M[s]^b \leftarrow m = N[s]^c \rangle^a$	(FP)
$\langle [U]^b \leftarrow m = V \rangle^a$	$\rightsquigarrow \langle U \leftarrow m = V \rangle^a$	(FRed)

Figure 6. The Modules L and C and F

stored. A *closure*  $M[s]^a$  is analogous to a closure in a  $\lambda$ -calculus, but it is given an address, and the terms in  $s$  are also addressed terms. The evaluation context  $[O]^a$  represents an object whose *internal* object-structure is  $O$  and whose object-identity is  $[ ]^a$ . In other words, the address  $a$  plays the role of an *entry point* of the object-structure  $O$ .  $(O \leftarrow m)^a$  is the evaluation context associated to a method-lookup *i.e.*, the scanning of the object-

structure to find the method  $m$ .  $\leftarrow$  is an auxiliary operator, reminiscent to the selection operator  $Sel$  of  $\lambda\mathcal{O}bj^+$ , invoked when one sends a message to an object. We recall that the term  $\bullet^a$  is a *back pointer*. From object point of view they allow to create “loops in the store”. Only  $\bullet^a$  can occur inside a term having the same address  $a$ , therefore generalizing our informal notion of admissible term and simultaneous rewriting.

*The Code Category.* Code terms, written  $M$  and  $N$ , provide the following constructs:

- Pure  $\lambda$ -terms, constructed from abstractions, applications, variables, and constants. This allows the definition of higher-order functions.
- Objects, constructed from the empty object  $\langle \rangle$  and a functional update operator  $\langle \_ \leftarrow \_ \rangle$ . In a functional setting, this operator can be understood as extension as well as override operator, since an override is handled as a particular case of extension.
- Method invocation  $(\_ \Leftarrow \_)$ .

*Evaluation Contexts.* These terms, written  $U$  and  $V$ , model *states of abstract machines*. Evaluation contexts contain an abstraction of the temporary structure needed to compute the result of an operation. They are given addresses as they denote dynamically instantiated data structures; they always denote a term closed under the distribution of an environment. There are the following evaluation contexts:

- *Closures*, of the form  $M[s]^a$ , are pairs of a code and an environment. Roughly speaking,  $s$  is a list of bindings for the free variables in the code  $M$ .
- The terms  $(UV)^a$ ,  $(U \Leftarrow m)^a$ , and  $\langle U \leftarrow m = V \rangle^a$ , are the evaluation contexts associated with the corresponding code constructors. Direct sub-terms of these evaluation contexts are themselves evaluation contexts instead of code.
- The term  $Sel^a(U, m, V)$  is the evaluation context associated to a method-lookup, *i.e.*, the scanning of the context  $U$  to find the method  $m$ , and apply it to the context  $V$ . It is an auxiliary operator invoked when one sends a message to an object.
- The term  $[U]^a$  denotes an indirection from the address  $a$  to the root of the addressed term  $U$ . The operator  $[\_ ]^a$  has no denotational meaning. It is introduced to make the right-hand side stay at the same address as the left-hand side. Indeed in some cases this has to be enforced. *e.g.* rule (FVAR). This gives account of phenomena well-known by implementers. Rules like (AppRed), (SRed) and (FRed) remove those indirections.

**Remark 3.1 (ATRS-based preterms of  $\lambda\mathcal{O}bj^a$ ).**

The concrete syntax of  $\lambda\mathcal{O}bj^a$  of Figure 5 can be viewed as syntactic sugar over the definition of ATRS preterm in two ways:

- 1 Symbols in the signature may also be infix (like *e.g.*,  $(\_ \Leftarrow \_)$ ), bracketing (like *e.g.*,  $[\_ ]$ ), mixfix (like  $\_[\_ ]$ ), or even “invisible” (as is traditional for application, represented by juxtaposition). In these cases, we have chosen to write the address outside brackets and parentheses. For example we write  $(UV)^a$  instead of  $\mathbf{apply}^a(X, Y)$  and  $M[s]^a$  instead of  $\mathbf{closure}^a(X, Y)$  (substituting  $U$  for  $X$ , etc.).
- 2 We shall use  $\lambda\mathcal{O}bj^a$  sort-specific variable names.

It is clear that not all preterms denote term-graphs, since this may lead to inconsistency in the sharing. For instance, the preterm  $((\langle \rangle [ ]^b \Leftarrow m)^a \langle \rangle [ ]^a)^c$  is inconsistent, because location  $a$  is both labeled by  $\langle \rangle [ ]$  and  $(\_ \Leftarrow \_)$ . The preterm  $(([\langle \rangle [ ]^a]^b \Leftarrow m)^c [\langle \rangle [ ]^e]^b)^d$  is inconsistent as well, because the node at location  $b$  has its successor at both locations  $a$  and  $e$ , which is impossible for a term-graph. On the contrary, the preterm  $(([\langle \rangle [ ]^a]^b \Leftarrow m)^c \langle \rangle [ ]^a)^d$  denotes a *legal* term-graph with four nodes, respectively, at addresses  $a$ ,  $b$ ,  $c$ , and  $d^\dagger$ . Moreover, the nodes at addresses  $a$  and  $b$ , corresponding respectively to  $\langle \rangle [ ]$  and  $\langle \rangle [ ]^a$ , are shared in the corresponding graph since they have several occurrences in the term. These are the distinction captured by the well-formedness constraints defined in section 2.1.

### 3.3. Operational Semantics of $\lambda Obj^a$

The rules of  $\lambda Obj^a$  as a computational-engine are defined in Figure 6. First of all observe that *there are no rules for code expressions*: in fact every code expression is evaluated starting from an empty substitution (you may think this empty substitution as the empty return continuation) and as such code belongs directly to the evaluation contexts category. In what follows we give a brief explanation of the operational semantic:

**Remark 3.2 (On fresh addresses).**

We assume that all addresses occurring in right-hand sides but not in left-hand sides are *fresh*. This is a sound assumption relying on the formal definition of fresh addresses and addressed term rewriting (see Section 2), which ensures that clashes of addresses cannot occur.

### 3.4. The intuition behind the rules

The set of thirteen rules is divided in three modules. Notice the specificity of ATRS's, namely that in each rule the left-hand side lies at the same address as this of the right-hand side. To avoid losing this property and prevent bad effects, an indirection  $[ ]^a$  is created when necessary. In  $L + C + F$  this is the case once, namely for (FVar). In some other rules, namely (AppRed), (SRed), (SelRed), and (FRed) the indirection is removed and the pointer is redirected. Notice that this can only be done inside a term, since a pointer has to be redirected and one has to know where this pointer comes from. In the name of these rules the suffix "Red" stands for redirection.

$\lambda Obj^a$  is based on extending a calculus of explicit substitutions and  $L$  is precisely that calculus which was introduced first by Benaissa et al. (Benaissa et al. 1996), itself derived from Bloo and Rose's  $\lambda x$  (Bloo & Rose 1995) and Curien's calculus of closures  $\lambda \rho$  (Curien 1991), by adding addresses. Like Rose and Bloo and unlike Curien, who works on de Bruijn's indices,  $L + C + F$  works on explicit names. As those calculi  $L$  has a rule

<sup>†</sup> Observe that computation with this term leads to a *method-not-found* error since the invoked method  $m$  does not belong to the object  $\langle \rangle [ ]^a$ , and hence will be rejected by a suitable sound type system or by a run-time exception.

(Bw) which looks for a  $\beta$ -redex and produces an extension of the environment. Rule (App) distributes the closure in an application. Note that (App) creates new nodes in the tree structure of the term, which is to say that it creates new addresses. But note that there is no duplication of  $s$ : this has its own set of addresses, replicated in the tree structure, but the repetition of these addresses merely indicates explicitly the sharing in memory. This comment is important since *this is the place where sharing actually takes place in object languages*. (Rvar) is when the code is a variable  $x$ . If that variable  $x$  mismatches the variable  $y$  of the first pair in the environment, the access is performed on the tail of the list. (FVar) returns the evaluation context associated with the variable  $x$  when the variable in the first pair in the environment is precisely  $x$ . The term on the left side lies at address  $a$ , but this is not the case for the evaluation context, so a redirection toward this evaluation context forces this evaluation context to be accessed through the address  $a$  and therefore the right-hand side lies also at address  $a$ . (AppRed) removes an indirection in a application. It does that only on the left part of the application, in order to avoid hiding a  $\lambda$ -abstraction and to enable a potential later application of (Bw). Since this indirection removal takes places inside a term, it can be easily implemented by a redirection.

Module C speaks about objects and method invocations. Like (App), (SP) is a rule that distributes an environment through a structure. Notice since  $m$  is just a label, the environment  $s$  has no effect on it. The left-hand side of (SA) is a method invocation. The intuitions behind *Sel* were given earlier. (SRed) deals with a redirection. Since, on the left-hand side, the method  $m$  is applied on an indirection, this indirection is removed in the left-hand side. This removal leads in an actual implementation to a pointer redirection. (SelRed) also deals with indirection. (SRed) can be considered as redundant with it, but this is left to the implementer's. (SU) and (NE) are for method selection. In (SU) the method is found and its body is applied to the actual object. In (NE) the method is not found and searched further.

Module F is about method extension. Notice that the semantics does not speak about method overriding, since when a method is added to an object which has already a method with the same label, only the last added method will be selected later one. Therefore this addition has the effect of a method overriding. Like (App) and (SP), (FP) distributes an environment through a structure. (FC) says that when one extends an evaluated object by a new method the result is an evaluated object. (FRed) is again an indirection removal leading to a redirection.

**Remark 3.3.**

Notice that *normal forms* are as follows.  $((\lambda x.M)[s])^a$  and  $(\langle M[s_1]^b \leftarrow m = N[s_2]^c \rangle)^a$  correspond to values computed by programs.  $x[ ]^a$  corresponds to an *illegal memory access* and  $Sel^a(\langle \rangle[s]^b, m, U)$  corresponds to *method not found*.

#### 4. ATRS at Work: an Example in $\lambda Obj^a$

Here we propose examples to help understanding the formalism. We first give an example showing a point object with a set method.

**Example 4.1 (A Point Object).** Let

$$\mathbf{point} \triangleq \langle \underbrace{\langle \langle \rangle \leftarrow \mathbf{n} = \lambda \mathbf{s}.0 \rangle}_P \leftarrow \underbrace{\mathbf{set1} = \lambda \mathbf{self}.\langle \mathbf{self} \leftarrow \mathbf{n} = \lambda \mathbf{s}.1 \rangle}_N \rangle.$$

The reduction of  $M \triangleq (\mathbf{point} \leftarrow \mathbf{set1})$  in  $\lambda\mathcal{O}bj^a$  starting from an empty substitution is as follows:

$$M[]^a \rightsquigarrow^* (\langle P[]^d \leftarrow \mathbf{set1} = N[]^c \rangle^b \leftarrow \mathbf{set1})^a \quad (1)$$

$$\rightsquigarrow Sel^a(P[]^d \leftarrow \mathbf{set1} = N[]^c, \mathbf{set1}, \mathbf{point}^b) \quad (2)$$

$$\rightsquigarrow ((\lambda \mathbf{self}.\langle \mathbf{self} \leftarrow \mathbf{n} = \lambda \mathbf{s}.1 \rangle)[]^c \mathbf{point}^b)^a \quad (3)$$

$$\rightsquigarrow \langle \mathbf{self} \leftarrow \mathbf{n} = \lambda \mathbf{s}.1 \rangle[\mathbf{point}^b/\mathbf{self}]^a \quad (4)$$

$$\rightsquigarrow \langle \mathbf{self}[\mathbf{point}^b/\mathbf{self}]^h \leftarrow \mathbf{n} = (\lambda \mathbf{s}.1)[\mathbf{point}^b/\mathbf{self}]^g \rangle^a \quad (5)$$

$$\rightsquigarrow \langle [\mathbf{point}^b]^h \leftarrow \mathbf{n} = (\lambda \mathbf{s}.1)[\mathbf{point}^b/\mathbf{self}]^g \rangle^a \quad (6)$$

$$\rightsquigarrow^2 \langle \mathbf{point}^b \leftarrow \mathbf{n} = (\lambda \mathbf{s}.1)[\mathbf{point}^b/\mathbf{self}]^g \rangle^a \quad (7)$$

In (1), two steps are performed to distribute the environment inside the extension, using rules (SP), and (FP). In (2), and (3), two steps (SA) (SU) perform the look up of method  $\mathbf{set1}$ . In (4) we apply (Bw). In (5), the environment is distributed inside the functional extension by (FP). In (6), (FVar) replaces  $\mathbf{self}$  by the object it refers to, setting an indirection from  $h$  to  $b$ . In (7) the indirection is eliminated by (FRed). There is no redex in the last term of the reduction, *i.e.* it is in normal form.

Sharing of structures appears in the above example, since *e.g.*  $\mathbf{point}^b$  turns out to have several occurrences in some of the terms of the derivation.

#### 4.1. Object Representations in Figures 7

The examples in this section embody certain choices about language design and implementation (such as “deep” *vs.* “shallow” copying, management of run-time storage, and so forth). It is important to stress that these choices are not tied to the formal calculus  $\lambda\mathcal{O}bj^a$  itself;  $\lambda\mathcal{O}bj^a$  provides a foundation for a wide variety of language paradigms and language implementations. We hope that the examples are suggestive enough that it will be intuitively clear how to accommodate other design choices. These schematic examples will be also useful to understand how objects are represented and how inheritance can be implemented in  $\lambda\mathcal{O}bj^a$ .

Reflecting implementation practice, in  $\lambda\mathcal{O}bj^a$  we distinguish two distinct aspects of an object:

- *The object structure:* the actual list of methods/fields.
- *The object identity:* a pointer to the object structure.

We shall use the word “pointer” where others use “handle” or “reference”. Objects can be bound to identifiers as “nicknames” (*e.g.*,  $\mathbf{pixel}$ ), but the only proper name of an object is its object identity: an object may have several nicknames but only one identity.

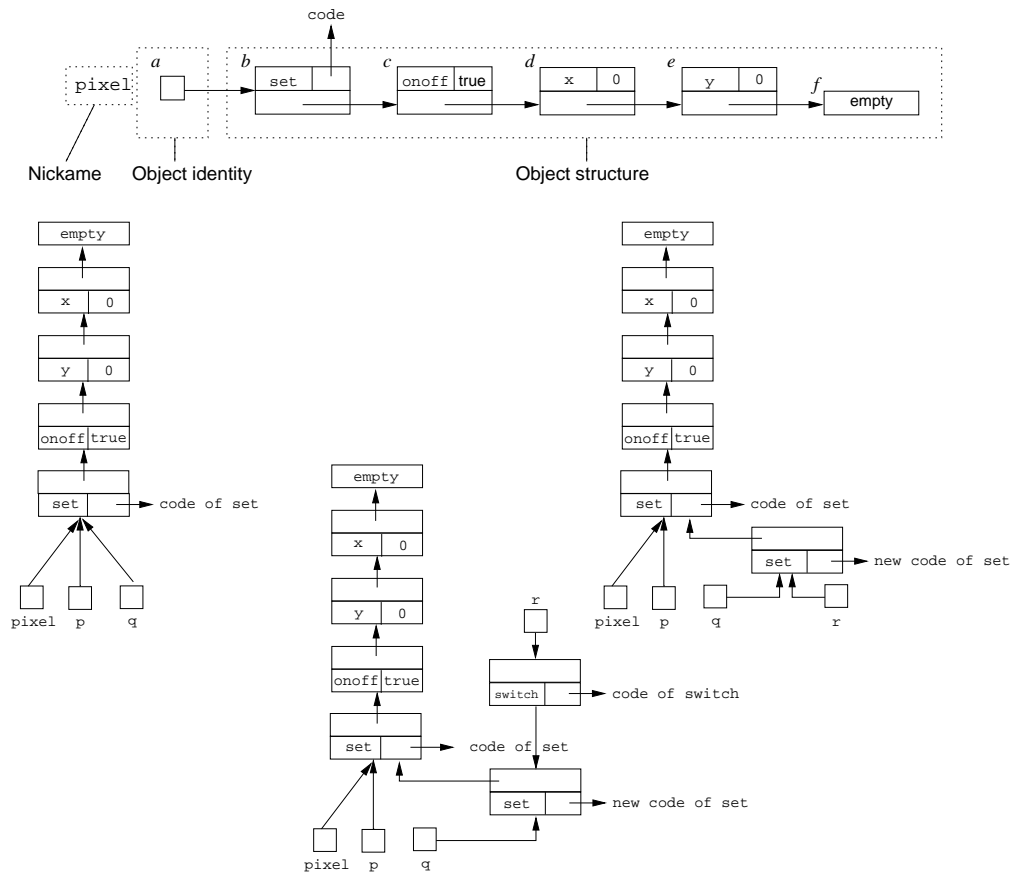


Figure 7. An Object Pixel (top), and the evolution of memory in Section 4.2

Consider the following definition of a “pixel” prototype with three fields and one method.

```

pixel = object {x      := 0;
                y      := 0;
                onoff  := true;
                set    := (u,v,w){x := u; y := v; onoff := w;};
                }
    
```

We make a slight abuse of notation, we use “:=” for both assignment of an expression to a variable or the extension of an object with a new field or method and for overriding an existing field or method inside an object with a new value or body, respectively.

After instantiation, the object `pixel` is located at an address, say `a`, and its object structure starts at address `b`, see Figure 7 (top). In what follows, we will derive three other objects from `pixel` and discuss the variations of how this may be done below.

## 4.2. Cloning: an example

The first two derived objects, nick-named `p` and `q`, are *clones* of `pixel` (Here `let x = A in B` is syntactic sugar for the functional application  $(\lambda x.B)A$ .)

```
let p = pixel in let q = p in q      // clone
```

Object `p` shares the same object-structure as `pixel` but it has its own object-identity. Object `q` shares also the same object-structure as `pixel`, even if it is a clone of `p`. The effect is pictured in Figure 7 (left). We might stress here that `p` and `q` should not be thought of as aliases of `pixel` as the Figure might suggest; this point will be clearer after the discussion of object overriding below. Then, we show what we want to model in our formalism when we *override* the `set` method of the clone `q` of `pixel`, and we extend a clone `r` of (the modified) `q` with a new method `switch`.

```
let p = pixel in
let q = p.set :=
  (u,v,w){(self.x := self.x*u).y := self.y*v}.onoff := w} in
let r = (q.switch := (){self.onoff := not(self.onoff);}) in r
```

which obviously reduces to: `(pixel.set := (u,v,w){..}).switch := (){..}`. Figure 7 (middle) shows the state of the memory after the execution of the first two `let` binding are executed (let us call these program points 1 and 2). Note that after 1 the object `q` refers to a new object-structure, obtained by chaining the new body for `set` with the old object-structure. As such, when the overridden `set` method is invoked, thanks to dynamic binding, the newer body will be executed since it will hide the older one. This dynamic binding is embodied in the treatment of the method-lookup rules (SU) and (NE) from Module C as described in Section 3.

Observe that the override of the `set` method does not produce any side-effect on `p` and `pixel`; in fact, the code for `set` used by `pixel` and `p` will be just as before. Therefore, executing `let q = p.set` only changes the object-structure of `q` without changing its object-identity. This is the sense in which our `clone` operator really does implement shallow copying rather than aliasing, even though there is no duplication of object-structure at the time that `clone` is evaluated.

This implementation model performs side effects in a very restricted and controlled way. The last diagram in Figure 7 shows the final state of memory after the execution of the binding of `r`. Again, the addition of the `switch` method changes only the object-structure of `r`.

In general, changing the nature of an object dynamically by adding a method or a field can be implemented by moving the object identity toward the new method/field (represented by a piece of code or a memory location) and to *chain it* to the original structure. This mechanism is used systematically also for method/field overriding but in practice (for optimization purposes) can be relaxed for field overriding, where a more efficient *field look up and replacement* technique can be adopted. See for example the case of the Object Calculus in Chapter 6-7 of (Abadi & Cardelli 1996), or observe that Java uses *static field lookup* to make the position of each field constant in the object.



### 4.3. Implementing

Representing object structures with the constructors  $\langle \rangle$  (the empty object), and  $\langle \_ \leftarrow \_ \rangle$  (the functional *cons* of an object with a method/field), and the object  $\mathbf{p}$  and  $\mathbf{q}$ , presented in Figure 7, will be represented by the following addressed terms.

$$\begin{aligned} \mathbf{p} &\triangleq \llbracket \langle \langle \langle \langle \langle \rangle \rangle^f \leftarrow \mathbf{y} = 0 \rangle^e \leftarrow \mathbf{x} = 0 \rangle^d \leftarrow \text{onoff} = \text{true} \rangle^c \leftarrow \text{set} = \dots \rangle^b \rrbracket^a \\ \mathbf{q} &\triangleq \llbracket \llbracket \langle \langle \langle \langle \langle \rangle \rangle^f \leftarrow \mathbf{y} = 0 \rangle^e \leftarrow \mathbf{x} = 0 \rangle^d \leftarrow \text{onoff} = \text{true} \rangle^c \leftarrow \text{set} = \dots \rrbracket^b \\ &\quad \leftarrow \text{set} = \dots \rrbracket^g \rrbracket^h \end{aligned}$$

The use of the same addresses  $b, c, d, e, f$  in  $\mathbf{p}$  as in  $\mathbf{q}$  denotes the sharing between both object structures while  $g, h$ , are unshared and new locations.

## 5. Relation between $\lambda\mathcal{O}b_j^\sigma$ and $\lambda\mathcal{O}b_j^a$

In this section we just list some fundamental results about the relationship between  $\lambda\mathcal{O}b_j^\sigma$  and  $\lambda\mathcal{O}b_j^a$ . As a first step we note that the results presented in Section A are applicable to  $\lambda\mathcal{O}b_j^\sigma$ .

### Lemma 5.1 (Mapping $\lambda\mathcal{O}b_j^a$ to $\lambda\mathcal{O}b_j^\sigma$ ).

Let  $\phi$  be the mapping from  $\lambda\mathcal{O}b_j^a$ -terms that erases addresses, indirection nodes ( $\llbracket \_ \rrbracket^a$ ), and leaves all the other symbols unchanged. Each term  $\phi(U)$  is a term of  $\lambda\mathcal{O}b_j^\sigma$ .

*Proof.*  $\lambda\mathcal{O}b_j^a$ -terms are acyclic and the calculus is mutation-free.  $\square$

Then we show a simulation result.

### Theorem 5.1 ( $\lambda\mathcal{O}b_j^\sigma$ Simulates $\lambda\mathcal{O}b_j^a$ ).

Let  $U$  be an acyclic  $\lambda\mathcal{O}b_j^a$ -term. If  $U \rightsquigarrow V$  in  $\mathbf{L} + \mathbf{C} + \mathbf{F}$ , then either  $\phi(U) \equiv \phi(V)$  or  $\phi(U) \rightsquigarrow \phi(V)$  in  $\lambda\mathcal{O}b_j^\sigma$ .

*Proof.* The proof relies on Theorem A.1. Just notice that each rule  $l \rightsquigarrow r$  of modules  $\mathbf{L} + \mathbf{C} + \mathbf{F}$  is acyclic and mutation-free, and that either it maps to a rule  $\phi(l) \rightsquigarrow \phi(r)$  which belongs to  $\lambda\mathcal{O}b_j^\sigma$ , or it is such that  $\phi(l) \equiv \phi(r)$ .  $\square$

Another issue, tackled by the following theorem, is to prove that all normal forms of  $\lambda\mathcal{O}b_j^\sigma$  can also be obtained in  $\mathbf{L} + \mathbf{C} + \mathbf{F}$  of  $\lambda\mathcal{O}b_j^a$ .

### Theorem 5.2 (Completeness of $\lambda\mathcal{O}b_j^a$ w.r.t. $\lambda\mathcal{O}b_j^\sigma$ ).

If  $M \rightsquigarrow^* N$  in  $\lambda\mathcal{O}b_j^\sigma$ , such that  $N$  is a normal form, then there is some  $U$  such that  $\phi(U) \equiv N$  and  $M \llbracket \_ \rrbracket^a \rightsquigarrow^* U$  in  $\mathbf{L} + \mathbf{C} + \mathbf{F}$  of  $\lambda\mathcal{O}b_j^a$ .

*Proof.* The result follows from the facts that

- 1 Each rule of  $\lambda\mathcal{O}b_j^\sigma$  is mapped by a rule of  $\mathbf{L} + \mathbf{C} + \mathbf{F}$ .
- 2 Rules of  $\lambda\mathcal{O}b_j^a$  are left linear in addresses, hence whenever a  $\lambda\mathcal{O}b_j^\sigma$ -term matches the left-hand side of a rule, whatever the addresses of a similar addressed term are, it matches the left-hand side of a rule in  $\mathbf{L} + \mathbf{C} + \mathbf{F}$  of  $\lambda\mathcal{O}b_j^a$ .

- 3 Whenever an acyclic  $\lambda\mathcal{O}bj^a$ -term  $U$  contains an indirection node  $[\_ ]^a$ , this node may be eliminated using one of rules (AppRed), (SRed), or (FRed) (hence, indirection nodes cannot permanently *hide* some redexes). □

The last issue is to show that  $L + C + F$  of  $\lambda\mathcal{O}bj^a$  does not introduce non-termination *w.r.t.*  $\lambda\mathcal{O}bj^\sigma$ .

**Theorem 5.3 (Preservation of Strong Normalization).**

If  $M$  is a strongly normalizing  $\lambda\mathcal{O}bj^\sigma$ -term, then all  $\lambda\mathcal{O}bj^a$ -term  $U$  such that  $\phi(U) \equiv M$  is also strongly normalizing.

*Proof.* By Theorem 5.1 it suffices to show that the set of  $\lambda\mathcal{O}bj^a$  rules  $l \rightsquigarrow r$  for which  $\phi(l) \equiv \phi(r)$  comprises a terminating system. This is the set of rules {(AppRed), (SRed), (SelRed), (FRed)}. But termination of this set is clear since each of these rules decreases the size of a term. □

It follows from the theorems of this section that modulo making addresses explicit, terms compute to the same normal forms in  $\lambda\mathcal{O}bj^a$  and  $\lambda\mathcal{O}bj^\sigma$ .

## 6. A Type System for $\lambda\mathcal{O}bj^a$

In this section, we present a simple type system for  $\lambda\mathcal{O}bj^a$ , inspired by the systems of Fisher and Mitchell in (Fisher & Mitchell 1995) and Bono and Bugliesi in (Bono & Bugliesi 1999). This type system feature subtyping, which allow method bodies to operate uniformly over all objects having some minimum set of required methods.

### 6.1. Types

Types are built from type constants, type variables, a function space constructor, and two type abstractions called respectively **obj** and **pro**. Moreover, one introduces row of types designed for typing objects with methods.

The type expressions are described as follows. For simplicity we work with a single base type constant  $\iota$ .

$$\begin{aligned} \sigma, \tau & ::= \iota \mid t \mid \sigma \rightarrow \tau \mid \mathbf{pro} \ t.R \mid \mathbf{obj} \ t.R \\ R, R' & ::= \langle\langle \rangle\rangle \mid \langle\langle R, \mathbf{m}:\sigma \rangle\rangle \end{aligned}$$

Here  $\sigma$  and  $\tau$  are meta-variables ranging over types.  $R$ , and  $R'$  are meta-variables ranging over rows.

Here are intuitions behind these concepts.

- $\iota$  is a constant, base, type.
- A row  $R$  is an *unordered* set of pairs (*method label, method type*).
- $t$  are type-variables. Type variables play a role in type abstractions (**obj** or **pro**) which behave as a kind of fixed point. The type variable  $t$  when it occurs in an abstraction

$\text{obj } t.R(t)$  (or in an abstraction  $\text{pro } t.R(t)$ ) is equivalent to **this** in a language like JAVA.

- $\text{pro } t.R$  is the type of prototypical objects that are modifiable via object override and object extension. On those objects, subtyping is forbidden, since subtyping is unsound when one allows objects to be extended (Abadi & Cardelli 1996, Liquori 1997).
- $\text{obj } t.R$  is the type of objects *of fixed size and behavior*; objects having this type can only receive messages. They can be, possibly, subsumed via subtyping; that is all.

## 6.2. Contexts and Judgments

Contexts are as follows

$$\Gamma ::= \varepsilon \mid \Gamma, x:\sigma \mid \Gamma, t \triangleleft\# \sigma \mid \Gamma, a:\sigma$$

(treated as sets). Judgments are as follows

$$\begin{aligned} (1) \quad & \Gamma \vdash \text{ok}, \quad \Gamma \vdash \sigma : \text{ok}, \quad \Gamma \vdash \sigma \triangleleft\# \tau, \quad \Gamma \vdash \sigma <: \tau \\ (2) \quad & \Gamma \vdash M : \sigma, \quad \Gamma \vdash U : \sigma \quad \Gamma \vdash a : \sigma \end{aligned}$$

- $\Gamma \vdash \text{ok}$  means that  $\Gamma$  is well-formed;
- $\Gamma \vdash \sigma : \text{ok}$  means that in the context  $\Gamma$ ,  $\sigma$  is well-formed;
- $\Gamma \vdash \sigma \triangleleft\# \tau$  is the matching relation introduced by Bruce et al in (Bruce, et al. 1997) and further studied by Abadi and Cardelli in (Abadi & Cardelli 1995, Abadi & Cardelli 1996). It means that the type  $\sigma$  (a **pro**-type) has *more methods* than the type  $\tau$  (another **pro**-type). Roughly speaking  $\text{pro } t.\langle\bar{m}:\bar{\sigma}, \bar{n}:\bar{\tau}\rangle \triangleleft\# \text{pro } t.\langle\bar{m}:\bar{\sigma}\rangle$ . Notice that in  $\sigma \triangleleft\# \tau$ ,  $\sigma$  cannot be an **obj**-type;
- $\Gamma \vdash \sigma <: \tau$  is subtyping. In conjunction with subsumption it allows using an object with a larger interface (*i.e.* with more methods) in any context expecting another object with a smaller interface (*i.e.* with less methods). This leads to an implicit form of polymorphism. In other words,  $\sigma$  can be the type of an update of an object of type  $\tau$ . This judgment formalizes the ability to “inherit” method types;
- $\Gamma \vdash U : \sigma$  means that an evaluation context  $U$  has type  $\sigma$  in the context  $\Gamma$ ;
- $\Gamma \vdash M : \sigma$  means that expression  $M$  has type  $\sigma$  in the context  $\Gamma$ ;
- $\Gamma \vdash a : \sigma$  means that address  $a$  has type  $\sigma$  in the context  $\Gamma$ .

In defining the type rules it will be convenient to be able to refer to the set of method labels in a row.

*Notation.* If  $R$  is the row  $\{\mathfrak{m}_1:\sigma_1, \dots, \mathfrak{m}_k:\sigma_k\}$  then  $\mathcal{M}(R)$  is the set of labels  $\{\mathfrak{m}_1, \dots, \mathfrak{m}_k\}$ .

6.2.1. *Bureaucracy, Matching and Subtyping* Rules in Figure 8 assert the well-formedness of contexts and types, and serve to define the subtype and matching relations on types. A few notes are in order:

- In rule (Type–Pro) the assumption  $t \triangleleft\# \text{pro } t.\langle\bar{m}\rangle$  makes  $t$  a **pro**-type a priori. (Type–Obj) says that if type  $\text{pro } t.R$  is well-formed, its brother  $\text{obj } t.R$  is also well-formed. The structure of **obj**-type really derives from this of **pro**-type.

<b>Well-formed contexts</b>	
$\frac{}{\varepsilon \vdash ok} \text{ (Cont-}\varepsilon\text{)}$	$\frac{\Gamma \vdash \sigma : ok \quad x \notin dom(\Gamma)}{\Gamma, x:\sigma \vdash ok} \text{ (Cont-x)}$
$\frac{\Gamma \vdash \sigma : ok \quad a \notin dom(\Gamma)}{\Gamma, a:\sigma \vdash ok} \text{ (Cont-a)}$	$\frac{\Gamma \vdash \sigma : ok \quad t \notin dom(\Gamma)}{\Gamma, t \triangleleft\# \sigma \vdash ok} \text{ (Cont-t)}$
<b>Well-formed types</b>	
$\frac{\Gamma \vdash ok}{\Gamma \vdash \iota : ok} \text{ (Type-Const)}$	$\frac{\Gamma, t \triangleleft\# \sigma, \Delta \vdash ok}{\Gamma, t \triangleleft\# \sigma, \Delta \vdash t : ok} \text{ (Type-Var)}$
$\frac{\Gamma \vdash \sigma : ok \quad \Gamma \vdash \tau : ok}{\Gamma \vdash \sigma \rightarrow \tau : ok} \text{ (Type-Arrow)}$	$\frac{\Gamma \vdash ok}{\Gamma \vdash \mathbf{pro}t.\langle\langle \rangle\rangle : ok} \text{ (Type-Pro-Empty)}$
$\frac{\Gamma \vdash \mathbf{pro}t.R : ok \quad m \notin \mathcal{M}(R) \quad \Gamma, t \triangleleft\# \mathbf{pro}t.\langle\langle \rangle\rangle \vdash \sigma : ok}{\Gamma \vdash \mathbf{pro}t.\langle\langle R, m:\sigma \rangle\rangle : ok} \text{ (Type-Pro)}$	$\frac{\Gamma \vdash \mathbf{pro}t.R : ok}{\Gamma \vdash \mathbf{obj}t.R : ok} \text{ (Type-Obj)}$
<b>Matching rules</b>	
$\frac{\Gamma, t \triangleleft\# \sigma, \Delta \vdash ok}{\Gamma, t \triangleleft\# \sigma, \Delta \vdash t \triangleleft\# \sigma} \text{ (Match-Var)}$	$\frac{\Gamma \vdash \sigma \triangleleft\# \tau \quad \Gamma \vdash \tau \triangleleft\# \rho}{\Gamma \vdash \sigma \triangleleft\# \rho} \text{ (Match-Trans)}$
$\frac{\Gamma \vdash \sigma : ok \quad \sigma \text{ is a } \mathbf{pro}t. \text{ or a variable}}{\Gamma \vdash \sigma \triangleleft\# \sigma} \text{ (Match-Refl)}$	$\frac{\Gamma \vdash \mathbf{pro}t.\langle\langle R, m:\sigma \rangle\rangle : ok}{\Gamma \vdash \mathbf{pro}t.\langle\langle R, m:\sigma \rangle\rangle \triangleleft\# \mathbf{pro}t.R} \text{ (Match-Project)}$
<b>(Width) Subtyping rules</b>	
$\frac{\Gamma \vdash \sigma <: \tau \quad \Gamma \vdash \tau <: \rho}{\Gamma \vdash \sigma <: \rho} \text{ (Sub-Trans)}$	$\frac{\Gamma \vdash \sigma : ok}{\Gamma \vdash \sigma <: \sigma} \text{ (Sub-Refl)}$
$\frac{\Gamma \vdash \sigma' <: \sigma \quad \Gamma \vdash \tau <: \tau'}{\Gamma \vdash \sigma \rightarrow \tau <: \sigma' \rightarrow \tau'} \text{ (Sub-Arrow)}$	$\frac{\Gamma \vdash \mathbf{obj}t.\langle\langle R, m:\sigma \rangle\rangle : ok}{\Gamma \vdash \mathbf{obj}t.\langle\langle R, m:\sigma \rangle\rangle <: \mathbf{obj}t.R} \text{ (Sub-Object)}$
$\frac{\Gamma \vdash \mathbf{pro}t.R : ok \quad t \text{ covariant in } R}{\Gamma \vdash \mathbf{pro}t.R <: \mathbf{obj}t.R} \text{ (Sub-Seal)}$	

Figure 8. Type rules, part I

- The *matching* rules define the matching relation as defined in (Bruce et al. 1997). (Match-Var) says that only variable types and **pro**-types can be related by a matching  $\triangleleft\#$ , while (Match-Project) says that any extension of **pro** $t.R$  matches **pro** $t.R$ .
- The *subtyping* rules are those expected in an object oriented calculus. An object with more methods can safely be subsumed (used) in any context expecting an object with fewer methods (Sub-Object).
- Rule (Sub-Seal) says that a **pro**-type can subsume an **obj**-type with the same interface. As explained in (Fisher & Mitchell 1995) this means that after the object has

<b>Code</b>	
$\frac{\Gamma \vdash ok}{\Gamma \vdash c : \iota}$ (Const)	$\frac{\Gamma, x:\sigma \vdash ok}{\Gamma, x:\sigma \vdash x : \sigma}$ (Var)
$\frac{\Gamma, x:\sigma \vdash M : \tau}{\Gamma \vdash \lambda x.M : \sigma \rightarrow \tau}$ (Abs)	$\frac{\Gamma \vdash M : \sigma \rightarrow \tau \quad \Gamma \vdash N : \tau}{\Gamma \vdash MN : \tau}$ (Appl)
$\frac{\Gamma \vdash M : \sigma \quad \Gamma \vdash \sigma <: \tau}{\Gamma \vdash M : \tau}$ (Sub)	$\frac{\Gamma \vdash ok}{\Gamma \vdash \langle \rangle : \langle \rangle}$ (Empty)
$\frac{\Gamma \vdash M : \tau \quad \Gamma \vdash \tau < \# \text{prot.}\langle\langle R, m:\sigma \rangle\rangle}{\Gamma \vdash M \leftarrow m : \sigma[\tau/t]}$ (Call-Pro)	$\frac{\Gamma \vdash M : \tau \quad \Gamma \vdash \tau <: \text{obj } t.\langle\langle R, m:\sigma \rangle\rangle}{\Gamma \vdash M \leftarrow m : \sigma[\tau/t]}$ (Call-Obj)
$\frac{\Gamma \vdash M : \tau \quad \Gamma \vdash \tau < \# \text{prot.}\langle\langle R, m:\sigma \rangle\rangle \quad \Gamma, t < \# \tau \vdash N : t \rightarrow \sigma}{\Gamma \vdash \langle M \leftarrow m = N \rangle : \tau}$ (Over)	
$\frac{\Gamma \vdash M : \text{prot.}R \quad \Gamma, t < \# \text{prot.}\langle\langle R, m:\sigma \rangle\rangle \vdash N : t \rightarrow \sigma \quad m \notin \mathcal{M}(R)}{\Gamma \vdash \langle M \leftarrow m = N \rangle : \text{prot.}\langle\langle R, m:\sigma \rangle\rangle}$ (Ext)	

Figure 9. Type rules, part II

<b>Evaluation contexts</b>	
$\frac{\Gamma, a:\sigma \vdash ok}{\Gamma, a:\sigma \vdash a : \sigma}$ (Ev-Var)	$\frac{\Gamma \vdash M : \sigma \quad \Gamma \vdash a : \sigma}{\Gamma \vdash M[\text{id}]^a : \sigma}$ (Ev-Clos <sub>1</sub> )
$\frac{\Gamma \vdash a : \tau \quad \Gamma \vdash U : \sigma \rightarrow \tau \quad \Gamma \vdash V : \sigma}{\Gamma \vdash (UV)^a : \tau}$ (Ev-App1)	$\frac{\Gamma \vdash U : \tau \quad \Gamma, x:\tau \vdash M[s]^a : \sigma \quad \Gamma \vdash a : \sigma}{\Gamma \vdash M[U/x; s]^a : \sigma}$ (Ev-Clos <sub>2</sub> )
$\frac{\Gamma \vdash U : \sigma \rightarrow \tau \quad \Gamma \vdash V : \sigma}{\Gamma \vdash (UV)^a : \tau}$ (Ev-App1)	$\frac{\Gamma \vdash a : \sigma[\tau/t] \quad \Gamma \vdash U : \tau \quad \Gamma \vdash \tau < \# \text{prot.}\langle\langle R, m:\sigma \rangle\rangle}{\Gamma \vdash (U \leftarrow m)^a : \sigma[\tau/t]}$ (Ev-Send)
$\frac{\Gamma \vdash U : \tau \quad \Gamma \vdash \tau < \# \text{prot.}\langle\langle R, m:\sigma \rangle\rangle \quad \Gamma, t < \# \tau \vdash V : t \rightarrow \sigma \quad \Gamma \vdash a : \tau}{\Gamma \vdash \langle U \leftarrow m = V \rangle^a : \tau}$ (Ev-Over)	
$\frac{\Gamma \vdash U : \text{prot.}R \quad \Gamma, t < \# \text{prot.}\langle\langle R, m:\sigma \rangle\rangle \vdash V : t \rightarrow \sigma \quad \Gamma \vdash a : \text{prot.}\langle\langle R, m:\sigma \rangle\rangle \quad m \notin \mathcal{M}(R)}{\Gamma \vdash \langle U \leftarrow m = V \rangle^a : \text{prot.}\langle\langle R, m:\sigma \rangle\rangle}$ (Ev-Ext)	
$\frac{\Gamma \vdash U : \tau \quad \Gamma \vdash \tau < \# \text{prot.}\langle\langle R, m:\sigma \rangle\rangle \quad \Gamma \vdash V : \rho \quad \Gamma \vdash \rho < \# \tau \quad \Gamma \vdash a : \sigma[\rho/t]}{\Gamma \vdash \text{Sel}^a(U, m, V) : \sigma[\rho/t]}$ (Ev-Lookup)	
$\frac{\Gamma \vdash U : \sigma \quad \Gamma \vdash a : \sigma}{\Gamma \vdash [U]^a : \sigma}$ (Ev-Id)	$\frac{\Gamma \vdash a : \text{prot.}\langle\langle \rangle \rangle}{\Gamma \vdash \langle \rangle^a : \text{prot.}\langle\langle \rangle \rangle}$ (Ev-Empty)

Figure 10. Type rules, part III

been “sealed” it can no longer be modified, *i.e.*, a sealed object cannot be overridden or extended. Moreover sealing is irreversible. Roughly speaking, this corresponds to “freezing” the object structure and makes it a message receiver only. This agrees with the class-based philosophy that stipulates that when an object has been created, it belongs to its class and it can only send and receive messages or be passed as a parameter. In class-based languages, once created, the structure of an object can no longer be modified.

**6.2.2. Type Rules for Code (Object Expressions)** These are the rules in Figure 9.

- rules for constants, variables, abstraction, application and subsumption are as usual in the  $\lambda$ -calculus with subtyping;
- rule (Empty) give a type to an empty object;
- rule (Call-Pro), (Call-Obj) give a type to a method call; in both cases we require the receiver be typed with a `pro t.`-type (resp. `obj t.`-type) that contains method `m`;
- rule (Over) deals with object override: we require the object to be overridden  $M$  be typed with a `pro t.`-type containing method `m` and that the new body  $N$  be typed in a context  $\Gamma$  enriched with a match-binding for the variable denoting the type of the object itself (*i.e.* `this`);
- rule (Ext) deals with object extension: we require the object to be extended  $M$  be typed with a `pro t.`-type that does not contains the new method `m` and that the new body  $N$  be typed in a context  $\Gamma$  enriched with a match-binding for the variable denoting the type of the object itself (*i.e.* `self`).

**6.2.3. Type Rules for Evaluation Contexts** These are the rules in Figure 10.

- The typing of addresses is monomorphic, *i.e.* an address attached to a some evaluation context (or even object structure) of type  $\tau$  must have type  $\tau$ , *i.e.* we forbid subtyping on addresses; this practice is quite common in functional languages with references, where store is considered to be monomorphic.
- The rules (Ev-Closure<sub>1,2</sub>) rules are inherited from calculi involving explicit substitutions.
- All the other rules present no surprises; the typing of the address enrich every premises in every rule.

It is worth to notice that the main point in the evaluation rules is how to evaluate address. In our formalism the rationale is as follows: if an address  $a$  explicitly surrounding an expression, then it has the same type as the expression itself. This, in principle can be generalized to every ATRS-based formalism, not only the object-based ones.

**Example 6.1 (Subtyping for Code Reuse, (Fisher & Mitchell 1995)).**

Consider the function

$$\text{avg} \triangleq \lambda p_1. \lambda p_2. ((p_1 \leftarrow x) + (p_2 \leftarrow x)) / 2$$

of type `obj t.⟨x:int⟩`  $\rightarrow$  `obj t.⟨x:int⟩`  $\rightarrow$  `int`. Now consider two classical objects like:

$$\begin{aligned} \text{point} &\triangleq \langle x = \lambda \text{self}.3, \text{move} = \lambda \text{self}. \lambda dx. \langle \text{self} \leftarrow x = \lambda s. (\text{self} \leftarrow x) + dx \rangle \rangle \\ \text{cpoint} &\triangleq \langle \text{point} \leftarrow \text{col} = \lambda \text{self}. \text{red} \rangle \end{aligned}$$

As in (Fisher & Mitchell 1995) we can assign to those object the types

$$\begin{aligned} \vdash \text{point} & : \text{prot}.\langle\langle x:\text{int}, \text{move}:\text{int} \rightarrow t \rangle\rangle \\ \vdash \text{cpoint} & : \text{prot}.\langle\langle x:\text{int}, \text{move}:\text{int} \rightarrow t, \text{col}:\text{colors} \rangle\rangle \end{aligned}$$

but also, via subtyping, the type  $\text{obj } t.\langle\langle x:\text{int} \rangle\rangle$ . As such we can type

$$\vdash \text{avg point cpoint} : \text{int}$$

### 6.3. Subject Reduction and Type Soundness

As an example of reasoning about implementations in the context of addressed term rewriting, we prove the subject reduction property and type soundness for typed  $\lambda\text{Obj}^a$ . In the following, we denote by  $\Gamma \vdash A$  any derivable judgment in our system.

As usual in explicit substitutions, we may assume, without loss of generality, that for any substitution of the form

$$[U_1/x_1 ; U_2/x_2 ; \dots U_n/x_n],$$

for each  $1 \leq i \leq n$ , and  $i \leq j \leq n$ , the variable  $x_i$  is not free in  $U_j$ .

#### Lemma 6.1 (Auxiliary).

- 1 If  $\Gamma \vdash \sigma \ll \tau$  or  $\Gamma \vdash \sigma < \tau$ , then  $\Gamma \vdash \sigma : \text{ok}$  and  $\Gamma \vdash \tau : \text{ok}$ .
- 2 If  $\Gamma \vdash M : \tau$  or  $\Gamma \vdash U : \tau$ , then  $\Gamma \vdash \tau : \text{ok}$ .
- 3 If  $\Gamma \vdash A$ , then  $\text{Var}(A) \subseteq \text{Dom}(\Gamma)$ .
- 4 If  $\Gamma \vdash A$ , and  $\Gamma, \Delta \vdash \text{ok}$ , then  $\Gamma, \Delta \vdash A$ , i.e. the rule  $\frac{\Gamma \vdash A \quad \Gamma, \Delta \vdash \text{ok}}{\Gamma, \Delta \vdash A}$  is admissible.
- 5 If  $\Gamma, x:\tau, \Delta \vdash A$ , and  $\Gamma \vdash \sigma < \tau$ , then  $\Gamma, x:\sigma, \Delta \vdash A$ .

*Proof.* By routine induction on the structure of derivations. Point (4) use point (3).  $\square$

#### Lemma 6.2 (Generation).

- 1  $\Gamma \vdash \lambda x.M : \tau \rightarrow \sigma$  if and only if  $\Gamma, x:\tau \vdash M : \sigma$ .
- 2  $\Gamma \vdash MN : \sigma$  if and only if there exists  $\tau$  such that  $\Gamma, x:\tau \vdash M : \tau \rightarrow \sigma$ , and  $\Gamma \vdash N : \tau$ .
- 3  $\Gamma \vdash (UV)^a : \sigma$ , if and only if there exists  $\tau$  such that  $\Gamma \vdash U : \tau \rightarrow \sigma$  and  $\Gamma \vdash V : \tau$ , and  $\Gamma \vdash a : \sigma$ .
- 4  $\Gamma \vdash M[U_1/x_1 ; U_2/x_2 ; \dots ; U_n/x_n]^a : \sigma$  if and only if there exist  $\tau_1, \dots, \tau_n$  such that for  $1 \leq i \leq n-1$ , we have  $\Gamma, x_1:\tau_1, \dots, x_i:\tau_i \vdash U_{i+1} : \tau_{i+1}$ , and  $\Gamma, x_1:\tau_1, \dots, x_n:\tau_n \vdash M : \sigma$ , and  $\Gamma \vdash a : \sigma$ .
- 5  $\Gamma \vdash \langle U \leftarrow m = V \rangle^a : \tau$  if and only if one of those cases are satisfied:
  - (a) there exists  $R, \sigma$  such that  $\Gamma \vdash U : \tau$  and  $\Gamma \vdash \tau \ll \text{prot}.\langle\langle R, m:\sigma \rangle\rangle$  and  $\Gamma, t \ll \tau \vdash V : t \rightarrow \sigma$ , and  $\Gamma \vdash a : \tau$ .
  - (b) there exists  $R, \sigma$  such that  $\Gamma \vdash U : \text{prot}.R$  and  $\Gamma, t \ll \text{prot}.\langle\langle R, m:\sigma \rangle\rangle \vdash V : t \rightarrow \sigma$ , and  $\Gamma \vdash a : \text{prot}.\langle\langle R, m:\sigma \rangle\rangle$  and  $m \notin \mathcal{M}(R)$ .

*Proof.* In each case the ‘‘if’’ direction is immediate from the shape of the typing rules. For the ‘‘only if’’ direction, the only complication is the presence of the non syntax directed rule (Sub); but a routine induction on the structure of the derivation suffices.  $\square$

**Lemma 6.3 (Substitution).**

- 1 If  $\Gamma, x:\sigma, \Delta \vdash M[s]^a : \tau$ , and  $\Gamma \vdash N : \sigma$ , then  $\Gamma, \Delta \vdash M[N/x ; s]^a : \tau$ .
- 2 If  $\Gamma, t \ll \# \sigma, \Delta \vdash A$ , and  $\Gamma \vdash \tau \ll \# \sigma$ , then  $\Gamma, \Delta[\tau/t] \vdash A[\tau/t]$ .

*Proof.* Both parts can be proved by induction on the structure of the derivations.  $\square$

We are now ready to prove the Subject Reduction property. Let  $\text{Addr}(U)$  denote the set of addresses in  $U$ .

**Theorem 6.1 (Subject Reduction).**

If  $\Gamma \vdash U : \sigma$ , and  $U \rightsquigarrow U'$ , then for some  $\Delta$  with  $\text{Dom}(\Delta) = \text{Addr}(U') \setminus \text{Addr}(U)$ ,  $\Gamma, \Delta \vdash U' : \sigma$ .

*Proof.* We organize the proof by cases over the reduction rule applied.

**Case:**

$$(MN)[s]^a \rightsquigarrow (M[s]^b N[s]^c)^a \quad (\text{App})$$

Let the explicit substitution  $[s]$  be  $[U_1/x_1 ; U_2/x_2 ; \dots U_n/x_n]$ . Given that  $\Gamma \vdash (MN)[s]^a : \sigma$ , by inspecting the rules we see that it must be that  $\Gamma \vdash a : \sigma$ . By Lemma 6.2(4), there exists  $\tau_1, \dots, \tau_n$  such that for  $1 \leq i \leq n-1$ , we have that

$$\Gamma, x_1:\tau_1, \dots, x_i:\tau_i \vdash U_{i+1} : \tau_{i+1} \quad \text{and} \quad \Gamma, x_1:\tau_1, \dots, x_n:\tau_n \vdash MN : \sigma$$

By Lemma 6.2(2) there exists a type  $\tau$  such that

$$\Gamma, x_1:\tau_1, \dots, x_n:\tau_n \vdash M : \tau \rightarrow \sigma \quad \text{and} \quad \Gamma, x_1:\tau_1, \dots, x_n:\tau_n \vdash N : \tau$$

Apply rules (Ev–Clos<sub>1,2</sub>) to get

$$\Gamma, b:\tau \rightarrow \sigma \vdash M[s]^b : \tau \rightarrow \sigma \quad \text{and} \quad \Gamma, c:\tau \vdash N[s]^c : \tau$$

and so, using weakening on contexts, apply rule (Ev–Appl) to get

$$\Gamma, b:\tau \rightarrow \sigma, c:\tau \vdash (M[s]^b N[s]^c)^a : \sigma$$

as desired.

**Case:**

$$((\lambda x.M)[s]^b U)^a \rightsquigarrow M[U/x ; s]^a \quad (\text{Bw})$$

We must have that  $\Gamma \vdash a : \sigma$ , and by Lemma 6.2(3) there exists  $\tau$  such that

$$\Gamma \vdash (\lambda x.M)[s]^b : \tau \rightarrow \sigma \quad \text{and} \quad \Gamma \vdash U : \tau.$$

Writing  $[s]$  as  $[U_1/x_1 ; \dots ; U_n/x_n]$ , we have by Lemma 6.2(4) that there exists types  $\tau_1, \dots, \tau_n$  such that for  $1 \leq i \leq n-1$ , we have

$$\Gamma, x_1:\tau_1, \dots, x_i:\tau_i \vdash U_{i+1} : \tau_{i+1} \quad \text{and} \quad \Gamma, x_1:\tau_1, \dots, x_n:\tau_n \vdash \lambda x.M : \tau \rightarrow \sigma.$$

By Lemma 6.2(1) we get

$$\Gamma, x_1:\tau_1, \dots, x_n:\tau_n, x:\tau \vdash M : \sigma.$$



Since

$$\Gamma, x_1:\tau_1, \dots, x_n:\tau_n \vdash U : \tau$$

we may apply rules (Ev–Clos<sub>1,2</sub>) to get

$$\Gamma \vdash M[U/x ; s]^a : \sigma$$

as desired.

**Case:**

$$x[U/x ; s]^a \rightsquigarrow [U]^a \quad (\text{FVar})$$

Let  $[s]$  be  $[U_1/x_1 ; \dots ; U_n/x_n]$ . We must have that  $\Gamma \vdash a : \sigma$ . By Lemma 6.2(4), there exists  $\tau, \tau_1, \dots, \tau_n$  such that

$$\Gamma \vdash U : \tau$$

and for  $1 \leq i \leq n-1$  we have

$$\Gamma, x_1:\tau_1, \dots, x_i:\tau_i, x:\tau \vdash U_{i+1} : \tau_{i+1} \quad \text{and} \quad \Gamma, x_1:\tau_1, \dots, x_n:\tau_n, x:\tau \vdash x : \sigma.$$

It follows from the last typing that  $\tau <: \sigma$ . Therefore

$$\Gamma \vdash U : \sigma$$

and so we may apply rule (Ev–id) to get

$$\Gamma \vdash [U]^a : \sigma.$$

as desired.

**Case:**

$$x[U/y ; s]^a \rightsquigarrow x[s]^a \quad x \neq y \quad (\text{RVar})$$

Let  $[s]$  be  $[U_1/x_1 ; \dots ; U_n/x_n]$ . We must have that  $\Gamma \vdash a : \sigma$ . By Lemma 6.2(4) there exist  $\tau, \tau_1, \dots, \tau_n$  such that

$$\Gamma \vdash U : \tau$$

and for  $1 \leq i \leq n-1$  we have

$$\Gamma, x_1:\tau_1, \dots, x_i:\tau_i, y:\tau \vdash U_{i+1} : \tau_{i+1} \quad \text{and} \quad \Gamma, x_1:\tau_1, \dots, x_n:\tau_n, y:\tau \vdash x : \sigma$$

Since  $x \neq y$  we get

$$\Gamma, x_1:\tau_1, \dots, x_n:\tau_n \vdash x : \sigma$$

apply rules (Ev–Clos<sub>1,2</sub>) to get

$$\Gamma \vdash x[s]^a : \sigma.$$

as desired.

**Case:**

$$([U]^b V)^a \rightsquigarrow (UV)^a \quad (\text{AppRed})$$

We must have that  $\Gamma \vdash a : \sigma$ . By Lemma 6.2(3) there exists a type  $\tau$  such that

$$\Gamma \vdash [U]^b : \tau \rightarrow \sigma \quad \text{and} \quad \Gamma \vdash V : \tau$$

By inspecting the typing rules we see that it must be the case that

$$\Gamma \vdash U : \tau \rightarrow \sigma$$

apply rule (Ev–Appl) to get

$$\Gamma \vdash (UV)^a : \sigma.$$

as desired.

**Case:**

$$(M \Leftarrow \mathfrak{m})[s]^a \rightsquigarrow (M[s]^b \Leftarrow \mathfrak{m})^a \quad (\text{SP})$$

Let  $[s]$  be  $[U_1/x_1 ; \dots ; U_n/x_n]$ , and let  $\Delta \triangleq x_1:\tau_1, \dots, x_n:\tau_n, x:\tau$ . We must have that  $\Gamma \vdash a : \sigma$ . By Lemma 6.2(4), there exist  $\tau_1, \dots, \tau_n$  such that, for  $1 \leq i \leq n-1$ , we get

$$\Gamma, \Delta \vdash U_{i+1} : \tau_{i+1} \quad \text{and} \quad \Gamma, \Delta \vdash (M \Leftarrow \mathfrak{m}) : \sigma$$

The last typing ended with rule (Call–Obj), so it must be that  $\sigma$  is of the form  $\delta[\tau/t]$  such that

$$\Gamma, \Delta \vdash M : \tau \quad \text{and} \quad \Gamma, \Delta \vdash \tau \ll\# \text{prot.} \langle\langle R, \mathfrak{m}; \delta \rangle\rangle$$

Since

$$\Gamma \vdash \tau \ll\# \text{prot.} \langle\langle R, \mathfrak{m}; \delta \rangle\rangle$$

apply rules (Ev–Clos<sub>1,2</sub>), using weakening on contexts, to get

$$\Gamma, b:\tau \vdash M[s]^b : \tau$$

and then rule (Ev–Send) to obtain

$$\Gamma, b:\tau \vdash (M[s]^b \Leftarrow \mathfrak{m})^a : \sigma$$

as desired.

**Case:**

$$(U \Leftarrow \mathfrak{m})^a \rightsquigarrow \text{Sel}^a(U, \mathfrak{m}, U) \quad (\text{SA})$$

We must have that  $\Gamma \vdash a : \sigma$ . Since the typing of  $(U \Leftarrow \mathfrak{m})^a$  ends with an application of rule (Ev–Send), then the type  $\sigma$  is of the form  $\delta[\tau/t]$  such that

$$\Gamma \vdash U : \tau \quad \text{and} \quad \Gamma \vdash \tau \ll\# \text{prot.} \langle\langle R, \mathfrak{m}; \delta \rangle\rangle$$

Rule (Ev–Lookup) is available with  $\rho \equiv \tau$ . The result type there is  $\sigma \equiv \delta[\tau/t]$ ; thus

$$\Gamma \vdash \text{Sel}^a(U, \mathfrak{m}, U) : \sigma$$

as desired.

**Case:**

$$([U]^b \leftarrow \mathfrak{m})^a \rightsquigarrow (U \leftarrow \mathfrak{m})^a \quad (\text{SRed})$$

Easy, since if  $[U]^b$  gets type  $\sigma$  under a given environment then so does  $U$ .

**Case:**

$$\text{Sel}^a(\langle U \leftarrow \mathfrak{m} = V \rangle^b, \mathfrak{m}, W) \rightsquigarrow (VW)^a \quad (\text{SU})$$

We must have that  $\Gamma \vdash a : \sigma$ . There are two cases to consider according to the  $\leftarrow$  operator is used as an override or an extension operator. We consider the first case the second being similar. By Lemma 6.2(5) we have

$$\Gamma \vdash \langle U \leftarrow \mathfrak{m} = V \rangle^b : \tau \quad \text{and} \quad \Gamma \vdash \tau \ll\# \text{prot}.\langle\langle R, \mathfrak{m}; \delta \rangle\rangle \quad \text{and} \quad \Gamma \vdash W : \rho \quad \text{and} \quad \Gamma \vdash \rho \ll\# \tau.$$

By Lemma 6.2 again we get

$$\Gamma, t \ll\# \tau \vdash V : t \rightarrow \delta.$$

The type  $\sigma$  of  $a$  is of the form  $\delta[\rho/t]$ . By Substitution Lemma (2) we get

$$\Gamma \vdash V : \rho \rightarrow \delta.$$

Apply rule (Sub) to get  $\Gamma \vdash W : \rho$  and rule (Appl) to get

$$\Gamma \vdash (VW)^a : \delta[\rho/t]$$

as desired.

**Case:**

$$\text{Sel}^a(\langle U \leftarrow \mathfrak{n} = V \rangle^b, \mathfrak{m}, W) \rightsquigarrow \text{Sel}^a(U, \mathfrak{m}, W) \quad \mathfrak{m} \neq \mathfrak{n} \quad (\text{NE})$$

We must have that  $\Gamma \vdash a : \sigma$ . There are two cases to consider according to the  $\leftarrow$  operator is used as an override or an extension operator. We consider the first case the second being similar. As in the previous case we have

$$\Gamma \vdash \langle U \leftarrow \mathfrak{m} = V \rangle^b : \tau \quad \text{and} \quad \Gamma \vdash \tau \ll\# \text{prot}.\langle\langle R, \mathfrak{m}; \delta \rangle\rangle \quad \text{and} \quad \Gamma \vdash W : \rho \quad \text{and} \quad \Gamma \vdash \rho \ll\# \tau.$$

Again we get

$$\Gamma \vdash U : \tau$$

The type  $\sigma$  of  $a$  is of the form  $\delta[\rho/t]$ . Apply rule (Ev-Lookup) to get

$$\Gamma \vdash \text{Sel}^a(U, \mathfrak{m}, W) : \delta[\rho/t]$$

as desired.

**Case:**

$$\text{Sel}^a([U]^b, \mathfrak{m}, V) \rightsquigarrow \text{Sel}^a(U, \mathfrak{m}, V) \quad (\text{SelRed})$$

Easy, since if  $[U]^b$  gets type  $\sigma$  under a given environment then so does  $U$ .

**Case:**

$$\langle M \leftarrow \mathbf{m} = N \rangle [s]^a \rightsquigarrow \langle M[s]^b \leftarrow \mathbf{m} = N[s]^c \rangle^a \quad (\text{FP})$$

Let  $[s]$  be  $[U_1/x_1; \dots; U_n/x_n]$ , and let  $\Delta \triangleq x_1:\tau_1, \dots, x_n:\tau_n$ . We must have that  $\Gamma \vdash a : \sigma$ . We know that  $\Gamma, \Delta \vdash \langle M \leftarrow \mathbf{m} = N \rangle : \sigma$ . There are two cases as to the last rule in this derivation: either the last applied rule is (Over) or (Ext). In the former case we have that

$$\Gamma, \Delta \vdash M : \sigma \quad \text{and} \quad \Gamma, \Delta \vdash \sigma \ll \# \text{prot.} \langle \langle R, \mathbf{m}; \tau \rangle \rangle \quad \text{and} \quad \Gamma, \Delta \vdash N : t \rightarrow \tau$$

Then

$$\Gamma \vdash M[s] : \sigma \quad \text{and} \quad \Gamma \vdash \sigma \ll \# \text{prot.} \langle \langle R, \mathbf{m}; \sigma_1 \rangle \rangle \quad \text{and} \quad \Gamma \vdash N[s] : t \rightarrow \tau$$

and we can augment the context  $\Gamma$  with declarations  $b:\sigma$  and  $c:t \rightarrow \tau$  to obtain the desired result. If the last applied rule is (Ext), then the proof is similar.

**Case:**

$$\langle [U]^b \leftarrow \mathbf{m} = V \rangle^a \rightsquigarrow \langle U \leftarrow \mathbf{m} = V \rangle^a \quad (\text{FRed})$$

Easy, since if  $[U]^b$  gets type  $\sigma$  under a given environment then so does  $U$ . □

Type Soundness follows from the Subject Reduction theorem. Recall Remark 3.3 concerning the normal forms of  $\lambda\text{Obj}^a$ .

**Theorem 6.2 (Type Soundness).** The evaluation of a typable  $\lambda\text{Obj}^a$  expression never results in *method not found*.

Formally: if  $\Gamma \vdash U : \sigma$ , and  $U \rightsquigarrow^* U'$ , then  $U'$  is not of the form  $\text{Sel}^a(\langle \rangle [s]^b, m, U)$ .

*Proof.* Immediate from the Subject Reduction theorem, since  $\text{Sel}^a(\langle \rangle [s]^b, m, U)$  is not typable. □

## 7. Conclusions

We have presented the initial steps in a theory of addressed term rewriting systems and detailed its use as a foundation for  $\lambda\text{Obj}^a$ , a formalism to describe object-based calculi. This case study of  $\lambda\text{Obj}^a$  shows how ATRS's can support the analysis of implementations at the level of resource usage, modeling sharing of computations and sharing of storage, where each computation step in the calculus corresponds to a constant-cost computation in practice.

There is much to be developed in the pure theory of ATRS's to be sure: fundamental questions concerning confluence, termination, critical lemmas, and so forth all deserve investigation. The ATRS setting is a congenial one to analyze *strategies* in rewriting-based implementations. For example the approach for functional languages studied in (Benaissa et al. 1996) should be generalizable to  $\lambda\text{Obj}^a$ : from a very general point of view, a strategy is a binary relation between addressed terms and addresses. The addresses, in relation

with a given term, determines which redex of the term has to be reduced next (note that in a given term at a given address, at most one rule applies).

The calculus  $\lambda\mathcal{O}bj^a$  itself is the basis for future work: we plan to extend  $\lambda\mathcal{O}bj^a$  to handle the embedding-based technique of inheritance, following (Lang et al. 1999), to include a type system consistent with object-oriented features with the ability to type objects extending themselves, following (Gianantonio et al. 1998).

The applied techniques in our formalism could be also be applied in the setting of fixed-size objects like the Abadi and Cardelli's Object Calculus (Abadi & Cardelli 1996).

Although  $\lambda\mathcal{O}bj^a$  is linear in addresses, we may consider whether to relax this constraint. Allowing non-linearity could have benefits such as supporting reasoning about term equality in a finer way. As an example we could design the following terms

$$\text{eq}(x^a, x^a) \rightarrow \text{true} \quad (1)$$

$$\text{eq}(x^a, x^b) \rightarrow \text{true} \quad (2)$$

$$\text{eq}(x^a, y^a) \rightarrow \text{true} \quad (3)$$

The first rewriting could correspond to physical equality (same object at the same address), while the second could correspond to a form of structural equality (same object in two different locations). The third equation could, *e.g.* be fired only if  $x = y$ .

Enriching our formalism with constant addresses is also another improvement. This could, *e.g.* allow terms of the following shape:

$$\text{private\_eq}(x^{\text{F9EE0004}}, y^{\text{F9EE0004}}) \rightarrow \text{true}$$

where `F9EE0004` is a constant address (in hexadecimal form).

Finally, a prototype of  $\lambda\mathcal{O}bj^a$  will make it possible to embed specific calculi and to make experiments on the design of realistic object oriented languages.

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## Appendix A. Acyclic Mutation-free ATRS

In this section, we consider a particular sub-class of ATRS, namely the ATRS involving *no cycles* and *no mutation*. Having no cycle means that there is no “pointer” from a node

to a node above it. We have noticed that such a back pointer is represented by a symbol  $\bullet$ . Thus an acyclic addressed term has no occurrence of  $\bullet$ . A mutation (or a side effect) is when an in-term which is not the in-term where the rewriting takes place is changed into another in-term. Due to the use of addresses this would be possible. The absence of mutation is made precise by the condition that addresses in the redexes cannot be changed. We show that the class of acyclic and mutation free ATRS is *sound* to simulate Term Rewriting Systems.

**Definition A.1 (Acyclicity and Mutation-freeness).**

- An addressed term is called *acyclic* if it contains no occurrence of  $\bullet$ .
- An ATRS rule  $l \rightsquigarrow r$  is called *acyclic* if  $l$  and  $r$  are acyclic.
- An ATRS is called *acyclic* if all its rules are acyclic.
- An ATRS rule  $l \rightsquigarrow r$  is called *mutation-free* if

$$a \in (\text{addr}(l) \cap \text{addr}(r)) \setminus \{\text{loc}(l)\} \Rightarrow l @ a \equiv r @ a.$$

- An ATRS is called *mutation-free* if all its rules are mutation-free.

The following definition aims at making a relation between an ATRS and Term Rewriting System. We define mappings from addressed terms to algebraic terms, and from addressed terms to algebraic contexts.

**Definition A.2 (Mappings).**

- An ATRS to TRS mapping is a homomorphism  $\phi$  from acyclic addressed preterms to finite terms which preserves variables and constructors (the set of constructors in the TRS is  $\{F_\phi \mid F \in \Sigma\}$ ):

$$\begin{aligned} \phi(X) &\triangleq X \\ \phi(F^a(t_1, \dots, t_n)) &\triangleq F_\phi(\phi(t_1), \dots, \phi(t_n)) \end{aligned}$$

- Given an ATRS to TRS mapping  $\phi$ , and an address  $a$ , we define  $\phi_a$  as a mapping from addressed preterms to multi-hole contexts, such that all sub-terms at address  $a$  (if any) are replaced with holes, written  $\diamond$ .

More formally,

$$\begin{aligned} \phi_a(X) &\triangleq X \\ \phi_a(F^b(t_1, \dots, t_n)) &\triangleq \begin{cases} \diamond & \text{if } a \equiv b \\ F_\phi(\phi_a(t_1), \dots, \phi_a(t_n)) & \text{otherwise} \end{cases} \end{aligned}$$

- Given a context  $C$  containing zero or more holes, we write  $C[t]$  the term obtained by filling all holes in  $C$  with  $t$ .
- Given an ATRS to TRS mapping  $\phi$ , we define the mapping  $\phi_s$  from addressed terms substitutions to term substitutions as follows:

$$\phi_s(\sigma)(X) \triangleq \begin{cases} \phi(\sigma(X)) & \text{if } X \in \text{dom}(\sigma) \\ X & \text{otherwise} \end{cases}$$

**Example A.1.** The address term  $\text{times}^a(\text{square}^b(2^c), \text{square}^b(2^c))$  is acyclic. The rule

$$\text{square}(x) \rightarrow \text{times}(x, x)$$



is mutation-free as left-hand side and right-hand side contain no internal addresses. One has:  $\phi_b(\text{times}^a(\text{square}^b(2^c), \text{square}^b(2^c))) = \text{times}_\phi(\diamond, \diamond)$ .

**Lemma A.1 (Mappings and Contexts).**

Let  $t$  be an acyclic addressed term, and  $a$  an address. Then  $\phi(t) \equiv \phi_a(t)[\phi(t @ a)]$ .

*Proof.* The case  $a \notin \text{addr}(t)$  is obvious since there is no hole to fill. Now let  $a \in \text{addr}(t)$ . We prove the lemma by structural induction on  $t$ .

- $t$  is obviously not a variable since it must contain at least address  $a$ .
- Let  $t$  be  $F^b(t_1, \dots, t_n)$ . The case  $a \equiv b$  is trivial. Otherwise,

$$\phi(t) \equiv F_\phi(\phi(t_1), \dots, \phi(t_n))$$

For each  $t_i$ , either  $a \notin \text{addr}(t_i)$  and then  $\phi(t_i) \equiv \phi_a(t_i)$ , or  $a \in \text{addr}(t_i)$  and then by induction hypothesis  $\phi(t_i) \equiv \phi_a(t_i)[\phi(t_i @ a)]$ . Since  $t$  is acyclic, then  $t_i @ a \equiv t @ a$ , hence  $\phi(t_i) \equiv \phi_a(t_i)[\phi(t @ a)]$ . Therefore,  $\phi(t) \equiv F_\phi(\phi_a(t_1), \dots, \phi_a(t_n))[\phi(t @ a)] \equiv \phi_a(t)[\phi(t @ a)]$  as desired. □

**Lemma A.2 (Replacements and Contexts).**

Let  $t$  and  $u$  be acyclic addressed terms where  $a \triangleq \text{loc}(u)$  is the only address in  $\text{addr}(t) \cap \text{addr}(u)$  such that  $u @ a \neq t @ a$ . Then

- 1  $\text{repl}(u)(t)$  is acyclic.
- 2  $\phi(\text{repl}(u)(t)) \equiv \phi_a(t)[\phi(u)]$ .

*Proof.*

- 1 Trivial according to the definition of replacement (no folding).
- 2 By structural induction on  $t$ .
  - If  $t$  is a variable  $X$ , then

$$\phi(\text{repl}(u)(X)) \equiv \phi(X) \equiv X \equiv X[\phi(u)] \equiv \phi_a(X)[\phi(u)]$$

- Let  $t$  be  $F^b(t_1, \dots, t_n)$ . Two cases are to be considered:

(a)  $a \equiv b$ : then

$$\phi(\text{repl}(u)(F^a(t_1, \dots, t_n))) \equiv \phi(u) \equiv \diamond[\phi(u)] \equiv \phi_a(t)[\phi(u)]$$

(b)  $a \neq b$ : note that since  $\text{repl}(u)(t)$  is acyclic, then

$$\phi(\text{repl}(u)(F^b(t_1, \dots, t_n))) \equiv \phi(F^b(\text{repl}(u)(t_1), \dots, \text{repl}(u)(t_n)))$$

(no folding). Hence, by induction hypothesis,

$$\begin{aligned} \phi(\text{repl}(u)(F^b(t_1, \dots, t_n))) &\equiv F_\phi(\phi_a(t_1)[\phi(u)], \dots, \phi_a(t_n)[\phi(u)]) \\ &\equiv F_\phi(\phi_a(t_1), \dots, \phi_a(t_n))[\phi(u)] \\ &\equiv \phi_a(t)[\phi(u)] \end{aligned}$$

□

**Lemma A.3 (Mapping and Substitution).**

If  $\langle \alpha; \sigma \rangle(t)$  is acyclic, then  $\phi(\langle \alpha; \sigma \rangle(t)) \equiv \phi_s(\sigma)(\phi(t))$ .

*Proof.* By structural induction on  $t$ .

— If  $t \equiv X$ , then

$$\phi(\langle \alpha; \sigma \rangle(X)) \equiv \phi(\sigma(X)) \equiv \phi_s(\sigma)(X) \equiv \phi_s(\sigma)(\phi(X))$$

— If  $t \equiv F^a(t_1, \dots, t_n)$ , then

$$\phi(\langle \alpha; \sigma \rangle(t)) \equiv \phi(F^{\alpha(a)}(\langle \alpha; \sigma \rangle(t_1), \dots, \langle \alpha; \sigma \rangle(t_n)))$$

since  $\langle \alpha; \sigma \rangle(t)$  is acyclic (fold is unnecessary). Hence,

$$\phi(\langle \alpha; \sigma \rangle(t)) \equiv F_\phi(\phi(\langle \alpha; \sigma \rangle(t_1)), \dots, \phi(\langle \alpha; \sigma \rangle(t_n)))$$

On the other hand,

$$\phi_s(\sigma)(\phi(t)) \equiv \phi_s(\sigma)(F_\phi(\phi(t_1), \dots, \phi(t_n))) \equiv F_\phi(\phi_s(\sigma)(\phi(t_1)), \dots, \phi_s(\sigma)(\phi(t_n)))$$

By induction hypothesis,  $\phi(\langle \alpha; \sigma \rangle(t_i)) \equiv \phi_s(\sigma)(\phi(t_i))$ .

□

**Lemma A.4 (In-term Substitution).**

Let  $\langle \alpha; \sigma \rangle(t)$  be an acyclic addressed term, and let  $b \in \text{addr}(t)$ , such that  $\alpha(b) \equiv a$ . Then  $\langle \alpha; \sigma \rangle(t) @ a \equiv \langle \alpha; \sigma \rangle(t @ b)$ .

*Proof.* By structural induction on  $t$ .

—  $t$  cannot be a variable since it must contain address  $b$ .

— If  $t$  be  $F^c(t_1, \dots, t_n)$ , then we consider two cases:

1  $b \equiv c$ :

$$\langle \alpha; \sigma \rangle(t) @ a \equiv F^a(\langle \alpha; \sigma \rangle(t_1), \dots, \langle \alpha; \sigma \rangle(t_n)) \equiv \langle \alpha; \sigma \rangle(t @ b)$$

2  $b \neq c$ :

$$\langle \alpha; \sigma \rangle(t) @ a \equiv F^{\alpha(c)}(\langle \alpha; \sigma \rangle(t_1), \dots, \langle \alpha; \sigma \rangle(t_n)) @ a$$

There must be some  $t_i$  such that

$$F^{\alpha(c)}(\langle \alpha; \sigma \rangle(t_1), \dots, \langle \alpha; \sigma \rangle(t_n)) @ a \equiv \langle \alpha; \sigma \rangle(t_i) @ a$$

Hence, by induction hypothesis,

$$\langle \alpha; \sigma \rangle(t) @ a \equiv \langle \alpha; \sigma \rangle(t_i @ b) \equiv \langle \alpha; \sigma \rangle(t @ b)$$

□

**Lemma A.5 (In-terms Conservation).**

Let  $l \rightsquigarrow r$  be an acyclic mutation-free rule,  $t$  an acyclic addressed term, and  $b$  an address in  $t$  such that  $t @ b \equiv \langle \alpha; \sigma \rangle(l)$  (i.e.,  $t @ b$  is a redex). Let  $\alpha_f$  be a fresh address renaming for  $l \rightsquigarrow r$  w.r.t.  $t$  and  $u \triangleq \langle \alpha \cup \alpha_f; \sigma \rangle(r)$ .

1  $u$  is acyclic.

2  $\forall a \in (\text{addr}(t) \cap \text{addr}(u)) \setminus \{b\}, t @ a \equiv u @ a$ .

*Proof.*

- 1 We show the acyclicity of  $u$  by contradiction. Assume  $u$  is cyclic. Since  $r$  is acyclic, there exists a sub-term of  $r$  of the form  $F^c(t_1, \dots, t_n)$  such that for one of the  $t_i$ ,  $\langle \alpha; \sigma \rangle(t_i)$  contains  $\alpha(c)$  (e.g., there is a fold that produces a  $\bullet$ ). Obviously,  $c$  is neither a fresh address nor the location of  $l$  and  $r$ , otherwise  $t_i$  would necessarily contain  $c$  i.e.,  $r$  would be cyclic. Hence,  $c$  is another address of  $l$ . We know by hypothesis that  $l@c \equiv r@c$  i.e., that  $l@c \equiv F^c(t_1, \dots, t_n)$ , and that  $t$  is acyclic i.e., that  $\langle \alpha; \sigma \rangle(t_i)$  does not contain  $\alpha(c)$ . Obviously, this is also true for  $\langle \alpha \cup \alpha_f; \sigma \rangle(t_i)$  since  $t_i$  does not contain fresh addresses (it belongs to  $l$ ). This contradicts the hypothesis. We conclude that  $\langle \alpha \cup \alpha_f; \sigma \rangle(r)$  is acyclic.
- 2 Assume there is  $a \in (\text{addr}(t) \cap \text{addr}(u)) \setminus \{b\}$  such that  $t@a \not\equiv u@a$ . Then  $a$  may have three distinct origins:
  - (a)  $a$  is a fresh address in  $u$ . Obviously, this is not possible since by hypothesis  $a \in \text{addr}(t)$ .
  - (b) There is an address  $c \in \text{addr}(l) \cap \text{addr}(r)$  such that  $\alpha(c) \equiv a$ . In this case,

$$u@a \equiv \langle \alpha \cup \alpha_f; \sigma \rangle(r)@a \equiv \langle \alpha \cup \alpha_f; \sigma \rangle(r@c)$$

from Lemma A.4 and acyclicity of  $u$ . Similarly,

$$t@a \equiv \langle \alpha; \sigma \rangle(l)@a \equiv \langle \alpha; \sigma \rangle(l@c)$$

From the hypothesis, we know that  $r@c \equiv l@c$ , hence  $r@c$  contains no fresh address i.e.,  $u@a \equiv \langle \alpha; \sigma \rangle(r@c) \equiv \langle \alpha; \sigma \rangle(l@c) \equiv t@a$ , which contradicts the hypothesis.

- (c) There is no address of  $l$  mapping to  $a$ . Since  $t$  is acyclic,  $\langle \alpha; \sigma \rangle(l)$  makes no folding i.e., for all sub-term of  $l$  of the form  $F^c(t_1, \dots, t_n)$ , we have

$$\langle \alpha; \sigma \rangle(F^c(t_1, \dots, t_n)) \equiv F^{\alpha(c)}(\langle \alpha; \sigma \rangle(t_1), \dots, \langle \alpha; \sigma \rangle(t_n))$$

Hence, there must be a variable  $X$  in  $l$  such that  $\sigma(X)$  contains  $a$ . According to the previous observation,  $t@a \equiv \sigma(X)@a$ . Since  $\langle \alpha; \sigma \rangle(r)$  is acyclic, it must also be the case that  $u@a \equiv \sigma(X)@a$ .

□

### Theorem A.1 (TRS Simulation).

Let  $S = \{l_i \rightsquigarrow r_i \mid i = 1..n\}$  be an acyclic mutation-free ATRS, and  $t$  an acyclic term. If  $t \rightsquigarrow u$  in  $S$ , then  $\phi(t) \rightsquigarrow^+ \phi(u)$  in the system  $\phi(S) = \{\phi(l_i) \rightsquigarrow \phi(r_i) \mid i = 1..n\}$

*Proof.* From the definition of addressed term rewriting, we have that  $t \rightsquigarrow \text{repl}(t)(u)$  where there are  $a, \alpha, \alpha_f, \sigma, l, r$  such that  $t@a \equiv \langle \alpha; \sigma \rangle(l)$ , and  $u \equiv \langle \alpha \cup \alpha_f; \sigma \rangle(r)$ . From Lemma A.1, we have  $\phi(t) \equiv \phi_a(t)[\phi(t@a)]$ . Note that  $a \in \text{addr}(t)$ , hence  $\phi_a(t)$  contains at least one hole. On the other hand, from Lemmas A.2 and A.5, we have  $\phi(\text{repl}(u)(t)) \equiv \phi_a(t)[\phi(u)]$ . We just have to show that  $\phi(t@a) \rightsquigarrow \phi(u)$  by rule  $\phi(l) \rightsquigarrow \phi(r)$ . This is immediate from Lemma A.3 since  $\phi(t@a) \equiv \phi(\langle \alpha; \sigma \rangle(l)) \equiv \phi_s(\sigma)(\phi(l))$ ,

and  $\phi(u) \equiv \phi(\langle \alpha \cup \alpha_f; \sigma \rangle(r)) \equiv \phi_s(\sigma)(\phi(r))$ , and obviously,  $\phi_s(\sigma)(\phi(l)) \rightsquigarrow \phi_s(\sigma)(\phi(r))$ ,  
by rule  $\phi(l) \rightsquigarrow \phi(r)$ .  $\square$