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Joint Routing and STDMA-based Scheduling to Minimize Delays in Grid Wireless Sensor Networks

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Joint Routing and STDMA-based Scheduling to Minimize Delays in Grid Wireless Sensor Networks

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**RESEARCH
REPORT**

N° 8488

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Project-Team Hipercom2



Joint Routing and STDMA-based Scheduling to Minimize Delays in Grid Wireless Sensor Networks

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Abstract: In this report, we study the issue of delay optimization and energy efficiency in grid wireless sensor networks (WSNs). We focus on STDMA scheduling, where a predefined cycle is repeated, and where each node has fixed transmission opportunities during specific slots (defined by *colors*). We assume a STDMA algorithm that takes advantage of the regularity of grid topology to also provide a spatially periodic coloring (“tiling” of the same color pattern). In this setting, the key challenges are: 1) minimizing the average routing delay by ordering the slots in the cycle 2) being energy efficient. Our work follows two directions: first, the baseline performance is evaluated when nothing specific is done and the colors are randomly ordered in the STDMA cycle. Then, we propose a solution, ORCHID that deliberately constructs an efficient STDMA schedule. It proceeds in two steps. In the first step, ORCHID starts from a colored grid and builds a hierarchical routing based on these colors. In the second step, ORCHID builds a color ordering, by considering jointly both routing and scheduling so as to ensure that any node will reach a sink in a single STDMA cycle. We study the performance of these solutions by means of simulations and modeling. Results show the excellent performance of ORCHID in terms of delays and energy compared to a shortest path routing that uses the delay as a heuristic.

Key-words: wireless sensor networks, grid, energy efficiency, delay, node activity scheduling, graph coloring, routing, ORCHID, cross layer, aggregation, dominating tree.

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Routage et Ordonnancement STDMA pour la minimisation des Délais dans les Réseaux de Capteurs sans fil en Grille

Résumé : Dans ce rapport , nous étudions l'optimisation des délais et l'efficacité énergétique dans réseau des réseaux de capteurs sans fil (WSNs). Nous nous concentrons sur l'ordonnancement STDMA. STDMA se base sur la répétition d'un cycle où chaque noeud a un slot pour transmettre ses données (ces slots sont définis par les *couleurs*). Nous supposons un algorithme d'ordonnancement STDMA qui tire avantage de la régularité de la topologie de réseau en grille pour fournir également un coloriage spatialement périodique (obtenu en répétant un certain motif de couleurs).

Dans ce cadre, les principaux défis sont les suivants: 1) réduire au minimum les délais de routage en ordonnant les slots sur le cycle de façon efficace. 2) tout en étant économe en énergie. Notre travail suit deux directions: d'abord, la performance de solutions de base c'est à dire, quand rien de précis est fait et les couleurs sont ordonnées arbitrairement sur le cycle STDMA. Ensuite, nous proposons une solution appelée ORCHID ("Optimized Routing and sCHeduling in grID wireless sensor networks") qui délibérément construit un ordonnancement STDMA efficace. ORCHID procède en deux étapes. Dans la première étape, ORCHID part d'une grille colorée et construit un routage hiérarchique basé sur ces couleurs. Dans la deuxième étape, ORCHID ordonne les couleurs sur le cycle STDMA en considérant conjointement le routage et l'ordonnancement dans le but de permettre à tout noeud d'atteindre le puits en un seul cycle. Nous étudions les performances de ces solutions par modélisation et simulation. Les résultats montrent les performances excellentes de ORCHID en termes d'économie d'énergie et des délais comparé au routage basé sur les plus courts chemins et utilisant le délai comme métrique.

Mots-clés : Réseaux de capteurs sans fil, grille, efficacité énergétique, délais, ordonnancement de l'activité des noeuds, coloriage des graphes, routage, ORCHID, optimisation multi-couche, aggrégation, arbre dominant.

Contents

I	General Introduction and System Model	5
I.1	Introduction	5
I.1.1	Context	5
I.1.2	Sources of Delays in STDMA	5
I.1.3	Contributions	6
I.1.4	Organization of the Report	7
I.2	Background	7
I.2.1	Delay Efficient TDMA	7
I.2.2	Geographic Routing	8
I.2.3	VCM: Vector-based Coloring Method	9
I.3	System Model and General Assumptions.	10
II	Random Color Ordering	12
II.1	Overview and Problem Statement	12
II.2	Performance Analysis with a Stochastic Model	12
II.3	Performance Analysis with Simulations	12
II.3.1	Shortest Delay Routing	12
II.3.2	Greedy Routing Heuristic	13
II.4	Experimental Results.	13
II.5	Discussion about IRCO Performance	14
III	ORCHID: “Optimized Routing and sCHeduling in grID wireless sensor networks”	15
III.1	Overview about ORCHID	15
III.1.1	Assumptions	15
III.1.2	Problem Statement.	15
III.1.3	Discussion about Intuitive Solutions.	15
III.1.3.1	First solution: OPERA [9].	15
III.1.3.2	Second solution: Spanning Tree with VCM	16
III.1.4	Principles of ORCHID	16
III.1.5	Overview	17
III.2	<i>Routes</i> : Dominating Tree Construction	19
III.2.1	Principles of the Construction of a Dominating Tree.	20
III.2.2	Algorithm of the Construction of a Dominating Tree	21
III.2.3	Properties of a Dominating Tree	22
III.2.3.1	General Properties	22
III.2.3.2	Properties Concerning Nodes in \mathcal{P}_A	23
III.2.4	Results: Examples of Dominating Trees	24
III.2.5	Routing Over the Dominating Tree	24
III.3	CO-Routes: Color Ordering for <i>Routes</i>	24
III.3.1	Method of CO-Routes.	24
III.3.2	Results about the Delays Obtained	25

III.4 <i>Highways</i> Construction and Coloring.	26
III.4.1 <i>Highways</i> Construction and Routing over <i>Highways</i>	26
III.4.2 CO- <i>Highways</i> : Color Ordering for <i>Highways</i>	27
III.5 Orchestration in a STDMA cycle	28
III.5.1 Overview	28
III.5.2 Further CO- <i>Highways</i> Optimizations	28
III.6 ORCHID Performance	29
III.6.1 Cycle Length	29
III.6.2 Energy Consumption	29
III.7 Experimental Results.	30
III.7.1 Number of Slots of ORCHID and IRCO	31
III.7.1.1 How the Number of Slots is Computed?.	31
III.7.1.2 Results.	31
III.7.2 Number of Reachable Aggregators in one Cycle per Node	32
III.7.3 Energy Consumption in ORCHID	33
III.7.3.1 How Energy Consumption is Computed?	33
III.7.3.2 Results.	33
IV Estimate of Normalized Delay per Range with Random Color Ordering	35
IV.1 Model	35
IV.2 Computation of the Normalized Delay Per Range	36
IV.3 Experimental Validation of Asymptotic Expression for Normalized Delay per Range	37
V Conclusion	40
VI Annex	44

Part I

General Introduction and System Model

I.1 Introduction

I.1.1 Context

TDMA scheduling is one of the energy efficient techniques used in WSNs. Each node is allocated a time slot for data transmissions. The main drawback of this pure TDMA is the underutilization of the bandwidth. To limit this problem, many solutions providing bandwidth spatial reuse are proposed (STDMA: Spatial Reuse TDMA) [1, 2]. One technique to perform such spatial reuse is the graph coloring. Generally those solutions work as follows. The graph is colored such that no two nodes that have the same color interfere. As an example, let us consider the linear network depicted in Figure 1(a). Assuming that interferences are limited to 2 hops, the same color is assigned to nodes *A* and *D*, while a second color is assigned to nodes *B* and *E* and a third color is assigned to nodes *C* and *F* because these couples of nodes are 3 hops away. Consequently, they can transmit in the same slot without interfering (see cycle in Figure 1(b)).

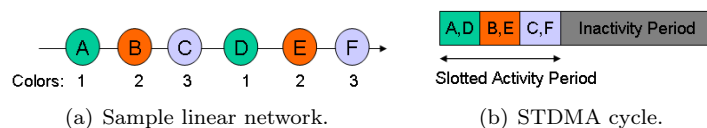


Figure 1: Example of a network and the STDMA cycle associated.

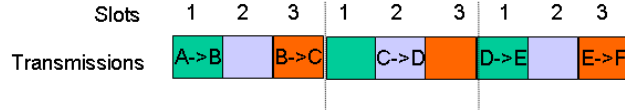
The slots form the activity period in the STDMA cycle (also called superframe). The remaining part of the STDMA cycle corresponds to the inactivity period where all nodes can turn off their radio and save energy. Consequently, this scheme avoids energy wasted in idle listening and collisions.

I.1.2 Sources of Delays in STDMA

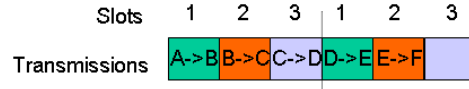
While the main advantage of STDMA in WSNs is energy saving, it is also deterministic. Indeed, contention-based protocols suffer from collisions especially under high data rate. This factor makes the end-to-end delays unpredictable. For delay sensitive applications like fire detection, this is not acceptable. However, it does not suffice to assign slots to nodes in an arbitrary way to obtain minimum end-to-end delays. Two reasons may increase delays:

- **First reason:** Due to the fact that interfering nodes cannot have the same slot, the cycle obtained may be very long. A schedule of minimum length can be achieved by maximizing the reuse of time slots. Therefore, most existing algorithms aim at maximizing the number of transmissions scheduled in the same slot and enable spatial reuse by devising strategies to eliminate interferences [3, 4].
- **Second reason:** While routing protocols address the delay issues by choosing the shortest path, the order of medium access by nodes on this path is also crucial. Let us consider the example of Figure 1(a) and the two schedules depicted in Figures 2(a) and 2(b). In the second schedule, colors purple and orange are associated with slots 2 and 3 respectively

instead of slots 3 and 2 in the first schedule. Notice that, consequently, slots assigned to E and F are switched.



(a) First ordering: green→slot1, purple→slot2, orange→slot3.



(b) Second ordering: green→slot1, orange→slot2, purple→slot3.

Figure 2: Examples of two color ordering for the same network.

Following the first scheduling (Figure 2(a)), node A requires 9 time slots to transmit data to node F . However, if nodes follow the second schedule (Figure 2(b)), only 5 slots are required. The reason is that, in the first scheduling, node B receives the packet from C after the end of its slot. However, in the second schedule, slots are sorted according to the same sequence as nodes appear in the route from A to F .

This result means that each time there is an inversion in the slot ordering compared to the appearance order of nodes in the path, delays are increased by one cycle. Consequently, to minimize the delays, in addition to minimizing the cycle length, finding a smart scheduling of the transmissions between any couple of transmitter and receiver is required. This is the main idea of this work. We focus on delay optimization of the scheduling done by a coloring algorithm called VCM [7, 8]. VCM is dedicated to grid networks and provides a periodic coloring obtained by repeating a specific color pattern.

I.1.3 Contributions

We treat the aforementioned problem following two directions:

- **First**, we study the performance of our baseline, a **random** assignment of slots (or colors) in the cycle, and we estimate **the average normalized delay per radio range**. We evaluate this delay by simulation (based on shortest-delay routing and greedy routing), and by a stochastic model (Section II.1).
- **Second**, we propose a solution for delay and energy optimization. Our contributions are summarized as:
 - In general, the delay optimization is either managed by routing or by scheduling separately. In our work, we integrate both approaches using cross-layering (i.e. jointly).
 - Usually researchers consider that minimizing delays in STDMA is done by minimizing the STDMA cycle length. We believe that, in addition, slot misordering must be investigated. Hence, in our work, we consider both problems.
 - We design a solution called **ORCHID ("Optimized Routing and sCHeduling in grID wireless sensor networks")** (see Section III.1) which includes:
 1. A hierarchical routing architecture where sensor nodes route data to a predefined set of data aggregators and these aggregators route data to the data sink.

2. A coloring of nodes and aggregators that allows sensor nodes to collect data to the closest aggregator in a single cycle.
 3. An orchestration of the medium access of sensors and aggregators in the STDMA cycle, that is the colors ordering in the cycle.
- The produced delays are optimized: ORCHID guarantees data collection in a single cycle.
 - ORCHID supports multiple sinks and sink mobility: indeed, any node is able to reach any aggregator in a single cycle. Hence, this node can route data to any of these aggregators. This also makes ORCHID fault tolerant.
 - Paths between aggregators are also fast: less than or equal to one cycle.
 - Simulations show that ORCHID outperforms shortest-delay path routing in terms of delays and energy.

I.1.4 Organization of the Report

This report contains 6 parts.

1. The first part is a general introduction describing the context, the problem and the contributions. Section I.2 presents the background of this work. In Section I.3, we present the general assumptions and the system model that we consider in this work.
2. The second part deals with the random ordering of colors in the cycle. We evaluate the performance of the system under this assumption by stochastic model (see Section II.2) and by simulation (see Section II.3).
3. The third part focuses on the proposed solution ORCHID. Section III.1 gives an overview of the proposed solution and its main principles. Sections III.2, III.3, III.4 and III.5 detail the components of ORCHID. In Section III.6, we present the performance evaluation of ORCHID.
4. The fourth part proposes a model (see Section IV.1) for the determination of the distribution of the average delay per range.
5. The fifth part concludes the report.
6. The sixth part is an annex providing figures and curves illustrating some obtained results, in addition to those presented in the previous parts.

I.2 Background

This section provides a state of the art about (1) Delay efficient TDMA, (2) Geographic routing and (3) VCM: Vector-Based Coloring Method since it is the coloring method that ORCHID uses.

I.2.1 Delay Efficient TDMA

Most delay-aware TDMA scheduling solutions for WSNs attempt to minimize the schedule length [3, 4]. Other works rather focus on delays induced by the misordering of slots. In general, this latter class includes two categories: the first one assumes that routing is given and optimizes the scheduling accordingly. Inversely, the second one deduces the routing from a given

scheduling. Examples of the first category include [11, 12, 13, 14, 15]. In [11], authors present an integer linear programming (ILP) formulation of the minimum latency link scheduling problem. Assuming that routes are given, the model is able to find the slot used by each link while minimizing the latency. We believe that the linear programming approach is not suitable for large networks. Solutions [13, 14, 15] assume that a data gathering tree is given. They ensure that any node has a slot earlier than its parent in the routing tree. Hence only one cycle is required for data aggregation. For instance, DMAC [14] allocates slots to nodes based on their positions in the data gathering tree. Nodes at the deepest levels in the tree are assigned the earliest slots. In SERENA [15] which is a scheduling algorithm based on graph coloring, each node has a color higher than the color of its parent in the data gathering tree. Consequently, when slots are assigned according to the decreasing order of colors, any parent node has a slot that follows the slots of all its children. We have shown in [15] that the cost of such ordering is the increase of the cycle length. Unlike DMAC, SERENA is conflict free while ensuring the spatial reuse.

In the second category, we can cite Yu et al. [16]. Indeed, each link i, j is assigned a cost that is equal to the time difference between slots assigned to nodes i and j respectively. The advantage of this solution is that all routes (from any node to another) are possible without scheduling change, but on the other hand nothing makes them energy-efficient. CBS [17] is based on a tree formed by cluster heads that relay data from other nodes. Both intra and inter-cluster communications are TDMA based. Furthermore, to minimize delays, each node has a slot after its parent in the tree. Also, the authors propose an asynchronous method for cluster heads communications.

I.2.2 Geographic Routing

Geographic routing algorithms use position information to perform packet forwarding. Most geographic routing algorithms use a greedy strategy that tries to approach the destination in each step, e.g. by selecting the neighbor closest to the destination as a next hop. However, greedy forwarding fails in local minimum situations, i.e. when reaching a node that is closer to the destination than all its neighbors. Furthermore, there are other several greedy routing strategies that select the relay based on the distance and the direction towards the destination [28]. [28] and [29] provide a good survey of geographic routing protocols in ad hoc networks. TTDD [31] and PGP [30] support sink mobility and propose a hierarchical routing structure. This structure is based on a virtual grid which divides the sensor field into many cells. Then the sensors on the grid points will serve as dissemination nodes. PGP optimizes the induced overhead of TTDD.

Geographic routing has also been proposed as a good approximation to minimize end-to-end delays. The idea [19] is to carry packets to their destination in as few hops as possible, by making as large progress as possible at each relaying node. In this context, related works turn around, among others, the determination of the number of hops of the optimal path [21] or the optimal transmission range as in [22] or the optimal delay per hop [18]. Ma et al. [21] proposed a geometric model to compute the conditional probability that a destination node has hop-count h with respect to a source node given that the distance between the source and the destination is d .

Not very far from our context, [20, 18, 19] proposed geographic routing to minimize delays considering a duty cycle based medium access. The heuristic that [19, 21] use is the progress towards the destination. Unlike [19, 21], we consider that a good heuristic to compute one-hop optimal delays has to take into account not only the progress towards the destination but also the transmission latency. This is the heuristic that we use in the design of our model for delay per range determination (see Section IV.1).

I.2.3 VCM: Vector-based Coloring Method

VCM [7, 8] is a graph coloring algorithm dedicated to dense grid networks. Indeed, the grid topology is one of the best methods to ensure full area coverage in surveillance applications [5, 6]. From the research point of view, working on grids can be a first step towards general graphs as it is easier especially for theoretical studies.

The intuitive idea of VCM is as follows. As the grid topology presents a regularity in terms of node positions, VCM produces a similar regularity in terms of colors and generates a color pattern that can be periodically reproduced to color the whole grid. An example of the coloring provided by VCM is given in Figure 3.

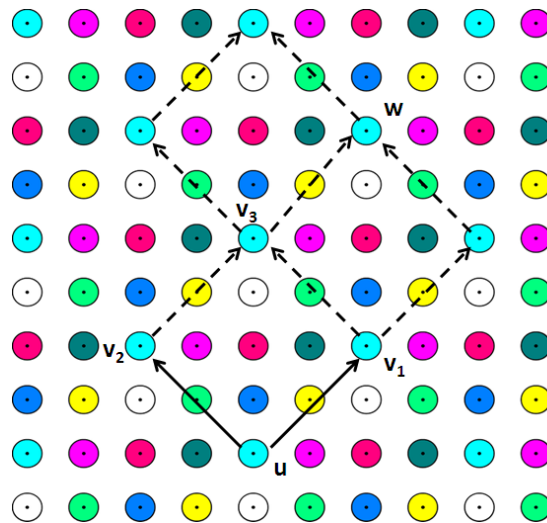


Figure 3: An example of 3-hop coloring provided by VCM (no 1 or 2 or 3-hop neighbors share the same color.)

The color pattern is defined by two vectors ($u\vec{v}_1$ and $u\vec{v}_2$ in Figure 3) called *VCM generator vectors*. VCM finds a color pattern that minimizes the number of colors used and hence minimizes the length of the TDMA cycle. VCM coloring has the following properties:

1. Nodes having the same colors form a lattice of generator vectors $u\vec{v}_1$ and $u\vec{v}_2$. Moreover, nodes with the same color are nodes with the same relative coordinates according to the parallelogram they belong to. Consequently, color computation is based on the coordinates of nodes relatively to their parallelogram.
2. All colors produced by VCM are the colors inside one parallelogram: The number of colors produced by VCM to color the whole grid for any transmission range R is equal to the scalar product of the two generator vectors.
3. The number of colors produced by VCM does not depend on the size of the grid, but on the transmission range of sensor nodes.

In this work, we adopt this algorithm because:

1. Nodes compute their colors using a simple formula without requiring message exchange with neighbors.

2. The schedule that VCM provides is optimal in terms of length when the transmission range tends to infinity. Indeed, we have proved in [8], that the number of colors is given by Property 1.

Property 1. (From [8]) *The number of colors $n_c(R)$ of an optimal periodic h -hop coloring, including VCM, for a fixed h verifies:*

$$n_c(R) = \theta R^2 (1 + O(\frac{1}{R})), \text{ with } \theta = \frac{\sqrt{3}}{2} h^2, \text{ when } R \rightarrow \infty.$$

I.3 System Model and General Assumptions

For the remaining of this paper, we consider the following system model.

- **Network:** Nodes are disposed in a grid of a grid step $s = 1$.
- **Neighborhood:** As in several wireless network studies, we adopt a unit disk communication model [33]. Any node can communicate directly with all nodes within a disk centered at itself with a communication range denoted R
- **Medium access:** is based on STDMA where each node has a slot to transmit data, and more than one node can share the same slot. These slots are granted based on colors computed by VCM.
- **STDMA cycle inactivity:** in performance evaluations, for simplicity, we assume a STDMA cycle without inactivity period, and the delay is expressed as the number of slots necessary for a packet to travel from a source to a destination¹ (as in Equation 2).
- **One-Hop Delay:** Let $D(i, j)$ denotes the induced one-hop delay for a packet of node i that is further relayed by node j . Assume that node i is assigned the slot s_i while j is assigned the slot s_j . Hence, $D(i, j)$ is equal to the difference between the slot where i starts transmitting and the slot s_j where the node j relays the last bit of the packet of i . Let S be the total number of slots. Like in [16] and assuming that there is no inactivity period, we have:

$$D(i, j) = \begin{cases} s_j - s_i & \text{if } s_j > s_i \\ S + s_j - s_i & \text{if } s_j < s_i \end{cases} \quad (1)$$

- **Route Delay:** on a route (path) \mathcal{P} of successive nodes $(v_1, v_2, v_3, \dots, v_k)$, the delay is computed as the sum of successive one-hop delays except for the last node (which is the destination and does not relay the packet).

$$D(\mathcal{P}) = D(v_1, v_2) + D(v_2, v_3) + \dots + D(v_{k-2}, v_{k-1}) \quad (2)$$

- **Normalized Delay per Range:** it is the route delay with distance normalized by radio range. In other words, if i and j are separated by geometric (Euclidean) distance $|i - j|$, and that path between them is $\mathcal{P} = (i, \dots, j)$, hence with delay $D(\mathcal{P})$ slots, the normalized delay \bar{D} is:

$$\bar{D}(\mathcal{P}) = \frac{D(\mathcal{P})}{|i - j|/R} \quad (3)$$

¹Notice that for a large delay D of spanning several cycles of length C , the number of inactivity periods is directly estimated as $\lfloor D/C \rfloor$ with an error of at most 1. Hence even with inactivity periods, for large D/C mostly the value of the delay D is of interest.

It represents the average delay, expressed in number of slots, necessary to progress by a distance equivalent to one radio range (on the route). This quantity takes into account both delay and geographic progress which makes it a good heuristic. Also, it is similar for many routes of a given network (color ordering), hence it is one of the most relevant metrics for performance evaluation.

Part II

Random Color Ordering

This part of the report assumes an arbitrary assignment of colors in the cycle. The objective is to evaluate the performance of the delay under such assumptions.

II.1 Overview and Problem Statement

In addition to the assumptions presented in Section I.3, we consider a grid colored with VCM and we assume that **the colors are randomly associated with slots**. This assumption let us define a basic method for building the global cycle. We denote this method **IRCO** (“**I**terated **R**andom **C**olor **O**rding”). IRCO consists in randomly ordering colors of a single VCM cycle and then repeating successively this *base VCM cycle* several times in a row before the inactivity period. E.g. one IRCO cycle is a repetition of several identical VCM cycles necessary to allow all data from any source to reach its destination. Notice that IRCO concerns colors ordering and is independent from the routing adopted. In the following, we will evaluate IRCO with shortest-path routing and greedy routing.

The aim of this section is to answer the question: *What is the performance of routing with a random coloring, in terms of delay?*

We answer this question by designing a stochastic analytical model (brief description in Section II.2 and more details in Section IV.1) and by simulation in Section II.3. Results of the two techniques are also compared at the end of Section II.3.

II.2 Performance Analysis with a Stochastic Model

We design a model to estimate the average normalized delay per range. Unlike previous works (such as [19, 21]), this model selects as relay the neighbor yielding the best normalized delay per range. We have proved the following property, in Section IV.1:

Property 2. *An estimate of the normalized delay per range is $\frac{3}{2}\theta + \frac{3}{4}\pi$, when the radio range R grows and the number of colors is θR^2 , where $\theta = \frac{\sqrt{3}}{2}h^2$ (for a h -hop coloring²).*

The implication of this result is rather strong: despite that fact that the number of colors grows as R^2 , with random color ordering, to travel a distance of K hops, the delay in “number of slots” stays equal to a constant $(\frac{3}{2}\theta + \frac{3}{4}\pi)$ multiplied by K . For example, for a 3-hop coloring with VCM, the estimate of the normalized delay per range is $\approx 14.05\dots$ which is rather low.

II.3 Performance Analysis with Simulations

Assuming IRCO, the routing method that we use for simulations is one of:

II.3.1 Shortest Delay Routing

Because the delay as defined in Section I.3 is an additive metric, it is possible to compute the “shortest path” using classical algorithms (Bellman-Ford, Dijkstra) with this metric, as proposed

²a h -hop coloring prevents nodes up to h hops to use the same color.

for instance by [23]. The paths that are found are the shortest paths in terms of delay (expressed as in Equation 2) necessary to reach the destination. Note that equivalently, they yield the lowest normalized delay per range computed over the whole route.

II.3.2 Greedy Routing Heuristic

One possible strategy for routing on a colored grid, is the following: at every hop, decide what would be the next hop, based only on local information (and not global information). This is a strategy derived from geographic routing (see Section I.2.2). For delay-aware routing, it is better to select a relay node presenting the best compromise between delay and progress. Such a *greedy* heuristic is proposed, for instance, in CMAC [18]: the selected next-hop is the one that yields the lowest ratio “delay”/“progress towards the source”. We can adopt this heuristic in this context of grid network and predefined STDMA. Note that this is equivalent to yielding the lowest normalized delay per range computed on one hop.

II.4 Experimental Results

The objective of this section is to provide statistics about the average normalized delays. The parameters of simulation are summarized in Table 1. The two simulated strategies are the greedy routing and the shortest-delay path denoted respectively “*Sim. greedy end-to-end*” and “*Sim. shortest-delay path end-to-end*” in Figure 4. For each strategy, source nodes were selected according to Table 1. The path from each source to the center point $(0, 0)$ was computed. We first compute the total end-to-end delay (expressed in slots). Then, we obtain the normalized delay per range by dividing this delay by the geometric distance (normalized by radio range). An average was taken on all selected nodes, and on multiple simulations.

Nb simulations	100 random orderings with same VCM vectors
Radio range	from 1 to 7 with step 1/4
Routing strategy	shortest-delay path or greedy heuristic
Size of the grid	$L \times L$ with $L = 601$
Sources for Greedy routing	100 random nodes at distance D with $0.9L \leq D \leq L$
Sources for Shortest-Delay Path routing	all nodes at distance D with $0.9L \leq D \leq L$

Table 1: Simulation parameters

For reference, we also computed the asymptotic estimate of the model given by Property 2 (plot with caption “*Model*” in Figure 4). The results are represented in Figure 4.

The main result is that the normalized delay per range increases only slowly with the radio range (although the number of colors increases as the square of the range) and is quickly plateauing (as predicted by the model in Section IV.1). This illustrates that the random ordering of colors yields a bounded normalized delay even when the number of colors increases to infinity. In other words, random ordering of colors is an optimal ordering when radio range increases.

Furthermore, for both cases of greedy routing and shortest-delay path, the normalized delay per range is below 12 (and the asymptotic estimate is 14.0..). Knowing that a loose lower bound

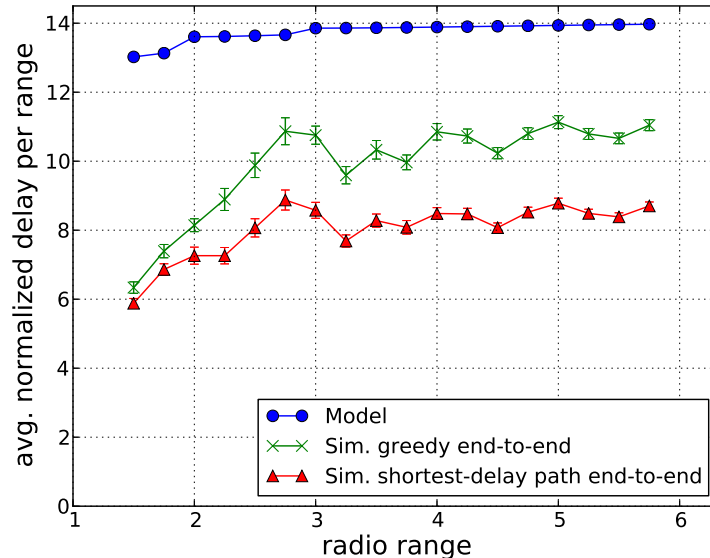


Figure 4: Normalized delay for random color orderings.

is 1, this shows that the obtained delay is within an order of magnitude of an optimal STDMA.

Comparing shortest-delay path and greedy routing, we see that shortest-delay path gives approximately 25% better performance than greedy routing. The conclusion is that greedy routing performs quite well, if we take into account the fact that it does require only local information. Indeed, shortest-delay path, requires, of course, a shortest-path computation of every destination, which is cumbersome for WSNs, in a scenario where the destination(s) are not known in advance. Notice here the irregularity of the curves. This is explained by the fact that colors by radio range area vary following the same irregularity. Indeed, when we plot the number of colors divided by the area of the disk of radio range (πR^2), we also see the same irregularity.

II.5 Discussion about IRCO Performance

The expected performance of IRCO is rather good, as deduced from results. However, random ordering suffers from drawbacks. The dimensioning of the entire cycle necessary to allow all data from any source to reach its destination is critical. It depends on the set of destinations and could be estimated from experimental results seen above. The drawback of IRCO is that either dimensioning is only estimated, potentially losing determinism (guarantees of arrival within one IRCO cycle), or the dimensioning requires pre-computing in advance all routes. In addition, all nodes are potential relays, and therefore should be awake on their neighbors slots. In the following, we investigate solutions without these drawbacks.

Part III

ORCHID: “Optimized Routing and sCHeduling in grID wireless sensor networks”

III.1 Overview about ORCHID

In this section, we will describe the problem statement and give an overview about the principles of the proposed solution ORCHID ("Optimized Routing and sCHeduling in grID wireless sensor networks").

III.1.1 Assumptions

In addition to the assumptions considered in Section I.3, we present some other assumptions. We assume a data gathering application where sensors monitor a region and transmit periodic measures or alarms to the sink. We assume also that aggregation is possible: any sensor is able to aggregate data it receives with the data it generates. This assumption is realistic especially for low traffic or when sensor nodes perform some operations like computing the average value or the maximum value of the received data. We assume that each node is assigned a single time slot that suffices for all its transmissions.

III.1.2 Problem Statement

When the scheduling is based on colors, ordering the slots of a given schedule is equivalent to associating each color with a time slot in the right order (cf. Figure 1).

The problem statement: Given a grid colored by the periodic coloring VCM, we want to build routes taking into account these colors and then to order these colors in a schedule ensuring that data reach the sink in minimized delays.

We will see in the next sections that solving this problem is achieved by a cross layering between the scheduling and the routing.

III.1.3 Discussion about Intuitive Solutions

Before describing the proposed solution, we explain the different steps that lead us to find the solution we propose under the aforementioned assumptions.

III.1.3.1 First solution: OPERA [9]

We have previously proposed the OPERA software composed of the routing EOLSR [9] and the scheduling OSERENA [10] adapted to random topologies. EOLSR builds a routing spanning tree and OSERENA colors the tree such that the color of any node is higher than the color of its parent. Colors are ordered in the cycle according to the decreasing order. Consequently, this coloring constraint allows the data aggregation in one cycle. However, for dense and large grid networks, VCM is more optimized than OSERENA in terms of number of colors. Moreover,

applying a routing algorithm that relies on a spanning tree like EOLSR and integrating it with a periodic coloring like VCM raises some issues as explained in the next section.

III.1.3.2 Second solution: Spanning Tree with VCM

This solution consists in building a spanning tree of the colored grid.

1. Assume first that we keep a random order of VCM colors on the STDMA cycle. This random order might result in slot inversion and hence additional delays as explained in the scenario of Figure 2. Hence, what we should do is to order colors of nodes in the routes.
2. Having a spanning tree that contains all nodes raises the following constraints:
 - (a) Due to the periodicity of VCM colors, it is impossible that any tree branch contains any color just once for large grids. In this case, delays might exceed one cycle.
 - (b) Let a node N and the route of this node towards the sink via the tree. We order the colors of intermediate nodes in this route such that the color of the first node that appears first in this route is associated to the first slot. This ensures one STDMA cycle for data transmission for this node N . However, we cannot do this for all nodes, for two reasons. First, colors are shared between routes (color repetitions with VCM). Second, nodes are located at different geographic locations relative to the data sink. So, optimizing the order of these colors for some nodes according to one direction might make this order not suitable for other directions.

The previous constraints lead us to define the following principles. Instead of building a unique spanning tree including all nodes where it is impossible to have delay optimal routes towards the sink for all nodes, we build several trees. Roots of these trees are particular nodes called aggregators. Then, we order the colors of nodes on these trees. Furthermore, each color does not appear more than once in any tree branch. Any aggregator routes data to another aggregator until data is received by the data sink. In the following two sections, a detailed description of the proposed solution ORCHID is given.

III.1.4 Principles of ORCHID

Figure 7 illustrates an example of a grid colored by VCM. u_1 and u_2 are the generator vectors of VCM.

1. Over this grid, ORCHID builds a hierarchical routing: nodes transmit data to a set of aggregators which are the vertices of VCM pattern, and these aggregators route data to the sinks (such as node A in Figure 7). The sinks should be placed themselves at aggregators.
2. Routing and scheduling must ensure that:
 - Any node reaches the closest aggregator in a single cycle. The path followed by this node is denoted '*Route*' in the remaining of this report.
 - The number of nodes that are able to reach more than one aggregator in a single cycle must be maximized. Hence, if a node cannot reach the closest aggregator because of link failure, it can route over another aggregator to reach the data sink. Also, this is useful also when there are multiple sinks. In this case, any node can select the closest aggregator for every sink.
 - Paths between aggregators should be fast. They are denoted '*Highways*' in the remaining of this report.

3. *Routes* and *Highways* are activated successively in the STDMA cycle as depicted in Figure 5. The first activity period of the STDMA cycle is dedicated to *Routes*: activation of

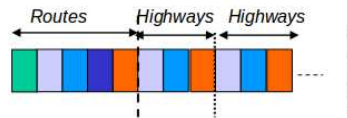


Figure 5: Orchestration of *Routes* and *Highways* slots in the STDMA cycle.

VCM colors. Each VCM color occurs only once. Unlike the first period which appears only once in the global STDMA cycle, the second period corresponds to the repetition of the *Highway* cycle a number of times sufficient to allow data sent by aggregators to reach the sink. We will see later that this cycle is divided into sub-cycles. Notice that the number of scheduled colors in this period is smaller than in the first one.

4. The scheduling is based on VCM colors. More precisely, no new colors are used and the spatial reuse obtained by VCM must still be guaranteed. Furthermore, these colors must be ordered in the STDMA cycle for both *Routes* and *Highways* periods, in such a way that the end-to-end delays are minimized.
5. Any node is active only during the slots associated with its color and the colors of its potential transmitters and can sleep the remaining time, both in the *Routes* period and the *Highways* period.

III.1.5 Overview

This section provides a more detailed description of the proposed solution. Figure 6 illustrates its different steps and components.

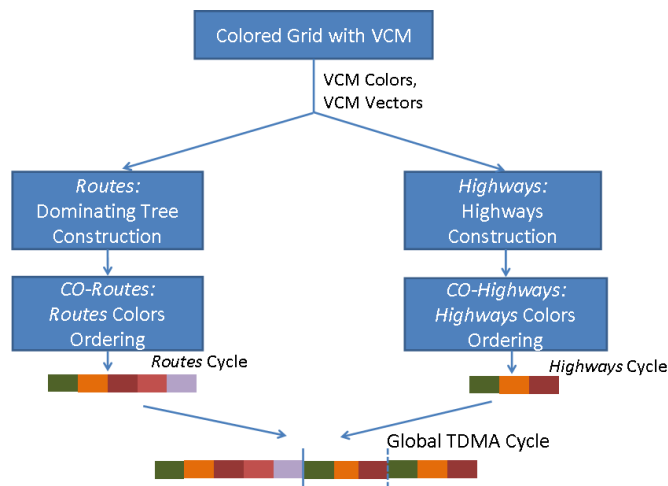


Figure 6: Steps of the proposed method.

First, the solution is applied to a colored grid of sensors where each node has a color computed by VCM. The principles of the solution can be presented in the following components:

1. **Hierarchical routing structure:** Figure 7 illustrates the hierarchical routing proposed. It is composed of *Routes* (dominating trees) and *Highways*. u_1 and u_2 are the VCM generator vectors.

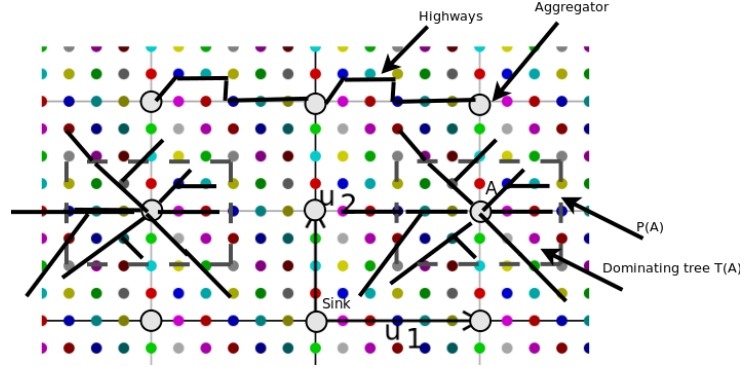


Figure 7: Hierarchical routing structure.

(a) **Routes:**

- A set of aggregators is chosen: the vertices of VCM patterns (see Figure 7 as an example).
- Each aggregator A is the root of a dominating tree \mathcal{T}_A . A dominating tree is defined by Definition 1.

Definition 1. A tree \mathcal{T} dominates a set of nodes if any node of this set is either in the tree or has at least one neighbor belonging to this tree. Such a neighbor is called dominator.

In other words, we build a connected dominated set (CDS) constructed as a tree which is rooted at the aggregator. The tree rooted at any aggregator A_2 is a copy of the tree rooted at another aggregator A_1 after translation by the vector A_1A_2 .

- For any aggregator A , any tree \mathcal{T}_A is as large as possible as long as any color appears only once in this tree branch.
- Any node in the grid is necessarily dominated by the tree rooted at the closest aggregator and possibly by trees rooted at other aggregators. As previously said, these other trees are copies of the tree rooted at the closest aggregator after being translated.

- (b) **Highways:** are used by aggregators to join the data sink. Except aggregators in the border of the grid, any other aggregator has 4 neighboring aggregators. We build 4 shortest paths between this aggregator and each of these 4 neighboring aggregators. These shortest paths are called *Highways*.

Remark 1. *Routes and Highways construction takes into account the colors of the nodes and is based on the same algorithm for all nodes. Hence, the obtained trees and Highways are the same for all aggregators (as illustrated in Figure 7). For this reason, in the following, we only reason about one aggregator.*

- (c) **Routing:** Concerning *routing*, any node should maintain the list and positions of the aggregators that it can reach in a single cycle. Given a sink with a known position, this node determines the closest aggregator to this sink and routes data over the

dominating tree rooted at this aggregator. Hence, it transmits data either to its parent if it belongs to the tree, or its dominator otherwise. Then, we use geographic routing to determine for each aggregator the following aggregator over *Highways*.

2. **Ordering colors:** Recall that the slot assignment is based on colors computed by VCM. To ensure minimized delays avoiding slot misordering, ORCHID performs **Color Ordering** of *Routes* and *Highways*. The Color Ordering is defined as:

Definition 2 (Color Ordering). *Given a set of colored nodes in a given path, each node color is associated with a time slot in such a way that the color of the node that appears first in the path (the farthest node from the destination) is associated with the first slot.*

Color Ordering ensures that nodes along a path are activated in the STDMA cycle in the same order of their appearance in this path. Hence, slot misordering delays are avoided and data are delivered within the same cycle. *Color Ordering of Routes* and *Highways* consists in:

- (a) **CO-Routes (Color Ordering of Routes):** The color of the first node linked to the tree is associated with slot 1, the color of the second node is associated with color 2, etc. Then, colors of remaining nodes are associated with remaining slots. This method ensures that the slot granted to a node increases with the node level in the tree. Also, any dominated node has a slot after its *dominator*.
 - (b) **CO-Highways (Color Ordering of Highways):** Similarly, for *Highways* the order of colors is the same as the order of the nodes on the routes from one aggregator to the next one.
3. **Orchestration:** The global STDMA cycle is composed of the *Routes* cycle and the *Highways* cycle. Notice that the *Highways* cycle is smaller. The *Highways* cycle itself is composed of sub-cycles repeated a number of times sufficient to allow the farthest aggregator to reach the data sink.

In the following, we will detail the steps of ORCHID.

III.2 *Routes*: Dominating Tree Construction

Aggregators are nodes with coordinates $\alpha u + \beta v$ where u and v are VCM generator vectors and α and $\beta \in \mathbb{Z}$. Each aggregator is the root of a dominating tree. Hence, ORCHID deals with the dominating tree construction problem. This problem [25, 26] is similar to the CDS (Connected dominating Set) problem [24] but the obtained structure must be a tree. Both of these problems are NP-complete [24, 25]. The dominating tree problem is defined in [25] as follows: given a graph G with weighted edges, DomTree problem asks to find a minimum total edge weight tree T such that each vertex is either in T or has a neighbor in T . In our case, the objective of ORCHID is to connect as many as possible sensors to their closest aggregator. Furthermore, constraints concern essentially colors and behind them the induced delays. More specifically, we define the problem of **Dominating Tree Construction** denoted **DomTree** as:

DomTree Problem: Finding a dominating tree with:

- The objectives:

O1. Each node dominated by a tree must reach the root of this tree in one cycle.

O2. The number of aggregators that any node can reach in a single cycle is maximized.

O3. Any tree dominates the largest possible number of nodes in the grid.

- The constraints:

C1. Each color appears only once in any branch of the dominating tree.

C2. Between any two branches of the tree, any two colors appear always in the same order when going on a path towards the root.

In this section, we describe our algorithm to answer this problem. We start by introducing the following notation where u and v are VCM generator vectors.

Notation 1 (Parallelogram \mathcal{P}_A). By abuse of notation, for a vector w of coordinates x_w, y_w , we denote $\frac{w}{2}$ the vector of coordinates $\lfloor \frac{x_w}{2} \rfloor$ and $\lfloor \frac{y_w}{2} \rfloor$ ³. Then, we define for any aggregator A of coordinates $(0, 0)$ the parallelogram \mathcal{P}_A with vertices $(0, u, v, u + v)$ translated by $-(\frac{u}{2} + \frac{v}{2})$.

III.2.1 Principles of the Construction of a Dominating Tree

We consider the aggregator with coordinates $(0, 0)$. The obtained tree will be repeated for all other aggregators (see Remark 1). We propose a connected dominating tree construction algorithm based on a "greedy" heuristic, as follows:

1. The tree is built in 2 steps: for the aggregator A of coordinates $(0, 0)$, we first build a connected dominating tree in \mathcal{P}_A only. Then, we extend the obtained tree via the domination of nodes outside this area. The same algorithm is used for the interior and the exterior of this area.
2. The first node added to the tree is its root. All its neighbors are then dominated.
3. More generally, when any node is added to the tree, all its undominated neighbors are marked as dominated. The algorithm will not attempt to dominate them later.
4. Nodes are dominated in the order of their increasing number of hops towards the root. For two nodes that are at the same number of hops from the root, the node with the smallest geographic distance to the root is dominated first.
5. Nodes belonging to \mathcal{P}_A must have dominators inside \mathcal{P}_A .
6. Nodes having VCM colors already used in the tree (color is identical to the color of a node already in the tree) cannot be dominated and cannot be used as dominators.
7. When any node N is linked to the tree after having dominated one node, other nodes that have the same color and that are already dominated, are also linked to the tree.
8. Selection of dominators is based on a priority denoted *priority*. To define this priority, we use a heuristic. The priority of any node is the total number of the undominated neighbors that will be dominated if this node is added to the tree (with all other candidates of the same color).

³ $\lfloor \cdot \rfloor$ is the symbol for integer part.

III.2.2 Algorithm of the Construction of a Dominating Tree

Given the aggregator A of coordinates $(0,0)$, Algorithms 1, 2 and 3 illustrate the construction of the dominating tree \mathcal{T}_A rooted at A .

Algorithm 1 DOMINATING-TREE-CONSTRUCTION

```

1: Input: the grid  $G$ , the aggregator  $A$  and the priority (the heuristic for the selection of the dominator)
2: Output: the dominating tree  $\mathcal{T}$ 
3:  $dominatedNodes = \{A\}$ 
4:  $treeColors = \{color\ of\ A\}$ 
5: Add all the neighbors of  $A$  to  $dominatedNodes$ 
6:  $insideNodes =$  list of nodes in  $\mathcal{P}_A$  sorted according to the increasing distance in number of hops relative to the root of the tree. In case of equality, consider the node with the smallest geographic distance to the sink.
7:  $inside = True$ 
8: for all ( $toBeDominated \in insideNodes$ ) /*Dominate nodes inside  $\mathcal{P}_A$ */ do
9:    $dominator =$  DOMINATE-NODE ( $\mathcal{T}$ ,  $toBeDominated$ ,  $inside$ ,  $dominatedNodes$ ,  $treeColors$ ,  $priority$ )
10:  if ( $dominator \neq NULL$ ) then
11:    ADD-NODES ( $dominator$ ,  $\mathcal{T}$ ,  $dominatedNodes$ ,  $treeColors$ ) /*Adding dominated nodes with the same color as  $dominator$ */
12:  end if
13: end for
14:  $outsideNodes =$  list of nodes  $\notin \mathcal{P}_A$  sorted according to the increasing distance in number of hops relative to the root of the tree. In case of equality, consider the node with the smallest geographic distance to the sink.
15:  $inside = False$ 
16: for all ( $toBeDominated \in outsideNodes$ ) /*Dominate nodes outside  $\mathcal{P}_A$ */ do
17:    $dominator =$  DOMINATE-NODE ( $\mathcal{T}$ ,  $toBeDominated$ ,  $inside$ ,  $dominatedNodes$ ,  $treeColors$ ,  $priority$ )
18:   if ( $dominator \neq NULL$ ) then
19:     ADD-NODES ( $dominator$ ,  $\mathcal{T}$ ,  $dominatedNodes$ ,  $treeColors$ ) /*Adding dominated nodes with the same color as  $dominator$ */
20:   end if
21: end for

```

Algorithm 2 ADD-NODES ($dominator$, \mathcal{T} , $dominatedNodes$, $treeColors$)

```

1: for all ( $node \in$  the grid  $G$  with the same color as  $dominator$ ) do
2:   if ( $node \in dominatedNodes$ ) then
3:      $hasDominated = False$ 
4:     for all ( $neigh$  neighbor of  $node$ ) do
5:       if ( $neigh \notin dominatedNodes$ ) and ( $color\ of\ neigh \notin treeColors$ ) then
6:          $hasDominated = True$ 
7:         Add  $neigh$  to  $dominatedNodes$ 
8:         The  $dominator$  of  $neigh = node$ 
9:       end if
10:    end for
11:   end if
12:   if ( $hasDominated == True$ ) then
13:     Add  $node$  to  $\mathcal{T}$ 
14:     The parent of  $node =$  the  $dominator$  of  $node$ 
15:   end if
16: end for

```

Algorithm 3 DOMINATE-NODE (\mathcal{T} , $toBeDominated$, $inside$, $dominatedNodes$, $treeColors$, $priority$)

```

1: if ( $toBeDominated \in \mathcal{T}$ ) OR ( $toBeDominated \in dominatedNodes$ ) OR ( $color\ of\ toBeDominated \in treeColors$ )
   then
2:   return NULL /*This node cannot be dominated*/
3: end if
4: /*Try to dominate node  $toBeDominated$  */
5: for all ( $neighbor \in neighbors\ of\ node\ toBeDominated$ ) do
6:   if ( $inside == True$  and  $neighbor \notin \mathcal{P}_A$ ) then
7:     continue /*next neighbor*/
8:   end if
9:   if ( $(neighbor \notin \mathcal{T})$  OR ( $neighbor \notin dominatedNodes$ ) OR ( $color\ of\ neighbor \in treeColors$ )) then
10:    continue /*next neighbor*/
11:   else
12:     Add  $neighbor$  to  $possibleDominators$ 
13:   end if
14: end for
15: if ( $possibleDominators$  is empty) then
16:   return NULL /*Cannot dominate this node*/
17: end if
18: Sort  $possibleDominators$  according to  $priority$ 
19:  $dominator$  = first node sorted in the list  $possibleDominators$ 
20: Add  $dominator$  to  $\mathcal{T}$ 
21: The parent of  $dominator$  = the dominator of  $dominator$ 
22: Add color of  $dominator$  to  $treeColors$ 
23: The dominator of  $toBeDominated$  =  $dominator$ 
24: return  $dominator$ 

```

III.2.3 Properties of a Dominating Tree

In this section, we present some results concerning the dominating tree.

III.2.3.1 General Properties

Theorem 1. *Algorithm 1 generates a tree.*

Proof: When any node is dominated, a new link is added to the tree between this node and its dominator. Consequently, there is no disconnected parts in the grid. Hence the result. ■

Lemma 1. *At each iteration, the closest undominated node N to the root A has necessarily at least one neighbor that is dominated by \mathcal{T}_A .*

Proof: Assume by contradiction that N has no dominated neighbors. Since nodes form a grid (a connected graph), this assumption means that there exists a shortest path N, N_1, \dots, N_k, A between N and the aggregator A , where k is a positive integer. Two cases are possible:

- either N_1 is dominated, then there is a contradiction because we could select it as a dominator for N ;
- either N_1 is not dominated and it is then the closest node to the root and not N as assumed. This results in a contradiction. Hence the proof. ■

Lemma 2. *At any iteration, the algorithm attempts to dominate the closest node N to the root aggregator that is not dominated. Two cases are possible,*

- *Case 1: N cannot be dominated because:*
 - *its color is used by another node already in the tree;*

– all of its dominated neighbors have colors already in the tree;

- Case 2: N can be dominated and a possible dominator exists.

Proof: First, at any iteration, Algorithm 1 considers the closest node N to the root and attempts to dominate this node if and only if this node is not dominated and this node has not a color used by a node belonging to the tree (see lines 1 and 2 in Algorithm 3). Second, from Lemma 1, N has at least one neighbor that is dominated. If this neighbor has a color that appears in the tree, it is not kept (see line 10 in Algorithm 3). Otherwise, it is a possible dominator for N (see line 12 in Algorithm 3). Hence the Lemma. ■

Theorem 2. *Algorithm 1 meets the constraints of DomTree problem.*

Proof: According to Algorithms 3 and 2, any color is linked to the tree in two occasions:

- First, to dominate any node, its dominator is added to the tree only if its color does not already appear in the tree (see lines 9 and 20 in Algorithm 3). Hence, no color repetition is produced. Let *dominator* denotes this dominator.
- Second, once *dominator* is linked to the tree, other nodes sharing the same color are added to the tree at the same time if they are already dominated and if they have undominated neighbors (see line 13 in Algorithm 2). This is the only time when nodes with this color are added to the tree. Because the tree is "grown" from the root, the same color cannot be repeated twice in the branch.

Now, to prove that Constraint **C2**. is met, let us assume that two colors c_1 and c_2 are on the same branch. Assume that c_1 is closer to the root than c_2 . This means that all nodes with color c_1 had been added in the tree before all nodes with the color c_2 and are all closer to the root. Hence, the ordering of colors on the branches must be identical. ■

Theorem 3. *A dominating tree addressing the DomTree problem cannot dominate all nodes for all sizes of the grids.*

Proof: From Lemma 2, dominating one node becomes not possible when the color unicity of the tree might be not guaranteed. However, the number of colors is finite. Consequently, to guarantee the color unicity in the tree, some nodes might not be dominated. ■

III.2.3.2 Properties Concerning Nodes in \mathcal{P}_A

Lemma 3. *Nodes in \mathcal{P}_A have all different colors.*

Proof: \mathcal{P}_A is nothing other than the VCM pattern translated by the vector $\frac{u}{2} + \frac{v}{2}$. By definition, inside VCM pattern, there is no color reuse. ■

Theorem 4. *Any node among \mathcal{P}_A is necessarily dominated by the dominating tree rooted at A .*

Proof: By contradiction, let us assume that there is one or several nodes in \mathcal{P}_A that are not dominated. Let M be the undominated node that is the closest to the aggregator. According to Lemma 1, node M has at least one neighbor D that is dominated. Because all nodes in \mathcal{P}_A (hence in the dominating tree \mathcal{T}_A) have different colors (Lemma 3), D could have been selected as a dominator for M . Hence the contradiction. ■

Corollary 1. *Any grid node is necessarily dominated by the tree rooted at the closest aggregator and possibly by trees rooted at other aggregators.*

Proof: The result is true because of Theorem 4 and because by construction, an aggregator A is the closest aggregator to nodes in \mathcal{P}_A . In addition, a tree rooted at A_i can dominate nodes belonging to some \mathcal{P}_{A_j} for i and j different positive integers. ■

III.2.4 Results: Examples of Dominating Trees

Figure 8 depicts the dominating tree rooted at the aggregator of coordinates $(0, 0)$. The radio range is set to $3 \times$ the grid step. Thick lines present the tree links while the thin lines are the links between a node and the nodes it dominates. We observe that the obtained tree dominates a larger area: almost 16 color patterns.

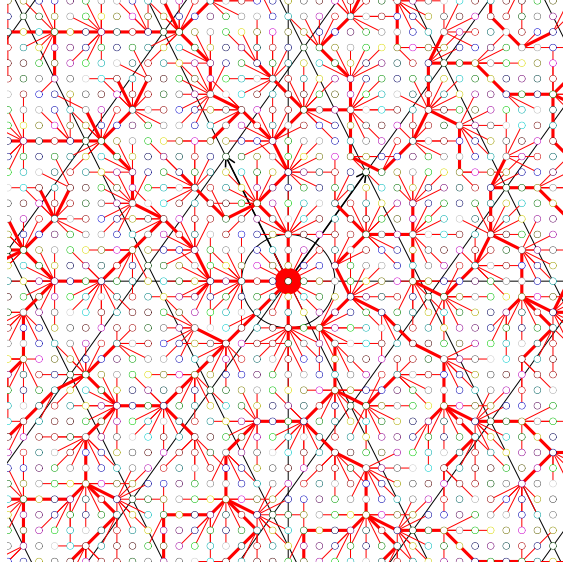


Figure 8: The dominating tree obtained for 3 hop coloring and radio range = $3 \times$ the grid step.

Figure 9 depicts two trees rooted at aggregators of coordinates $(0, 0)$ and $(6, -1)$. Only tree links are presented.

III.2.5 Routing Over the Dominating Tree

Any node that is dominated by a tree, including the tree rooted at the closest aggregator, can reach the root of this tree. Its next hop to reach this aggregator is:

- Either its parent in the tree, if it belongs to the tree.
- Or its dominator relative to this tree, otherwise.

III.3 CO-Routes: Color Ordering for *Routes*

Our objective is to have *Color Ordering of Routes*.

III.3.1 Method of CO-Routes

Method 1 (CO-Routes). *The method is based on the following steps:*

1. *Step1: Nodes of a tree:*

Consider the tree \mathcal{T}_A rooted at the aggregator A of coordinates $(0, 0)$. We first start by ordering colors of nodes belonging to \mathcal{T}_A . During the construction procedure of the dominating

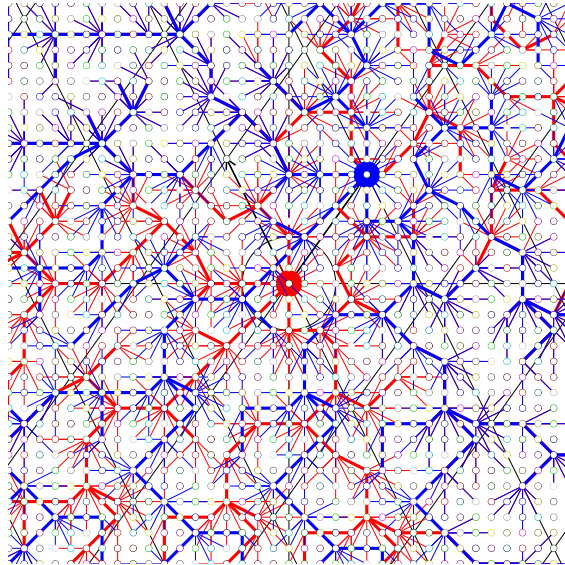


Figure 9: Two dominating trees for 3 hop coloring and radio range = $3 \times$ the grid step.

tree (line 20 in Algorithm 3), the color of the first node added to the tree is associated with slot 1, the second (node or set of nodes) is associated with slot 2, etc. With this method, nodes with deepest levels in the tree are scheduled in the latest slots of the schedule.

2. Step2: Remaining nodes:

The remaining grid nodes are sorted according to the increasing number of hops to the root of \mathcal{T}_A and in case of equality the geographic distance is considered as a second criteria. Then, the colors of these nodes are associated with time slots starting with the last slot of the first step.

Remark 2. Method 1 produces a periodic coloring obtained by repeating \mathcal{P}_A for any aggregator A . Consequently, no new colors than those of VCM are used and no color conflict are created.

Lemma 4. According to Method 1, for any aggregator A , colors of nodes inside \mathcal{P}_A are ordered before nodes outside.

Proof: Colors of nodes linked first to the tree are ordered firstly. Since the dominating tree construction starts by dominating nodes inside \mathcal{P}_A , we have the result. ■

III.3.2 Results about the Delays Obtained

Theorem 5. Nodes belonging to any tree \mathcal{T}_A reach A in one cycle.

Proof: Let N be any node in \mathcal{T}_A . By Method 1, N will be scheduled after all its ascendants since they are linked to the tree before node N . Moreover, from Theorem 2, no two nodes have the same color among these ascendants. Because of Constraint C2., the ordering of colors is consistent on all branches. Hence, no slot misordering is produced and no more than one cycle is needed by any node N to reach this aggregator A . ■

Theorem 5 is essential. Indeed, generally the whole activity period in the STDMA cycle is followed by an inactivity period. Hence, delivering data in a single cycle avoids the latency caused by this inactivity period.

Lemma 5. *All nodes that reach any aggregator A in one cycle are necessarily dominated by the tree \mathcal{T}_A .*

Proof: Assume that a node can reach the aggregator in one cycle. This means that there is a path to the root with only increasing slots numbers. Algorithm 1 adds nodes to the tree one after the other, and orders their colors at the same time. Iteratively, each node of the path, in the reverse order, will be successively dominated by the next-hop and later linked to the tree. Therefore, the path will be (part of) a branch of the tree. ■

Lemma 6. *For any aggregator A , for any node $N \in \mathcal{P}_A$, $N \notin \mathcal{T}_A$ and $N \in \mathcal{T}_{A_1}$ where A_1 is another aggregator, then N is scheduled after its dominator relative to the tree rooted at A .*

Proof: According to Lemma 4, the colors of nodes inside \mathcal{P}_A is ordered before the part outside. Thus, the color of N is ordered after all nodes in \mathcal{P}_{A_1} . This means that the color of N is ordered after all nodes in \mathcal{P}_A (because \mathcal{T}_A is a copy of \mathcal{T}_{A_1}). Hence, N has a slot after its dominator relatively to \mathcal{T}_A . ■

Theorem 6. *Given any aggregator A , nodes in \mathcal{P}_A reach the aggregator A in one cycle.*

Proof: Let N be a node in \mathcal{P}_A . From Lemma 4, this node has necessarily one dominator which is its next hop towards the aggregator A . Three cases are possible:

1. $N \in \mathcal{T}_A$: the result comes from Theorem 5.
2. $N \notin \mathcal{T}_A$ and does not belong to any tree. According to Method 1, the color of N is ordered after nodes in the tree, and hence after its dominator. This dominator is able to reach the sink in a single cycle (see Theorem 5).
3. $N \notin \mathcal{T}_A$ but belongs to a tree \mathcal{T}_{A_1} rooted at another aggregator A_1 . According to Lemma 6, N is scheduled after its dominator, itself able to reach the sink in a single cycle according to Theorem 5. ■

III.4 Highways Construction and Coloring

Highways are paths between aggregators and the data sink. Data are routed from one aggregator to another in the direction of the sink. These paths are called *Highways* because they are fast; they comprise a small number of nodes and slots granted to these nodes are ordered according to the appearance order of nodes in these paths. In this section, we detail these aspects.

III.4.1 Highways Construction and Routing over Highways

The construction of *Highways* is based on the following steps:

1. Considering the grid composed of aggregators (that is a grid where vertices are the aggregators), except aggregators on the grid border, other aggregators have 4 neighboring aggregators. That is, if we consider the aggregator A_0 placed at the center of the grid, this aggregator has 4 neighboring aggregators A_u, A_v, A_{-u}, A_{-v} placed respectively at nodes of coordinates $u, v, -u$ and $-v$ where u and v are the VCM vectors.

2. We build 4 shortest paths between the aggregator A_0 and each of its neighboring aggregators A_u, A_v, A_{-u}, A_{-v} . For this reason, we speak about 4 *Highways*: H_u on the direction of u vector (between A_0 and A_u), the second one denoted H_v in the direction of v vector, the third one denoted H_{-u} is in the direction of $-u$ vector and the fourth one denoted H_{-v} in the direction of $-v$ vector.
3. Two different *Highways* should not share the same nodes in order to balance the routing load among different nodes.

Figure 10 illustrates the *Highways* built by ORCHID for the node $(0, 0)$ for $R = 3$.

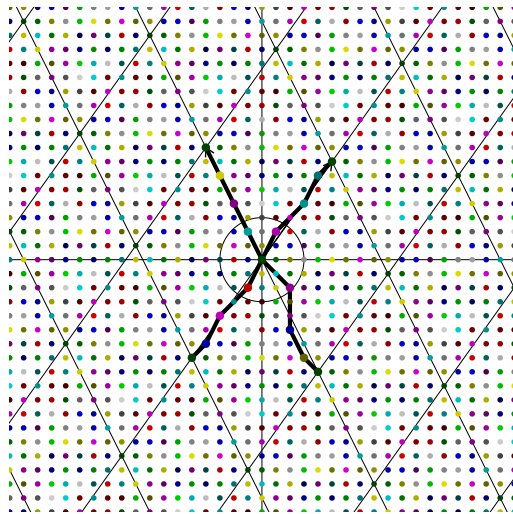


Figure 10: Highways built by ORCHID for $R = 3$.

4. The *Highways* built for the aggregator A_0 are repeated for all other aggregators. Notice that aggregators that are in the border of the grid do not need to use all of these *Highways*.
5. The previous steps allow each aggregator to have *Highways* in four directions. These *Highways* are used for routing as follows: We use a geographic routing like [27] to select the closest neighboring aggregator to the sink. The path followed to reach this aggregator is the *Highway* computed in step 2.

Remark 3. *Each aggregator has Highways in 4 directions. This ensures a routing flexibility especially in the following scenarios: the position of the sink is unknown beforehand, the sink is mobile or when there are multiple sinks. Furthermore, it increases robustness against routing failures.*

Remark 4. *Notice that building Highways for further directions means activating more nodes in H period. Hence, the number of built Highways for any aggregator is subject to a compromise between delays and energy consumed per node.*

III.4.2 CO-Highways: Color Ordering for *Highways*

After building *Highways*, we now order the colors of nodes in these *Highways* in a specific part of the STDMA cycle dedicated to *Highways*. Indeed, for each *Highway*, we order the color of

intermediate nodes of this *Highway* such that the color of the i^{th} node on this path is associated with the time slot number i in the part of the STDMA cycle dedicated to *Highways*.

Theorem 7. *With Highways, no more than one cycle is required to any aggregator to reach one of its neighboring aggregators.*

Proof: Nodes on *Highways* have colors that are sorted according to the order of nodes in *Highways*. So, each node is scheduled before its next hop. Hence the theorem. ■

III.5 Orchestration in a STDMA cycle

The key idea of the proposed architecture is that each packet is transmitted first from its source node to the closest aggregator, then this packet is transmitted from one aggregator to another via *Highways* until it reaches the data sink. In this section, we describe how to carry out this orchestration.

III.5.1 Overview

Figure 11 depicts the global STDMA cycle obtained. This cycle is composed of two periods:

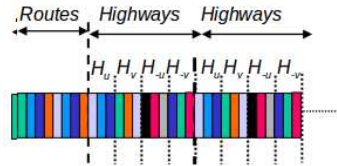


Figure 11: Global STDMA cycle.

1. The first period R corresponds to the schedule of *Routes*. In this period, each node is awake during its slots and during the slots of its children in the dominating tree. Let $|R|$ be the number of slots of this period.
2. The second period H is composed of colors of the *Highways*. This period is divided itself into 4 sub-periods: H_u , H_v , H_{-u} and H_{-v} . Each period H_w corresponds to a *Highway* between two aggregators optimized according to the direction of vector w (see Section III.4.1). The alternation of the 4 directions in H cycle favors access fairness between aggregators that follow different directions to reach the sink. The period H is repeated a number of times until the farthest aggregator from the sink reaches this sink. In this period, each node belonging to any *Highway* is awake during its slots and during the slots of nodes for which it is a next hop.

III.5.2 Further CO-Highways Optimizations

In the cycle for *Highways*, H_u , H_v , H_{-u} , H_{-v} , we observe that one slot is allocated to aggregators at the beginning of each H_k cycle where $k \in \{u, v, -u, -v\}$. Some of these are unneeded, such as the first slot of H_u that immediately follows the last slot of the *Routes*, also dedicated for aggregators. By further reordering the *Highway* sequence as H_u , H_{-u} , H_v , H_{-v} , the first slots of H_{-u} and H_{-v} can also be removed, as they never provide progress for routing⁴. Hence, if the highway H_u has 3 intermediate nodes, the relative cycle should contain only 3 slots.

⁴The one of H_v is still required so as to be able to route both along u and v in the same highway cycle.

III.6 ORCHID Performance

The objective of this section is to estimate the number of slots of the global STDMA cycle of ORCHID and compare the energy consumed in ORCHID and in IRCO (see Section II.1). First, we introduce the following notations:

- The radio range is R and the network is comprised of nodes of the grid, within the disk of size defined by L (with radius = LR).
- The periodic coloring is defined with vectors u_1, u_2 ; their angle is α , and the number of colors is θR^2 (Property 1).
- We have proved in [7] that an asymptotically optimal (in number of colors) h-hop coloring produced by VCM yields when $R \rightarrow \infty$ values asymptotically equivalent to: $\theta = \frac{\sqrt{3}}{2}h^2$, $\alpha = \frac{\pi}{3}$, $|u_1| = |u_2| = hR$

The determination of the number of nodes in a disk of radius R is known as the *Gauss Circle Problem*⁵. This number is equal to $\pi R^2 + o(R)$. In the following, we asymptotically approximate this number by πR^2 .

III.6.1 Cycle Length

The STDMA cycle of ORCHID comprises a number of slots estimated by Theorem 8.

Theorem 8. *With VCM coloring, the global STDMA cycle comprises a number of slots estimated by $\theta R^2 + 4(h+1)\frac{4\sqrt{3}L}{3h}$ ⁶.*

Proof: The global STDMA cycle of ORCHID is composed of *Routes* that contains θR^2 slots and *Highway* that should be repeated a number of times that we denote λ . λ should be such that one aggregator is able to reach the farthest other aggregator in the disk of radius LR . The largest *geographic* distance between any two nodes corresponds to the diagonally opposite nodes with $2L$. The minimum (worst-case) progress in along one direction when doing geographic routing is $\min(|u_1|, |u_2|) \sin(\alpha)$ per full *Highway* cycle (H_u, H_v, H_{-u}, H_{-v}). Consequently, (when $R \rightarrow \infty$):

$$\lambda = \frac{2LR}{\min(|u_1|, |u_2|) \sin(\alpha)} = (\text{for VCM}) \frac{4\sqrt{3}L}{3h} \quad (4)$$

Now, we need to compute the number of slots of a given *Highway*. Remember that a *Highway* is a path between two given neighbor aggregators. Asymptotically when $R \rightarrow \infty$ (and thus $|u_1|, |u_2|$ also grow), the number of hops approaches the distance, and is $\lfloor \frac{\max(|u_1|, |u_2|)}{R} \rfloor + 1$. For VCM, asymptotically, they are exactly $h+1$ hops away (see [7]). This completes the proof. ■

Corollary 2. *ORCHID with VCM coloring needs 1 cycle of length θR^2 and $4 \times \frac{4\sqrt{3}L}{3h}$ cycles of length $(h+1)$.*

Notice here that the *Highway* cycle is shorter than the *Routes* cycles. We will find the same result with simulation results in Section III.7.1.

III.6.2 Energy Consumption

The aim of this section is to evaluate the benefits of ORCHID from energy point of view. Notice that generally with radio duty cycling scheduling, the sensor node consumes energy: during its slot if it is transmitting and during the slots of its potential transmitters. For simplicity, we will

⁵http://en.wikipedia.org/wiki/Gauss_circle_problem

⁶without the optimization of Section III.5.2.

assume that such active node (during a slot), receiving or transmitting, will require an energy equal to \mathcal{E} . From results in Section III.6.1 and in Section IV.1, we can estimate the total energy consumed by all nodes in ORCHID and IRCO.

In Property 2, we have evaluated the average delay in number of slots needed to traverse one radio range which is equal to $\frac{3}{2}\theta + \frac{3}{4}\pi$. In a disk of radius LR , the farthest two nodes are of a distance $2LR$. Hence, to route between any two nodes in this disk, the total number of slots needed is: $2L(\frac{3}{2}\theta + \frac{3}{4}\pi)$, Thus, a number of cycles equal to $2L(\frac{3}{2}\theta + \frac{3}{4}\pi) \times \frac{1}{\theta R^2}$.

We assume that the density defined as the number of nodes per surface unit is equal to 1. Hence, the total number of nodes in the disk of radius LR is equal to: $\pi L^2 R^2$. Each node is awake one slot per cycle for transmission and its neighbors ($\pi R^2 - 1$ neighbors) are awake during this slot for reception. Hence, the total energy consumed in IRCO is:

$$E_{\text{IRCO}} = 2L \times \left(\frac{3}{2}\theta + \frac{3}{4}\pi\right) \times (\pi R^2) \times (\pi R^2 L^2) \times \frac{1}{\theta R^2} \times \mathcal{E} \quad (5)$$

Recall that $\theta = \frac{\sqrt{3}}{2}h^2$, hence we have:

$$E_{\text{IRCO}} = \frac{\pi^2 \sqrt{3}(\pi + h^2 \sqrt{3})R^2 L^3}{h^2} \mathcal{E} \quad (6)$$

For ORCHID, the total energy consumed is the energy consumed in the *Route* cycle plus the energy consumed in the *Highway* cycle.

The *Route* cycle contains θR^2 slots. For each slot, all nodes having the associated color (whose number is $= \frac{1}{\theta R^2} \times \pi R^2 L^2$) are awake as well as their neighbors ($\pi R^2 - 1$).

From Corollary 2, *Highway* cycles are repeated $4 \times \frac{4\sqrt{3}L}{3h}$, each of length $h+1$ slots. During each *Highway* slot, 2 nodes are active (the transmitter and its next hop). Of course, all nodes having the same color as these two nodes (with a number $= \frac{1}{\theta R^2} \times \pi R^2 L^2$) are also active, hence:

$$E_{\text{ORCHID}} = \left(\frac{1}{\theta R^2} \times \pi R^2 L^2 \times \theta R^2 \times \pi R^2 + 4 \times \frac{4\sqrt{3}L}{3h} \times (h+1) \times 2 \times \frac{1}{\theta R^2} \times \pi L^2 R^2 \right) \mathcal{E} \quad (7)$$

Hence,

$$E_{\text{ORCHID}} = (\pi^2 R^4 L^2 + \frac{32\pi\sqrt{3}(h+1)}{3h\theta} L^3) \mathcal{E} \quad (8)$$

As a result, when R is fixed and when $L \rightarrow \infty$: most of the cost from E_{ORCHID} comes from the *Highways* (right part), and the ratio between the two methods converges to $E_{\text{ORCHID}}/E_{\text{IRCO}} \rightarrow \frac{a}{R^2}$ where a is a constant, illustrating the gains of ORCHID for higher densities.

III.7 Experimental Results

In this section, we provide experimental performance evaluation of ORCHID and compare it with our baseline, IRCO with shortest-delay path routing (see also Section III.6), in terms of slot number and energy consumption. We used a topology with nodes of the grid inside the disk of radius $L = 300$. Our experiments consist in computing ORCHID and IRCO scheduling parameters (such as the number of slots of one cycle) so that: information sent by any node in this selected topology reaches the aggregator \mathcal{O} , the center of the disk⁷.

⁷they could be extended to the case where every node reaches one aggregator

III.7.1 Number of Slots of ORCHID and IRCO

III.7.1.1 How the Number of Slots is Computed?

- **ORCHID:** We first find λ , the number of *Highways* repetition. Then, the number of slots is obtained by adding this to $\lambda \times$ size of *highways* cycle + the size of the *Route* cycle (= the number of VCM colors).
- **IRCO with Shortest-Delay Path:** as in Section II.1.

III.7.1.2 Results

We run experiments with various radio ranges (1 experiment par radio range). Table 2 gives the worst case results (in terms of number of slots). Hence, this table gives an idea about the dimensioning of the global STDMA cycle.

Radio range	VCM parameters (<i>Route</i>)			IRCO	ORCHID	
	u_1	u_2	nb colors.	max nb VCM cycles	nb highway slots	max nb highway cycles
2	(4, 3)	(-3, 4)	25	62	13	60
3	(5, 7)	(-4, 8)	68	16	13	39
4	(8, 8)	(-3, 11)	112	9	13	30
5	(15, 3)	(4, 14)	198	5	13	23

Table 2: Comparison of results for sample grid networks with various radio range

From Table 2 we can deduce two results. *First*, as highlighted in Section III.6.2, in ORCHID, the number of slots produced by the repetition of the *Highways* is smaller than the number of slots in the *Route* cycle. For instance, for radio range = 4, *Route* cycle comprises 112 slots and the *Highway* cycle should be repeated 30 times and comprises 13 slots (with optimisation of Section III.5.2, otherwise 16): hence a total of 390 slots. *Second*, ORCHID has a smaller number of slots than IRCO. For instance, for radio range = 4, while ORCHID uses $390 + 112 = 502$ slots, IRCO uses $112 \times 9 = 1008$ slots.

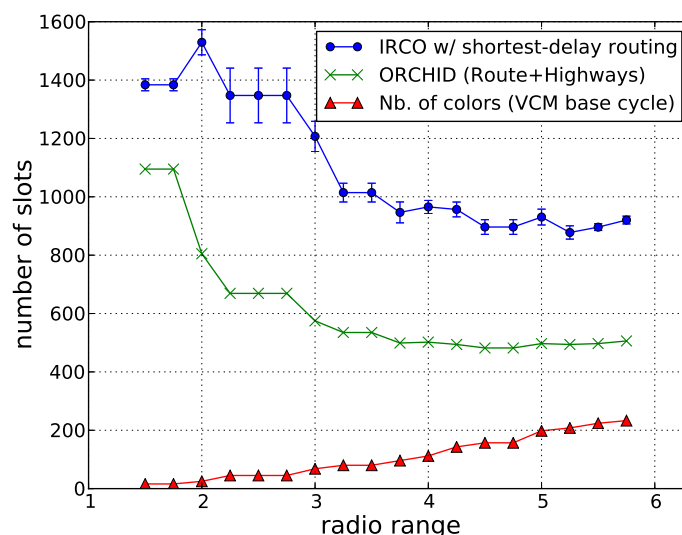


Figure 12: Total number of slots necessary for reaching center node in one cycle

Figure 12 represents graphically the same information (average of 60 simulations for IRCO): it is the total number of slots necessary to guarantee that every node of the disk of radius LR reaches the center aggregator \mathcal{O} .

As illustrated in Figure 12, ORCHID is noticeably more efficient than IRCO. This highlights the efficiency of ORCHID scheduling.

Notice also that as the radio range increases, the number of colors in the VCM cycle becomes the main part of the ORCHID scheduling. For IRCO, this part would suffice to cover a large set of the nodes which means that this cycle is repeated only a few times in these topologies. Consequently, the margin of improvement is restricted.

Note also that in actual WSN networks it is unlikely that a “all-to-all” route computation would be performed, hence another routing such as IRCO with greedy routing would be implemented, at the cost of worst performance than shortest-delay path. This means that ORCHID is suitable for WSNs.

III.7.2 Number of Reachable Aggregators in one Cycle per Node

Figure 13 illustrates the average number of aggregators that any node can reach in a single cycle. This figure shows that this number is large. For instance, for a radio range $R = 4.5$, 400 aggregators are reachable in one cycle. Consequently, a node can route through any of these aggregators in one cycle. This is useful in case of multiple sinks or mobile sink. In these scenarios, the closest aggregator to a given sink is not necessarily the best aggregator to reach another sink, or the same sink in a new location. Hence, having many possible aggregators that a node can reach rapidly (in one cycle) is a real advantage of ORCHID. This is also useful in case of link failure with a given aggregator. To conclude, ORCHID supports sink mobility and some link failures.

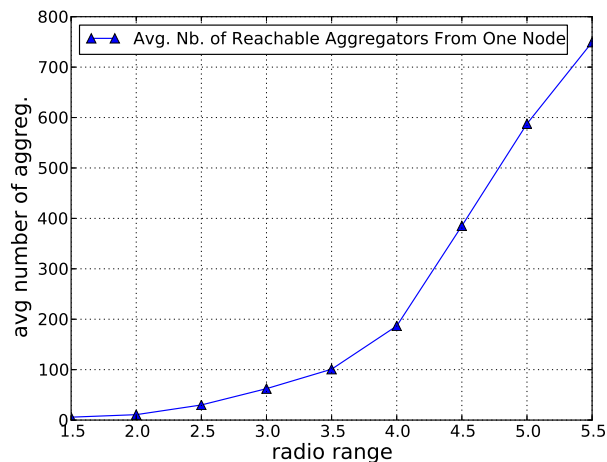


Figure 13: Average number of reachable aggregators per node.

III.7.3 Energy Consumption in ORCHID

III.7.3.1 How Energy Consumption is Computed?

The energy evaluation is computed as follows: we count 1 unit energy consumed for every slot where one node is listening to neighbors or is transmitting. In a VCM cycle (*Route cycle*), potentially every node should be listening to every neighbor (as it might be used as a relay by these neighbors). In a *Highway* cycle, in each slot, only nodes belonging to a *Highway* can be transmitting and only their predefined destination should be listening.

III.7.3.2 Results

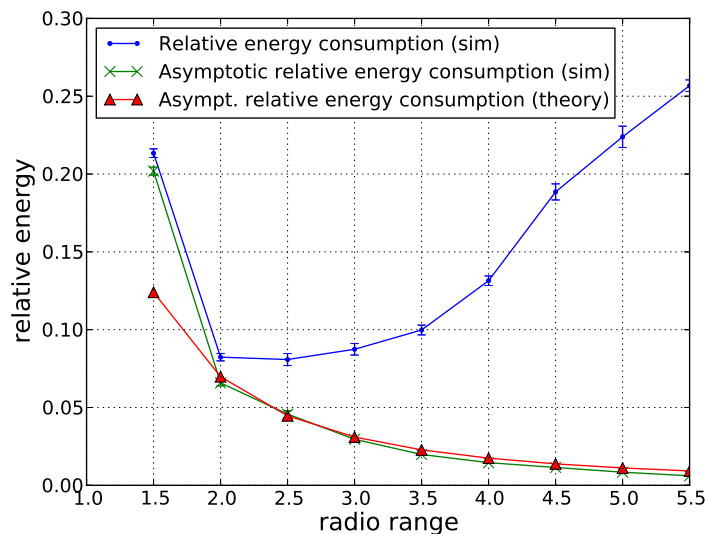


Figure 14: Ratio between energy needed by ORCHID, and by IRCO

Although ORCHID requires less cycles for guaranteeing the same routing to one aggregator, another essential feature of ORCHID is the energy-efficiency. This is illustrated by Figure 14: the energy consumption is evaluated for both methods, with schedule parameters found as in Figure 12.

In Figure 14, we show the ratio between the energy consumed with ORCHID and the energy consumed by IRCO. It is represented by the curve “Relative energy consumption”: it appears that this ratio is between 0.1 and 1/3: this means that ORCHID consumes 3 to 10 times less energy than repeating VCM, a dramatic improvement.

Moreover, as detailed previously (Section III.6.2), the asymptotic energy gain of ORCHID is even better: this is because the density of the *Highway* is much lower than the density of the network (especially when the radio range increases). Hence it is very beneficial to replace repetitions of the VCM cycles by repetitions of the *Highway* cycles. In Figure 12, we extrapolate the simulation results for estimating the ratio when the radius of the disk grows to infinity. Under this condition, in ORCHID, *Highways* must be repeated several times. Hence, the *Route* cycle would be a vanishingly low part of the energy cost compared to *Highways*. For this reason, we compare the energy consumed because of the repetition of *Highways* compared to the repetition

of E_{IRCO} . This is illustrated by the curve “Asymptotic relative energy consumption”: as shown, energy consumption in ORCHID is 2 orders of magnitude lower than VCM cycle repetition for range greater than 4.

Part IV

Estimate of Normalized Delay per Range with Random Color Ordering

In this part, we design a model for the determination of the normalized delay per range when the radio range $\rightarrow \infty$. We consider the same system model of Section I.3 and in addition, we make the assumption that the colors are randomly associated with slots.

IV.1 Model

We follow the approach commonly used in geographic routing studies, assuming that a route from a source to a destination is followed. Then the expected progress towards the destination is computed on one step at an intermediate relay node (as in [22] for instance). Greedy routing is assumed: but here, unlike geographic routing, the selected next hop is not the one that is the closest to the destination, but the one that minimizes the “normalized delay per range” (as in [18]).

Let N be this intermediate node. Without loss of generality, we assume that its coordinates are $(0, 0)$ and its color is 0. We denote n the radio range. Let \mathcal{N}_u the set of neighbors of N with x-coordinate u where u in an integer $\in [0, n]$. Let $y(u)$ be the maximum y-coordinate of these nodes. We have $y(u) = \lfloor \sqrt{n^2 - u^2} \rfloor$. Also, asymptotically, we can write:

$$y(u) = \sqrt{n^2 - u^2} + O(1) \tag{9}$$

The colors of these nodes are denoted $c_{u,-y(u)}, \dots, c_{u,1}, c_{u,2}, \dots, c_{u,y(u)}$. Figure 15 illustrates an example. For clarity reasons, we only present the colors of nodes in the quarter of the disk.

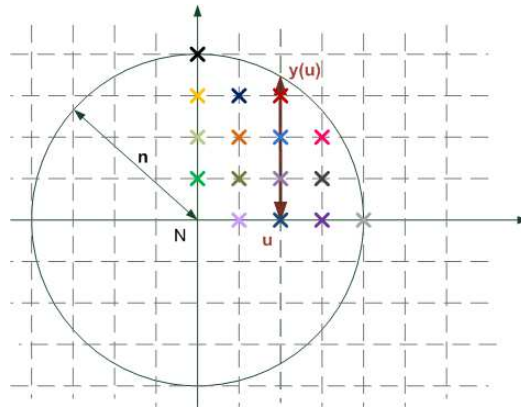


Figure 15: Figure illustrating the model used.

We assume the following approximations: 1) that the destination lies on the x-axis (towards infinity), 2) that the colors are i.i.d (independent and identically distributed) random real values (not integer) uniformly distributed in the interval $[\alpha, \beta]$. We select as next hop the neighbor with coordinates (u, i) that minimizes the “normalized delay per range”, that is the quantity $\frac{c_{u,i}}{\frac{u}{n}}$.

Let $Z \triangleq \max_{(u,i) \in \mathcal{N}_u} \frac{c_{u,i}n}{u}$ be this quantity. It is a random variable, and we show that:

Property 3. For $n \rightarrow \infty$, the distribution of Z is approximated by a shifted exponential distribution that has expected value of $\frac{\alpha\pi}{4} + \frac{3(\beta-\alpha)}{2n^2}$.

IV.2 Computation of the Normalized Delay Per Range

Let now prove Property 3. We introduce intermediate random variables:

- $Y_{u,i} \triangleq \frac{c_{u,i}n}{u}$ is the normalized delay per range when selecting neighbor (u,i) with color $c_{u,i}$. Because $c_{u,i}$ is uniformly distributed in $[\alpha, \beta]$, its cumulative distribution function is

$$F_{Y_{u,i}}(x) = Pr(Y_{u,i} \leq x) = \sigma\left(\frac{xu}{\beta - \alpha} - \alpha\right) \quad (10)$$

with σ “bounding function”, defined as follows: $\sigma(x) = x$ for $0 \leq x \leq 1$, $\sigma(x) = 0$ for $x < 0$, and $\sigma(x) = 1$ for $x > 1$.

- $X_u \triangleq \min_{i=-y(u), \dots, +y(u)} Y_{u,i}$ is the normalized delay per range when choosing about the best neighbor with x-coordinate u . Its distribution verifies:

$$F_{X_u}(x) = Pr(X_u \leq x) = 1 - (1 - Pr(Y_{u,0} \leq x))^{(2y(u)+1)} \quad (11)$$

- The random variable Z can also be expressed as a minimum of minima: $Z = \min_{u \in \{0,1,\dots,n\}} X_u$.

Using Equations (10) and (11), it is possible to express its cumulative distribution $F_Z(x)$ as:

$$F_Z(x) = Pr(Z \leq x) = 1 - \prod_i (1 - Pr(X_u \leq x)) \quad (12)$$

$$\log(1 - F_Z(x)) = \sum_{u=1}^n (2y(u) + 1) \log\left(1 - \sigma\left(\frac{xu}{\beta - \alpha} - \alpha\right)\right) \quad (13)$$

If we approximate $\log(1 - a) \approx -a$ for a near 0 (also an upper bound in all cases), and if we ignore bounding effects of σ , we can approximate Z by \tilde{Z} , with a close distribution function (when $n \rightarrow \infty$):

$$\log(1 - F_{\tilde{Z}}(x)) = - \sum_{u=1}^n (2y(u) + 1) \frac{xu}{\beta - \alpha} \quad (14)$$

Using Equation 9, we have:

$$\log(1 - F_{\tilde{Z}}(x)) = - \frac{2}{\beta - \alpha} \left[\frac{x}{n} \sum_{u=1}^n u \sqrt{n^2 - u^2} - \alpha \sum_{u=1}^n \sqrt{n^2 - u^2} + O(n) \right] \quad (15)$$

Now we introduce the following approximation:

$$\sum_{u=0}^n \sqrt{n^2 - u^2} = \frac{\pi n^2}{4} + O(n)$$

$$\sum_{u=0}^n u \sqrt{n^2 - u^2} = \frac{n^3}{3} + O(n^2)$$

Indeed, these are sum of (bounded) functions, respectively: non-decreasing and with one unique maximum (increasing then decreasing). It is easy to bound them with integrals:

$$\begin{aligned} \left(\int_0^n \sqrt{n^2 - u^2} \, du\right) - n &\leq \sum_{u=0}^n \sqrt{n^2 - u^2} \leq \left(\int_0^n \sqrt{n^2 - u^2} \, du\right) + n \quad (\text{since } \sqrt{n^2 - u^2} \leq n) \\ \left(\int_0^n u\sqrt{n^2 - u^2} \, du\right) - 3n^2 &\leq \sum_{u=0}^n u\sqrt{n^2 - u^2} \leq \left(\int_0^n u\sqrt{n^2 - u^2} \, du\right) + 3n^2 \quad (\text{since } u\sqrt{n^2 - u^2} \leq n^2) \end{aligned}$$

Hence,

$$\log(1 - F_{\tilde{Z}}(x)) = -\frac{2n^2}{\beta - \alpha} \left(\frac{x}{3} - \alpha \frac{\pi}{4} + O\left(\frac{1}{n}\right) \right) \quad (\text{bounding sums by integrals}) \quad (16)$$

Further ignoring the $O(\frac{1}{n})$, we approximate \tilde{Z} by \hat{Z} with:

$$Pr(\hat{Z} \leq x) = 1 - \exp\left[-\frac{2n^2}{3(\beta - \alpha)}\left(x - \frac{3\alpha\pi}{4}\right)\right] \quad (17)$$

Equation 17 meaningful for $x \geq \frac{3\alpha\pi}{4}$. Equation 17, corresponds to a variable $\hat{Z} - \frac{3\alpha\pi}{4}$ exponentially distributed with parameter $\lambda = \frac{2n^2}{3(\beta - \alpha)}$. Therefore, the expectation of \hat{Z} , $E(\hat{Z})$ is:

$$E(\hat{Z}) = \frac{3\alpha\pi}{4} + \frac{1}{\lambda} = \frac{\alpha\pi}{4} + \frac{3(\beta - \alpha)}{2n^2} \quad (18)$$

\hat{Z} is an approximation of Z and hence Property 3.

IV.3 Experimental Validation of Asymptotic Expression for Normalized Delay per Range

In the previous section, an asymptotic expression was obtained for the normalized delay per range. The goal of the present section is compare this asymptotic expression with actual results obtained by simulations of the exact modeled system: this experimentally validates the convergence to the asymptotic value, and also some of the approximations that were made.

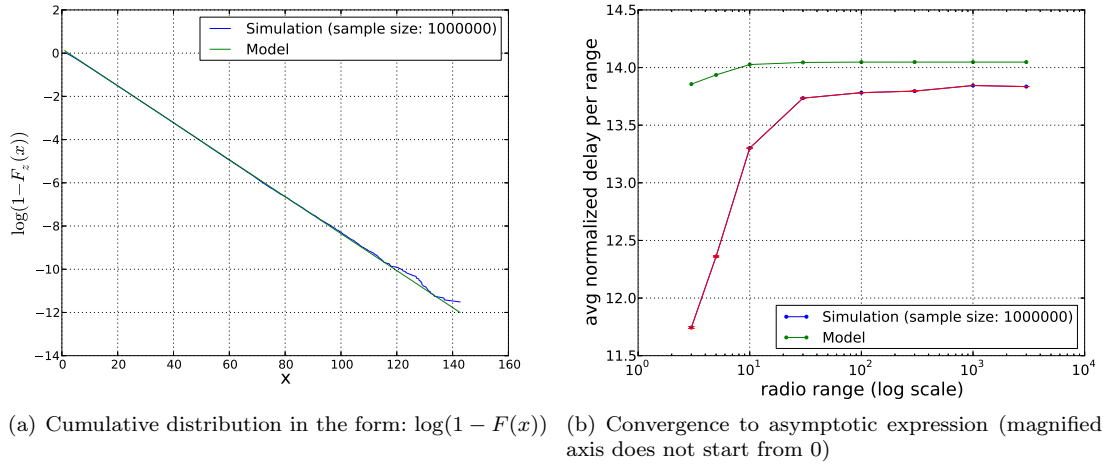


Figure 16: Experimental Validation of Model.

Figure 16(a) represents the results of computing the average value of normalized delay per range for a radio range of 1000 (from one million random draws). The number of colors is equal to $\lfloor \theta n^2 \rfloor$ where $\theta = \frac{\sqrt{3}}{2} h^2$; this is asymptotic number of colors of VCM for a 3-hop coloring, see [7]. As shown in the figure, parameters of the exponential distribution match very closely the actual cumulative distribution.

The complementary Figure 16(b) represents the convergence of the simulation results to the asymptotic expression. The chosen y-axis scale enhances the differences, but for a radio range equal to 3000 we witness a difference of only 1.5% between model and simulations.

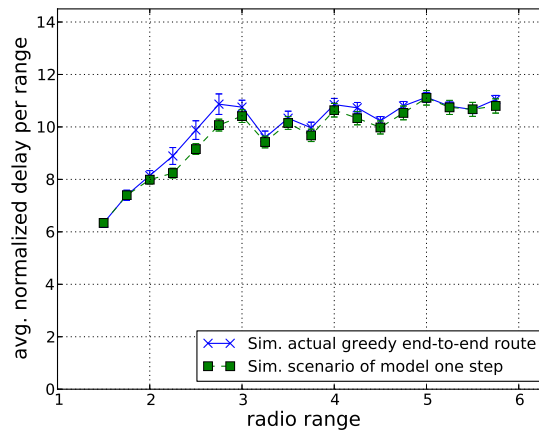


Figure 17: Normalized delay in greedy routing and model.

Figure 17 illustrates the average normalized delay per range computed by exactly simulating the scenario of the model, and also the simulations of Section III.7 with greedy routing.

1. “Sim. scenario of model one-step” represents the simulation of the scenario described in the model for one step (see Section IV.1). Colors are assimilated to real random numbers.
2. “Sim. actual greedy end-to-end route” represents the simulation of the greedy end-to-end route. Recall that colors are randomly ordered in the cycle and that this greedy routing uses the same heuristic as the model (minimize ratio delay per progress). We determine the end-to-end greedy path and the corresponding end-to-end delay. The average normalized delay per range is then obtained by dividing this end-to-end delay by the number of hops of the path according to the definition.

The observed normalized delay is similar for both methods. This result validates the method and scenario adopted by our model to compute the average normalized delay per range.

Part V

Conclusion

This report addressed the delay optimization issue in STDMA systems. In particular, we focused on delays induced by the misordering of time slots which is less often treated in literature. To deal with this problem, we first started by evaluating the delays when the slots are randomly ordered in the cycle. Hence, we evaluated the normalized delay per range with simulation and with a stochastic model. Then, we proposed a solution called ORCHID that joins routing and scheduling.

The challenge was to build routes and then to order nodes transmissions according to their appearance order in these routes. This ensures that data are delivered in a single STDMA cycle, which is very delay efficient. We performed analytical and experimental performance analysis of ORCHID. Results show that ORCHID uses smaller number of slots and is more energy efficient than shortest-delay path routing.

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Part VI

Annex

In this annex, we provide figures illustrating some results.

Figure 18 illustrates a dominating tree built by ORCHID for $R = 2$.

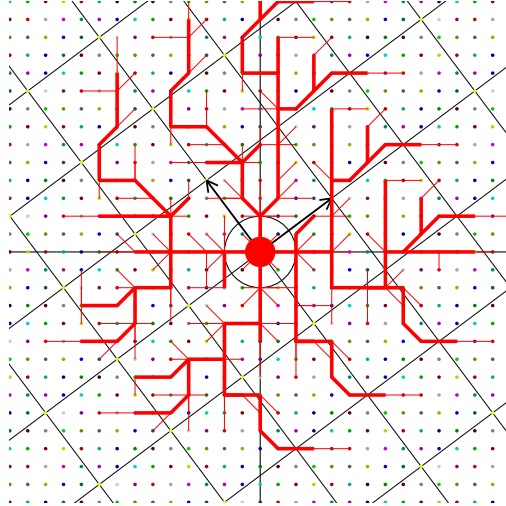


Figure 18: The dominating tree generated by ORCHID for $R=2$.

Figure 19 illustrates two dominating trees rooted at $(0,0)$ and $(4,3)$ for $R = 2$.

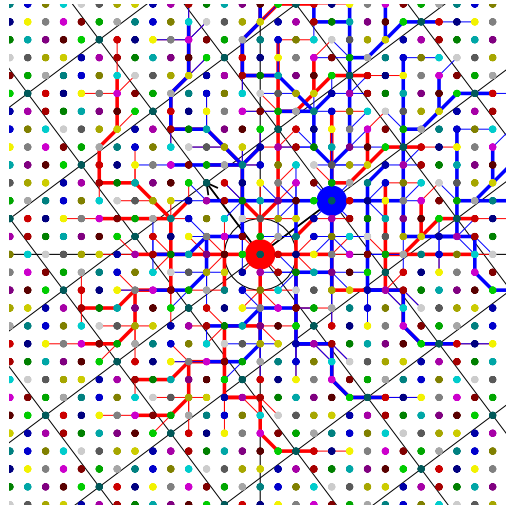


Figure 19: Two dominating trees generated by ORCHID for $R=2$.

Figure 20 illustrates the highways built by ORCHID for $R = 2$ and $R = 3$.

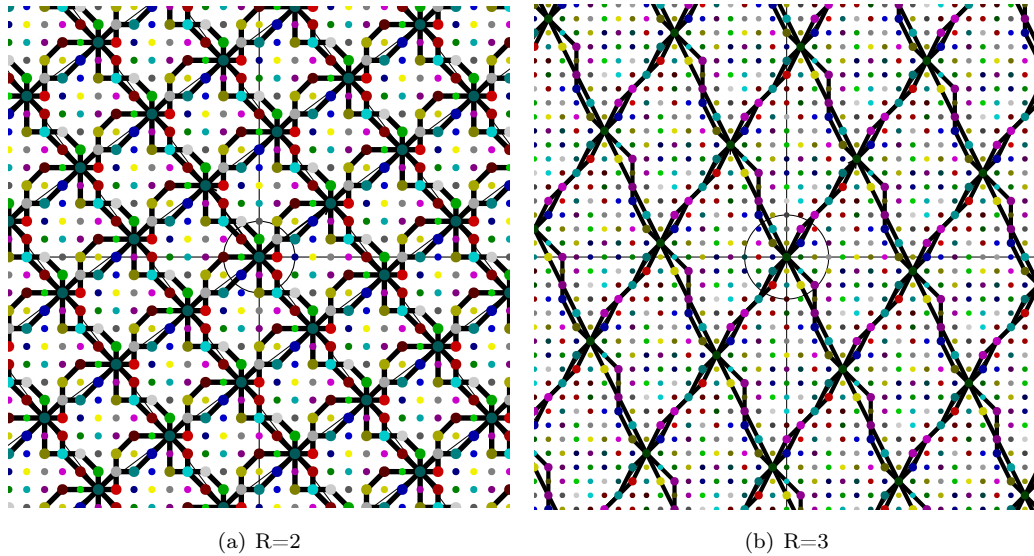
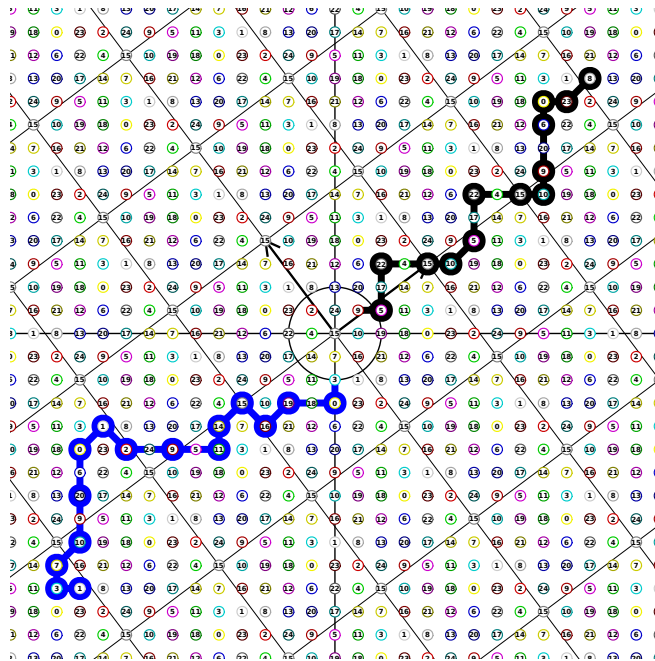
(a) $R=2$ (b) $R=3$

Figure 20: Highways built by ORCHID.

Figure 21 illustrates the shortest-delay path between the node $(11, 11)$ and the sink at $(0, 0)$, and also the greedy routing path from $(-11, -11)$ to the same sink. The numbers in the circles indicate the color of each node.

Figure 21: Examples of paths computed for $R = 2$.



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